

DATA SHEET SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

High-Voltage

NP0/X7R 500 V TO 3 KV 0.47 pF to 33 nF

RoHS compliant & Halogen Free



YAGEO

20

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

<u>SCOPE</u>

This specification describes High-Voltage NP0/X7R series chip capacitors with lead-free terminations.

APPLICATIONS

PCs, Hard disk, Game PCs Power supplies LCD panel ADSL, Modem

FEATURES

RoHS compliant Halogen Free compliant MSL class MSL I Soldering is compliant with J-STD-020D

ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value. **YAGEO BRAND ordering code**

GLOBAL PART NUMBER (PREFERRED)

CC <u>XXXX</u> <u>X</u> <u>X</u> <u>XXX</u> <u>X</u> <u>X</u> <u>X</u> <u>XXX</u> (1) (2) (3) (4) (5) (6) (7) (8)

(I) SIZE – INCH BASED (METRIC)

0805 (2012) / 1206 (3216) / 1210 (3225) / 1808 (4520) / 1812 (4532) / 2220 (5750)

(2) TOLERANCE

 $B = \pm 0.1 \text{ pF}$ $C = \pm 0.25 \text{ pF}$ $D = \pm 0.5 \text{ pF}$ $G = \pm 2\%$ $J = \pm 5\%$ $K = \pm 10\%$ $M = \pm 20\%$

(3) PACKING STYLE

R = Paper/PE taping reel; Reel 7 inch

- K = Blister taping reel; Reel 7 inch
- P = Paper/PE taping reel; Reel 13 inch
- F = Blister taping reel; Reel 13 inch

(4) TC MATERIAL

NPO = NPOX7R = X7R

(5) RATED VOLTAGE

B = 500V	Floating design:
Z = 630V	C = IKV
	D = 2 KV
	S = 2.5KV
	E = 3 KV

(6) CONTROL CODE

A: Anti-arc coating

B: Standard Type

(7) PROCESS

N = NP0

B = X7R

(8) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $|2| = |2 \times |0| = |20 \text{ pF}$

Barrier Layer (Ni)

Termination Finis (100% Matte Sn End Termination/ External Electode (Cu)

> Inner Electrodes (Ni)

Dielectric Material

Fig. I Surface mounted multilayer ceramic capacitor construction

Dielectric Material

CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

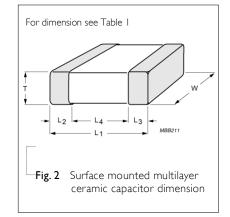
The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. 1.



Table I For outlines see fig.2 L₂ / L₃ (mm) L_4 (mm) TYPE L_I (mm) W (mm) T (MM) min. min. max. 2.0 ±0.10 1.25 ±0.10 0.60 ±0.10 0805 0.25 0.75 0.70 0.85 ±0.10 2.0 ±0.20 1.25 ±0.20 1.25 ±0.20 3.2 ±0.15 1.60 ±0.15 0.60 ±0.10 0.85 ±0.10 1206 0.25 0.75 1.40 3.2 ±0.30 1.60 ±0.20 1.25 ±0.20 1.60 ±0.20 3.2 ±0.20 2.50 ±0.20 0.85 ± 0.10 1.25 ±0.20 1210 0.25 0.75 1.40 3.2 ±0.30 2.50 ±0.20 1.60 ±0.20 2.00 ± 0.20 1.25 ±0.20 1.35 ±0.15 1808 0.25 0.75 4.5 ±0.40 2.00 ±0.30 2.20 1.60 ±0.20 2.00 ±0.20 0.85 ±0.10 1.25 ±0.20 1812 0.25 0.75 4.5 ±0.40 3.20 ±0.20 2.20 1.35 ±0.15 1.60 ±0.20 2.00 ±0.20 2220 5.7 ±0.40 5.0 ±0.3. 2.00 ±0.20 0.25 0.75 3.40

OUTLINES

Inner Electrodes (Ni)



Detailed Cross Section

End Termination/ External Electode (Cu) Barr

Barrier Laye (Ni)

> Termination Finish (100% Matte Sn)

<mark>4</mark> 20 Product specification

Surface-Mount Ceramic Multilayer Capacitors NP0/X7R 500 V to 3 KV High-Voltage

Table 2 Sizes from 0805 to 1210 CAP. 0805 1206 1210 500 V 630 V I KV 500 V 630 V ΙΚ٧ 2 KV 3 KV 500 V 630 V I KV 2 KV 1.0 pF I.2 pF 1.5 pF 1.8 pF 2.2 pF 2.7 pF 0.85±0.1 3.3 pF 3.9 pF 4.7 pF 5.6 pF 6.8 pF 8.2 pF 0.85±0.1 10 pF 1.25±0.2 12 pF 0.6±0.1 0.6±0.1 15 pF 18 pF 0.85±0.1 1.25±0.2 22 pF 27 pF 33 pF 39 pF 0.6±0.1 1.25±0.2 1.25±0.2 47 pF 1.25±0.2 56 pF 68 pF 1.25 ± 0.2 1.25±0.2 1.25±0.2 1.25±0.2 82 pF 1.25±0.2 100 pF 120 pF 150 pF 180 pF

CAPACITANCE RANGE & THICKNESS FOR NPO

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-12 series is on request



CAPACITANCE RANGE & THICKNESS FOR NPO

Sizes fr	rom 0805 t	:0 1210 (0	continued)								
0805			1206					1210			
500 V	630 V	I KV	500 V	630 V	I KV	2 KV	3 KV	500 V	630 V	ΙΚΫ	2 KV
						1.25±0.2					1.25±0.2
0.85±0.1	0.85±0.1										
			0.6±0.1								
					1.25±0.2				1.25±0.2	1.25±0.2	
								105100			
								1.25±0.2			
1.25±0.2	1.25±0.2										
				125102							
			0.85±0.1	1.25±0.2							
			1.25±0.2								
			1.6±0.2	1.6±0.2							
	0805 500 ∨ 0.85±0.1	0805	0805 500 V 630 V I KV 0.85±0.1 0.85±0.1	1206 500 V 630 V 1 KV 500 V 630 V 1 KV 500 V 0.85±0.1 2 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	500 V 630 V I KV 500 V 630 V 0.85±0.1 0.85±0.1 0.6±0.1 0.6±0.1 0.6±0.1 1.25±0.2 1.25±0.2 0.85±0.1 1.25±0.2	0805 1206 500 V 630 V 1 KV 0.85±0.1 1 KV 0.85±0.1 0.85±0.1 1.25±0.2 0.6±0.1 1.25±0.2 0.6±0.1 1.25±0.2 0.6±0.1 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2	0805 1206 500 V 630 V 1 KV 500 V 630 V 1 KV 2 KV 0.85±0.1 -	0805 1206 500 630 1 KV 500 630 1 KV 2 KV 3 KV 630 1 KV 2 KV 3 KV 1 L25±02 1 L25±02	1206 500 × 630 × 1 KV 500 × 630 × 1 KV 2 KV 3 KV 500 ×	1206 1206 1210 1210 1210 500 V 630 V 1KV 2KV 3KV 500 V 630 V 0.85±0.1 6.00 0.6±0.1 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2 1.25±0.2	0805 1206 1210 500 V 630 V 1KV 2KV 3 KV 500 V 630 V 1 KV 0.85 ±0.1 0.85 ±0.1 0.85 ±0.1 0.64 ±0.1 1.25 ±0.2 1.25

NOTE

I. Values in shaded cells indicate thickness class in mm

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Table 3 Sizes from 0805 to 1210 (continued)

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Surface-Mount Ceramic Multilayer Capacitors NP0/X7R 500 V to 3 KV High-Voltage

				ESS FOR NPC	<u>)</u>				
	Sizes from 18		12		1012				
CAP.		808 КV	2 KV	3 KV	1812 500 V	630 V	I KV	2 KV	3 KV
0.47			210	51(1	500 1	050 1		2 101	5100
0.56									
0.68									
0.82									
	pF								
	pF								
	pF								
	pF								
2.2									
2.7									
3.3									
3.9	pF								
4.7									
5.6	рF								
6.8	рF								
8.2	рF								
10	рF								
12	рF								
15	рF								
18	рF								
22	рF								
27	рF								
33	pF			1.6±0.2					
39	pF								105.00
47	pF						1.25±0.2	1.25±0.2	1.25±0.2
56	pF								
68	pF	0.2							
82	pF	:0.2	1.25±0.2						
100	рF				1.25±0.2	1.25±0.2			
120	pF								
150									
180	рF								

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ΝΟΤΕ

- I. Values in shaded cells indicate thickness class in mm
- 2. Capacitance value of non E-12 series is on request



Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

CAPACITANCE RANGE & THICKNESS FOR NPO

Table 5	Sizes from 1808	3 to 1812 (con	tinued)					
CAP.			1812					
	I KV	2 KV	3 KV	500 V	630 V	I KV	2 KV	3 KV
220 pF								I.25±0.2
270 pF								
330 pF	105.00							
390 pF	1.25±0.2	1.25±0.2						
470 pF							1.25±0.2	
560 pF				1.25±0.2	I.25±0.2	1.25±0.2		
680 pF								
820 pF								
I.0 nF								
I.2 nF								
I.5 nF								

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-12 series is on request



2 KV

I KV

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

Table 6 Sizes from 0805 to 1210 0805 1206 1210 CAP. 500 V 630 V ΙΚ٧ 500 V 630 V ΙΚ٧ 2 KV 2.5KV 500 V 630 V 100 pF 150 pF

CAPACITANCE RANGE & THICKNESS FOR X7R

220 pF												
330 pF			0.85±0.1									
470 pF												I.25±0.2
680 pF							1.25±0.2					
I.0 nF	0.85±0.1	0.85±0.1						1.6±0.2				
I.5 nF					1.25±0.2	1.25±0.2					1.25±0.2	
2.2 nF			1.25±0.2	I.25±0.2	1.25±0.2						1.25±0.2	1.60±0.2
3.3 nF												
4.7 nF												
6.8 nF										1.25±0.2		
10 nF		1.25±0.2							1.25±0.2			
I5 nF	1.25±0.2								1.25±0.2			
22 nF					1.60±0.2					1.6±0.2	1.6±0.2	
33 nF				1.60±0.2	1.60±0.2					2.0±0.2		
47 nF										2.0±0.2		
68 nF									2.0±0.2			
100 nF									2.0±0.2			

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-6 series is on request

3. For products with 5% tolerance, please contact local sales force before ordering

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Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

CAPACITANCE RANGE & THICKNESS FOR X7R

Table 7 S)8 to 1812							
CAP.	1808 1 KV	2 KV	3 KV	1812 500 ∨	630 V	I KV	2 KV	3 KV	2020 630 ∨
100 -5		2 KV	3 KV	500 V	830 V		2 KV	3 KV	630 V
100 pF									
150 pF		_							
220 pF									
330 pF			1.6±0.2						
470 pF									
680 pF		1.35±0.15							
I.0 nF			2.0±0.2					1.6±0.2	
I.5 nF	1.35±0.15								
2.2 nF		1.6±0.2					1.35±0.15		
3.3 nF									
4.7 nF					1.35±0.2	1.35±0.15			
6.8 nF	I.6±0.2			1.25±0.2		_			
10 nF	1.0±0.2			1,20±0,2					
15 nF									
22 nF						_			
33 nF						1.6±0.2			
47 nF					1.6±0.2				
68 nF									
100 nF				1.6±0.2	2.0±0.2				
150 nF									
220 nF									2.0±0.2
330 nF									
470 nF									
680 nF									
ΙµF									

ΝΟΤΕ

- I. Values in shaded cells indicate thickness class in mm
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Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

ELECTRICAL CHARACTERISTICS

NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise stated all electrical values apply at an ambient temperature of 20 ± 1 °C, an atmospheric pressure of 86 to 106 kPa, and a relative humidity of 63 to 67%.

Table	8	
DESCRIP	PTION	VALUE
Capacitar	nce range	0.47 pF to 33 nF
Capacita	nce tolerance	
NP0	C < 10 pF	±0.25 pF, ±0.5 pF
	C ≥ 10 pF	±2%, ±5%
X7R		±5% ⁽¹⁾ , ±10%
Dissipatio	on factor (D.F.)	
NP0	C < 30 pF	≤ I / (400 + 20C)
	C ≥ 30 pF	≤ 0.1 %
X7R		≤ 2.5 %
Insulation	n resistance after 1 minute at U _r (DC)	$\text{R}_{\text{ins}} \geq$ 10 GQ or $\text{R}_{\text{ins}} \times$ C \geq 500 seconds whichever is less
msulation	$\frac{1}{2} = \frac{1}{2} = \frac{1}$	$R_{ins} \times C \ge 100 \ \Omega.F^{(2)}$
	n capacitance change as a function of temperature ture characteristic/coefficient):	
NP0		±30 ppm/°C
X7R		±15%
Operatin	g temperature range:	
NP0/X7	7R	–55 °C to +125 °C

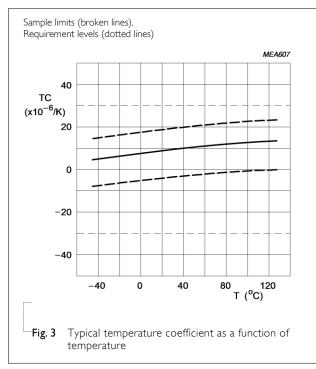
NOTE

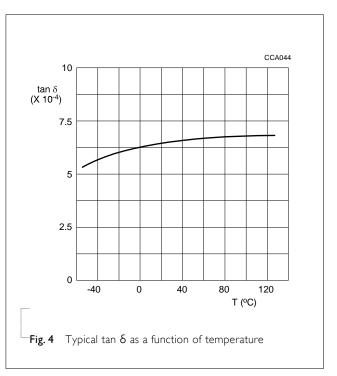
1. ±5% tolerance of capacitance value isn't available for X7R full product range, please contact local sales force before ordering

2. X7R/0805/≥3.9nF X7R/1206/≥12nF X7R/1210/≥12nF X7R/1808/≥18nF X7R/1812/≥27nF X7R/2220/≥150nF



HIGH-VOLTAGE NP0

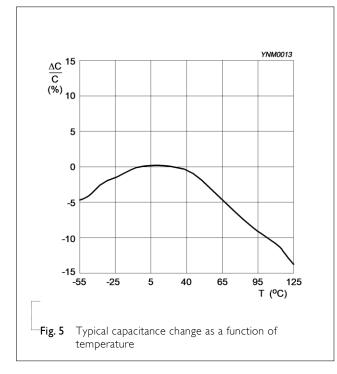


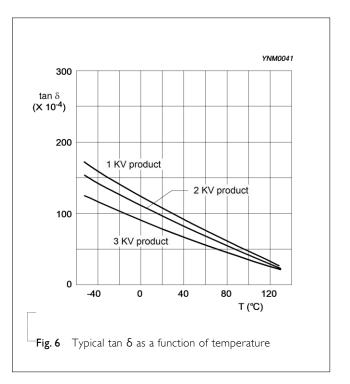


Product specification

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HIGH-VOLTAGE X7R





Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

SOLDERING RECOMMENDATION

Table 9	
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SOLDERING METHOD	SIZE 0402	0603	0805	1206	≥ 1210
Reflow	Reflow only	≥ 1.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave		< 1.0 µF	< 2.2 µF	< 4.7 µF	

TESTS AND REQUIREMENTS

Table 10 Test procedures and requirements

TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS		
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage		
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification		
Capacitance		4.5.1	NP0: $f = 1$ MHz for C ≤ 1 nF, measuring at voltage 1 V _{rms} at 20 °C f = 1 KHz for C > 1 nF, measuring at voltage 1 V _{rms} at 20 °C X7R: $f = 1$ KHz for C ≤ 10 µF, measuring at voltage 1 V _{rms} at 20 °C	Within specified tolerance		
Dissipation Factor (D.F.)		4.5.2	NP0: $f = 1$ MHz for C ≤ 1 nF, measuring at voltage 1 V _{rms} at 20 °C $f = 1$ KHz for C ≥ 1 nF, measuring at voltage 1 V _{rms} at 20 °C X7R: $f = 1$ KHz for C ≤ 10 µF, measuring at voltage 1 V _{rms} at 20 °C	In accordance with specification		
Insulation Resistance		4.5.3	$U_r \ge 500 \text{ V: At } 500 \text{ V} \text{ for I minute}$	In accordance with specification		

TEST	TEST METH	IOD	PROCEDURE	REQUIREMENTS			
Temperature Coefficient		4.6	Capacitance shall be measured by the steps shown in the following table. The capacitance change should be measured after 5 min at each specified temperature stage.	NP0: Δ C/C: ±30ppm X7R:			
			StepTemperature(°C)a 25 ± 2 bLower temperature $\pm 3^{\circ}$ Cc 25 ± 2 dUpper Temperature $\pm 2^{\circ}$ Ce 25 ± 2 (1) NP0:Temperature Coefficient shall be calculated from the formula as belowTemp, Coefficient = $\frac{C2 - CI}{CI \times \Delta T} \times 10^{6}$ [ppm/°C]C1: Capacitance at step cC2: Capacitance at 125°C ΔT : 100°C (=125°C -25°C)	Δ C/C: ±15%			
			(2) X7R: Capacitance Change shall be calculated from the formula as below $\Delta C = \frac{C2 - C1}{C1} \times 100\%$ C1: Capacitance at step c C2: Capacitance at step b or d				
Adhesion	IEC 60384- 21/22	4.7	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N			
Bending Strength		4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3 Conditions: bending 1 mm at a rate of 1 mm/s, radius jig 5 mm	No visible damage $\Delta C/C$ NP0: within $\pm 1\%$ or 0.5 pF, whichever is greater X7R: $\pm 10\%$			

TEST	TEST METI	HOD	PROCEDURE	REQUIREMENTS
Resistance to Soldering Heat		4.9	Precondition: $ 50 + 0/-10 \degree C$ for 1 hour, then keep for 24 ± 1 hours at room temperature Preheating: for size ≤ 1206 : $120 \degree C$ to $150 \degree C$ for 1 minute Preheating: for size >1206 : $100 \degree C$ to $120 \degree C$ for 1 minute and $170 \degree C$ to $200 \degree C$ for 1 minute Solder bath temperature: $260 \pm 5 \degree C$ Dipping time: 10 ± 0.5 seconds Recovery time: 24 ± 2 hours	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned $\Delta C/C$ NP0: within ±0.5% or 0.5 pF, whichever is greated X7R: ±10% D.F. within initial specified value R _{ins} within initial specified value
Solderability		4.10	Preheated to a temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination
			 Temperature: 235±5°C / Dipping time: 2 ±0.5 s Temperature: 245±5°C / Dipping time: 3 ±0.5 s (lead free)Depth of immersion: 10mm 	
Rapid Change	IEC 60384-	4.11	Preconditioning;	No visual damage
	21/22		0 +0/-10 °C for 1 hour, then keep for ±1 hours at room temperature ycles with following detail: minutes at lower category temperature minutes at upper category temperature	Δ C/C NP0: within ±1% or 1 pF, whichever is greater X7R: ±15%
			Recovery time 24 ±2 hours	D.F. meet initial specified value R _{ins} meet initial specified value
Damp Heat		4.13	 Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 	No visual damage after recovery
			 24 ± I hour at room temp 2. Initial measure: Spec: refer to initial spec C, D, IR 3. Damp heat test: 500 ± I 2 hours at 40 ± 2 °C; 90 to 95% R.H. 4. Recovery: NP0: 6 to 24 hours X7R: 24 ± 2 hours 5. Final measure: C, D, IR P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned according to <i>"IEC 60384 4.1"</i> and then the requirement shall be met. 	$\begin{array}{l} \Delta C/C \\ \mbox{NP0: within $\pm 2\%$ or 1 pF, whichever is greater} \\ \mbox{X7R: $\pm 15\%$} \\ \mbox{D.F.} \\ \mbox{NP0: ≤ 2 x specified value} \\ \mbox{X7R: ≥ 25 V: $\leq 5\%$} \\ \mbox{R}_{ins} \\ \mbox{NP0: $\geq 2,500$ MΩ or R_{ins} \times C_r $\geq 25s$ whichever is} \\ \mbox{less} \\ \mbox{X7R: ≥ 500 MΩ or R_{ins} $\times C_r $\geq 25s$ whichever is} \\ \mbox{less} \end{array}$

TEST	TEST METHOD PROCEDURE					REQUIREMENTS			
Endurance	IEC 60384- 21/22	4.14	24 ±1 hour 2. Initial measu	°C /1 hour, t at room tem ure: to initial spec	hen keep for P	No visual damage $\Delta C/C$ NP0: within ±2% or 1 pF, whichever is greater X7R: ±15% D.F.			
			Temperatur Specified str	re: NP0/X7R: ress voltage ap	125 °C oplied for 1,000 hours. vs the stress conditions	NP0: $\leq 2 \times \text{specified value}$ X7R: $\geq 25 \vee \leq 5\%$ R _{ins} NP0: $\geq 4,000 \text{ M}\Omega \text{ or}$			
			Voltage	NPO	X7R	$R_{ins} \times C_r \ge 40s$ whichever is less			
			500/630V	I.3 x Ur	1.2 × Ur	X7R: ≥ 1,000 MΩ or $R_{ins} \times C_r \ge 50s$ whichever is less			
			\geq KV	I.2 x Ur	I.I x Ur	$V_{ins} \times C_r \ge 303$ which level is less			
			3. Recovery ti	me: 24 ±2 ho	urs				
			4. Final measu	re: C, D, IR					
			minimum valu measurements	e permitted, t s have been m oned accordin	is less than the hen after the other hade the capacitor shall g to <i>"IEC 60384 4.1"</i> hall be met.				
Voltage Proof			Specified stres Ur = 500 V se (1.3 Ur + 100 Ur > 500 V: 1 Ur ≥ 1KV: 1.2 Charge/Discha	eries applied) .3 Ur Ur	ied for 1~5 seconds ess than 50mA	No breakdown or flashover			

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 500 V to 3 KV

THICKNESS CLASSES AND PACKING QUANTITY

Table II

		PACKING			ØI	80 MM / 7 INCH		0 MM / 3 INCH	QUANTITY
SIZE CODE	THICKNESS CLASSIFICATION	7 INCH		TAPE WIDTH - QUANTITY PER REEL	Paper	Blister	Paper	Blister	PER BULK CAS
	0.6 ±0.1 mm	R	Р	8 mm	4,000		20,000		10,000
0005	0.85 ±0.1 mm	R	Р	8 mm	4,000		15,000		8,000
0805	1.00 ±0.1 mm	К	F	8 mm		3,000		10,000	
	1.25 ±0.2 mm	К	F	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	R	Р	8 mm	4,000		20,000		
	0.85 ±0.1 mm	R	Р	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	К	F	8 mm		3,000		10,000	
	1.25 ±0.2 mm	К	F	8 mm		3,000		10,000	
	1.6 ±0.2 mm	К	F	8 mm		2,000		8,000	
	0.85 ±0.1 mm	К	F	8 mm		4,000		10,000	
	1.15 ±0.1 mm	К	F	8 mm		3,000		10,000	
	1.15 ±0.15 mm	К	F	8 mm		3,000		10,000	
	1.25 ±0.2 mm	К		8 mm		3,000			
1210	1.5 ±0.1 mm	К		8 mm		2,000			
	1.6±0.2 mm	К		8 mm		2,000			
	2.0 ±0.2 mm	K		8 mm		2,000 1,000			
	2.5 ±0.2 mm	К		8 mm		1,000 ا 500			
	1.15 ±0.15 mm	К		l2 mm		3,000			
	1.25 ±0.2 mm	К		l2 mm		3,000			
1808	1.35 ±0.15 mm	К		l2 mm		2,000			
1000	1.5 ±0.1 mm	К		l2 mm		2,000			
	1.6 ±0.2 mm	К		l2 mm		2,000			
	2.0 ±0.2 mm	К		l2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	К		l2 mm		2,000			
	1.15 ±0.1 mm	К		l2 mm		1,000			
	1.15 ±0.15 mm	К		l2 mm		1,000			
	1.25 ±0.2 mm	К		l2 mm		I ,000			
1812	1.35 ±0.15 mm	К		l2 mm		I ,000			
	1.5 ±0.1 mm	К		l2 mm		I ,000			
	1.6 ±0.2 mm	К		l2 mm		1,000			
	2.0 ±0.2 mm	К		l2 mm		1,000			
	2.5 ±0.2 mm	К		l2 mm		500			
2220	2.0 ±0.2 mm	К		12 mm		1000			

PAPER/PE TAPE SPECIFICATION

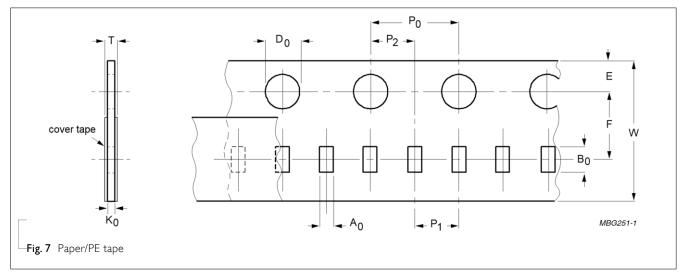


Table 12 Dimensions of paper/PE tape for relevant chip size; see Fig.3

SIZE	SYMBO	L.									Unit: mm
CODE	A ₀	B ₀	W	E	F	$P_0^{(1)}$	Ρ _Ι	P ₂	ØD ₀	K ₀	Т
0201	0.39 ± 0.06	0.70 ± 0.06	8.0 ± 0.20	1.75 ± 0.1	3.50 ± 0.05	4.0 ± 0.05	2.0 ± 0.05	2.0 ± 0.05	1.55 ± 0.03	0.38 ± 0.05	(0.47 / 0.55)±0.10
0402	0.70 ± 0.15	1.21 ± 0.12	8.0 ± 0.20	1.75 ± 0.1	3.50 ± 0.05	4.0 ± 0.05	2.0 ± 0.05	2.0 ± 0.05	1.50 +0.1 /-0	(0.75 / 0.60)±0.10	(0.85 / 0.70)±0.10
0603	1.05 ± 0.14	1.86 ± 0.13	8.0 ± 0.20	1.75 ± 0.1	3.50 ± 0.05	4.0 ± 0.10	4.0 ± 0.10	2.0 ± 0.05	1.50 +0.1 /-0	(1.05 / 0.95 / 0.75)±0.10	(1.15 / 1.05 / 0.85)±0.10
0805	1.50 ± 0.15	2.26 ± 0.20	8.0 ± 0.20	1.75 ± 0.1	3.50 ± 0.05	4.0 ± 0.10	4.0 ± 0.10	2.0 ± 0.05	1.50 +0.1 /-0	(1.05 / 0.95 / 0.75)±0.10	(1.15 / 1.05 / 0.85)±0.10
1206	1.90 ± 0.15	3.50 ± 0.20	8.0 ± 0.20	1.75 ± 0.1	3.50 ± 0.05	4.0 ± 0.10	4.0 ± 0.10	2.0 ± 0.05	1.50 +0.1 /-0	(0.95 / 0.75)±0.10	(1.05 / 0.85)± 0.10

NOTE

I. P_0 pitch tolerance over any 10 pitches is ± 0.2 mm

BLISTER TAPE SPECIFICATION

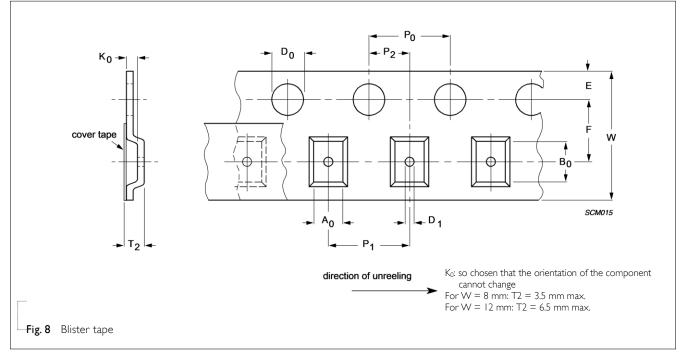


Table 13 Dimensions of blister tape for relevant chip size; see Fig.8

	SYM	SYMBOL Unit: n									iit: mm					
size Code	A ₀		B ₀		K ₀		W	E	F	ØD ₀	ØDI	P ₀ ⁽²⁾	Pı	P ₂	Т2	
	Min.	Max.	Min.	Max.	Min.	Max.					Min.				Min.	Max,
0805	1.29	1.65	2.09	2.60	1.25	1.62	8.1 ±0.20	1.75 ±0.1	3.5 ±0.05	1.5 +0.1/-0.0	+0. /-0.0	4.0 ±0.10	4.0 ±0.10	2.0 ±0.05	1.30	1.67
1206	1.65	2.12	3.30	3.75	1.22	2.15	8.1 ±0.20	1.75 ±0.1	3.5 ±0.05	1.5 +0.1/-0.0	+0.1/-0.0	4.0 ±0.10	4.0 ±0.10	2.0 ±0.05	1.27	2.20
1210	2.55	3.02	3.31	3.88	0.97	2.92	8.1 ±0.20	1.75 ±0.1	3.5 ±0.05	1.5 +0.1/-0.0	+0.1/-0.0	4.0 ±0.10	4.0 ±0.10	2.0 ±0.05	1.02	2.97
1808	2.05	2.55	4.80	5.45	1.30	2.45	2. ±0.20	1.75 ±0.1	5.5 ±0.05	1.5 +0.1/-0.0	1.5 +0.1/-0.0	4.0 ±0.10	4.0 ±0.10	2.0 ±0.05	1.35	2.50
1812	3.35	3.75	4.70	5.33	0.70	2.40	2. ±0.20	1.75 ±0.1	5.5 ±0.05	1.5 +0.1/-0.0	1.5 +0.1/-0.0	4.0 ±0.10	8.0 ±0.10	2.0 ±0.05	0.75	2.45
2220	5.12	5.32	5.84	6.04	1.28	1.48	12.0 ±0.20	1.75 ±0.1	5.5 ±0.05	1.5 +0.1/-0.0	1.5 +0.1/-0.0	4.0 ±0.10	8.0 ±0.10	2.0 ±0.05	1.33	1.53

ΝΟΤΕ

I. Typical capacitor displacement in pocket

2. P_0 pitch tolerance over any 10 pitches is $\pm 0.2 \mbox{ mm}$



REEL SPECIFICATION

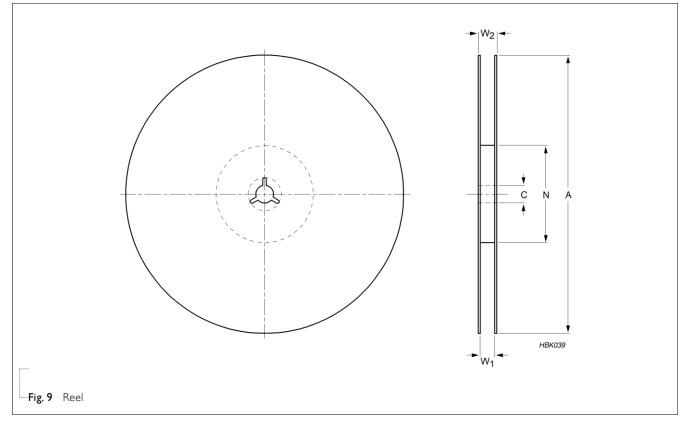


Table 14 Reel dimensions; see Fig.9

	SYMBOL				Unit: mm
TAPE WIDTH	A	Ν	С	W	W _{2max.}
8 (Ø178 mm/7")	178 ±1.0	60 ±1.0	3 +0.50/-0.20	9.4 ±1.5	4.4
8 (Ø330 mm/13")	330 ±1.0	100 ±1.0	13 +0.50/-0.20	9.0 ±0.2	14.4
12 (Ø178 mm/7")	178 ±1.0	60 ±1.0	13 +0.50/-0.20	3.4 ± .5	18.4

PROPERTIES OF REEL

Material: polystyrene

Surface resistance: $<10^{10}$ X/sq.



REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 16	May 10, 2023	-	- Add NP0/1206/2.2nF to 10nF/500V to 630V
Version 15	Dec. 5, 202	-	- I.R. Spec. updated X7R/0805/≥3.9nF X7R/1206/≥12nF X7R/1210/≥12nF X7R/1808/≥18nF X7R/1812/≥27nF X7R/2220/≥150nF
Version 14	Oct. 26, 2021	-	- Add 500V/630V
Version 13	Feb. 28, 2021	-	- Add NPO/1206/0.47pF to 10pF with 0.85 mm
Version 12	Dec. 01, 2020	-	- Add X7R/0805/1.5nF to 3.3nF/1KV. NPO/0805/ 56pF to 82pF/1KV
Version 11	Jul. 13, 2018	-	- Add NPO/1206/10pF to 47pF/3KV
Version 10	Mar. 7, 2017	-	- 0805 L4 spec updated
Version 9	Jan. 16, 2017	-	- Product range updated
Version 8	Oct. 12, 2015	-	- Product range updated
Version 7	May 21, 2014	-	- Product range updated
Version 6	Jun. 17, 2012	-	- Product range updated
Version 5	Sep 25, 2012	-	- Product range updated
Version 4	Aug 08, 2011	-	- Product range updated
Version 3	Jan 19, 2011	-	- Dimension updated - Add NP0 0805 TKV
Version 2	Feb 02, 2010	-	- Change to dual brand datasheet that describe High-Voltage NP0/X7R series with RoHS compliant
			- Replace the high voltage part of pdf files: UP-NP0X7R_HV_IK-to-4KV_I and UY-NP0X7R_HV_IK-to-4KV_I
			- Description of "Halogen Free compliant" added
			- Product range updated
			- Define global part number
			- Test method and procedure updated
Version I	Sep 30, 2005	-	- Thickness revised
Version 0	Sep 12, 2005	-	- New



Surface-Mount Ceramic Multilayer Capacitors

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