



SmartLynq Data Cable

by: Xilinx, Inc

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SmartLynq is a high performance JTAG cable for high-speed FPGA and flash programming, hardware and software debug, performance analysis, and event trace.

Part Number: HW-SMARTLYNQ-G

Lead Time: 8 weeks

Device Support: Zynq-7000, Zynq UltraScale+ MPSoC, Zynq UltraScale+ RFSoC



or buy from: Authorized Distributors



Overview

Product Information

Resources

OVERVIEW

Product Description

SmartLynq is a high performance JTAG cable for high-speed FPGA and flash programming, hardware and software debug, performance analysis, and event trace.

This cable delivers:

- Up to 40Mbps throughput
- Ethernet host connection for remote access
- USB 2.0 host connection
- Faster embedded software debugging
- Support for Linux and Hypervisor aware debugging
- 8 GPIO for additional debug signals

The SmartLynq Data Cable is backward compatible with the Platform Cable USB II through a standard PC4 JTAG header connection to the target board. It provides higher throughput than previous generation cables, allowing for faster programming and debug. It is compatible with the Vivado® Design Suite, Labtools, and Xilinx Software Development Kit.

Comparison Metric	Platform Cable USB II	SmartLynq Data Cable
Device Programming and Hardware Debug	Υ	Υ
Basic Software Debug	Y	Y
OS / Hypervisor Aware Debug	N	Υ
Remote Access/Debug Capability	N	Y
USB 2.0 Host Interface	Υ	Υ
10/100/1000 Ethernet Host Interface	N	Y
Default Bitstream Programming Speed (MB/s)	0.4	4
JTAG Max Clock Speed (MHz)	12	40
Connection to Target via PC4 JTAG or Flying Leads (provided)	Υ	Υ
GPIO Available with Flying Leads Cable (provided)	N	Υ
Hardware Server Application	On Host	Local to Cable





01 SmartLynq Data Cable Module
02 JTAG Ribbon Cable
03 USB Cable
04 JTAG Flying Wire Adapter Cable
05 GPIO Flying Lead Cable
06 Ethernet Cable
07 Power Supply



04

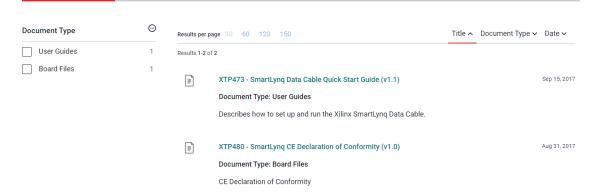






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