### **PNP Power Silicon Transistor**

#### Features

- Available in JAN, JANTX and JANTXV per MIL-PRF-19500/508
- TO-3 Package
- Designed for Power Amplifier and Medium Speed Switching Applications

### Electrical Characteristics ( $T_A = +25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Units	Min.	Max.
Collector - Emitter Breakdown Voltage	I <sub>C</sub> = -50 mA dc, 2N6437 I <sub>C</sub> = -50 mA dc, 2N6438	V <sub>(BR)CEO</sub>	V dc	-100 -120	_
Collector - Emitter Cutoff Current	V <sub>CE</sub> = -50 V dc, 2N6437 V <sub>CE</sub> = -60 V dc, 2N6438	I <sub>CEO</sub>	µA dc	_	-50
Collector - Emitter Cutoff Current	V <sub>CE</sub> = -100 V dc, V <sub>BE</sub> = +1.5 V dc, 2N6437 V <sub>CE</sub> = -120 V dc, V <sub>BE</sub> = +1.5 V dc, 2N6438	I <sub>CEX1</sub>	µA dc	_	-5.0
Collector - Base Cutoff Current	V <sub>CB</sub> = -120 V dc, 2N6437 V <sub>CB</sub> = -140 V dc, 2N6438	I <sub>CBO</sub>	µA dc	_	-10
Emitter - Base Cutoff Current	V <sub>EB</sub> = -6 V dc	I <sub>EBO</sub>	µA dc		-100
Base - Emitter Voltage (saturated)	I <sub>C</sub> = -10 A dc, I <sub>B</sub> = -1.0A dc	$V_{\text{BE(sat)}}$	V dc	_	-1.8
Collector-Emitter Voltage (saturated)	$I_{C}$ = -10 A dc, $I_{B}$ = -1.0 A dc $I_{C}$ = -25 A dc, $I_{B}$ = -2.5 A dc	V <sub>CE(sat)1</sub> V <sub>CE(sat)2</sub>	V dc	_	-1.0 -1.8
	$V_{CE}$ = -2 V dc, I <sub>C</sub> =5 A dc	$h_{\text{FE1}}$		40	
Forward Current Transfer Ratio	$V_{CE}$ = -2 V dc, $I_C$ = -10 A dc	$h_{\text{FE2}}$	-	30	120
	$V_{CE}$ = -2 V dc, $I_C$ = -25 A dc	h <sub>FE3</sub>		12	
Collector - Emitter Cutoff Current	T <sub>A</sub> = +150°C V <sub>CE</sub> = -100 V dc, V <sub>BE</sub> = +1.5 V dc, 2N6437 V <sub>CE</sub> = -120 V dc, V <sub>BE</sub> = +1.5 V dc, 2N6438		mA dc	_	-1.0
Forward - Current Transfer Ratio	$T_A = -55^{\circ}C$ $V_{CE} = -2V$ dc, $I_C = -10$ A dc	h <sub>FE4</sub>		10	





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## Electrical Characteristics ( $T_A = +25^{\circ}C$ unless otherwise specified)

Parameter		Test Conditions	Symbol	Units	Min.	Max.
Turn-On Time		$V_{CC}$ = -80 V dc, I <sub>C</sub> = -10 A dc, I <sub>B1</sub> = -1.0 A dc	t <sub>on</sub>	μs	_	0.5
Turn-Off Time		$V_{CC}$ = -80 V dc, I <sub>C</sub> = -10 A dc, I <sub>B1</sub> = I <sub>B2</sub> = -1.0 A dc	t <sub>off</sub>	μs	_	1.25
Storage Time		$V_{CC}$ = -80 V dc, I <sub>C</sub> = -10 A dc, I <sub>B1</sub> = I <sub>B2</sub> = -1.0 A dc	ts	μs	_	1.0
Dynamic Charact	teristics					
Magnitude of Common-Emitter Small-Signal Short-Circuit Forward Current Transfer Ratio		$V_{CE}$ = -10 V dc, I <sub>C</sub> = -1 A dc, f = 10 MHz	h <sub>fe</sub>	-	4.0	12
Output Capacitance (Open Circuit)		$V_{CB}$ = -10 V dc; I <sub>E</sub> = 0, 0.1 MHz ≤ f ≤ 1 MHz	C <sub>obo</sub>	pF	_	700
Safe Operating A	Area Continuous					
DC Tests: $T_c = +25^{\circ}C$ , I Cycle, t = 1.0 s						
Test 1:	$V_{CE}$ = -8 V dc, $I_{C}$ = -25 A dc (Both device types)					
Test 2:	V <sub>CE</sub> = -14 V dc, I <sub>C</sub> = -14 A dc (Both device types)					
Test 3:	$V_{CE}$ = -100 V dc, I <sub>C</sub> = -100 mA dc 2N6437 $V_{CE}$ = -120 Vdc, I <sub>C</sub> = -83 mA dc 2N6438					





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### Absolute Maximum Ratings ( $T_A = +25^{\circ}C$ unless otherwise specified)

Ratings	Symbol	Value
Collector - Emitter Voltage 2N6437 2N6438	V <sub>CEO</sub>	-100 V dc -120 V dc
Collector - Base Voltage 2N6437 2N6438	V <sub>CBO</sub>	-120 V dc -140 V dc
Emitter - Base Voltage	V <sub>EBO</sub>	-6 V dc
Base Current	I <sub>B</sub>	-10 A dc
Collector Current	Ic	-25 A dc
Total Power Dissipation $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	P <sub>T</sub> <sup>(1)</sup>	200 W 112 W
Operating & Storage Temperature Range	T <sub>op</sub> , T <sub>stg</sub>	-65°C to +200°C

#### **Thermal Characteristics**

Characteristics	Symbol	Max. Value
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.875°C/W

1. Between  $T_c$  = +25°C and  $T_c$  = +200°C, linear derating factor (average) = 1.14 W/°C.

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**Outline Drawing (TO-3)** 



FIGURE 1. Physical dimensions (TO-204 similar to TO-3).

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#### Outline Drawing (TO-3)

Ltr	Dimensions				Notes
	Inches		Millir	Millimeters	
	Min	Max	Min	Max	
CD		.875		22.23	
СН	.250	.360	6.35	9.14	
HR	.495	.525	12.57	13.33	4
HR <sub>1</sub>	.131	.188	3.33	4.78	4
нт	.060	.135	1.52	3.43	
LD	.038	.043	0.97	1.09	4, 6
LL	.312	.500	7.92	12.7	
L1		.050		1.27	6
MHD	.151	1.65	3.83	41.91	4
MHS	1.177	1.197	29.90	30.40	
PS	.420	.440	10.67	11.18	3
PS <sub>1</sub>	.205	.225	5.21	5.72	3
S <sub>1</sub>	.655	.675	16.64	17.15	
Notes	1, 2, 5, 7 1		1, 2	, 5, 7	

NOTES:

1. Dimensions are in inches.

Millimeters are given for general information only.
These dimensions should be measured at points .050 inch (1.27 mm) +.005 inch (0.13 mm)

- -.000 inch (0.00 mm) below seating plane. Measurement will be made at the seating plane. 4. Two places.
- 5. The seating plane of the header shall be flat within .001 inch (0.03 mm) concave to .004 inch (0.10 mm) convex inside a .930 inch (23.62 mm) diameter circle on the center of the header and flat within .001 inch (0.03 mm) concave to .006 inch (0.15 mm) convex overall.
- Lead diameter shall not exceed twice LD within L1.
- 7. Lead designation, shall be as follows:
- In accordance with ASME Y14.5M, diameters are equivalent to \u03e6x symbology.

Lead number	Bipolar transistor
1	Emitter
2	Base
Case	Collector



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FIGURE 3. Maximum safe operating area (continuous dc).

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FIGURE 4 Safe operating area for switching between saturation and cutoff (unclamped inductive load).

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