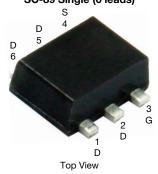


Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A)	Q _g (TYP.)			
	0.100 at V _{GS} = -4.5 V	-1.44				
-30	0.112 at V _{GS} = -3.7 V	-1.36	8.1 nC			
	0.140 at V _{GS} = -2.5 V	-1.22				

SC-89 Single (6 leads)



Marking Code: C
Ordering Information:

Si1079X-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

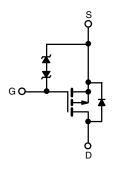
- TrenchFET® power MOSFET
- Typical ESD performance 2500 V
- 100 % R_a tested
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912

Pb-free RoHS

ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- Load switch for portable devices
- Power management



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V _{GS}	± 12	v	
Continuous Drain Current (T, = 150 °C)	T _A = 25 °C		-1.44 ^{b, c}		
Continuous Drain Current (1) = 150 C)	T _A = 70 °C	I _D	-1.15 ^{b, c}	^	
Pulsed Drain Current (t = 300 µs)		I _{DM}	-8	A	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S -0.28 ^{b, c}			
Maximum Dawar Dissination	T _A = 25 °C	В	0.33 b, c	W	
Maximum Power Dissipation	T _A = 70 °C	P _D	0.21 ^{b, c}	VV	
Operating Junction and Storage Temperature Ra	inge	T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum Junction-to-Ambient ^{a, b}	t ≤ 5 s	R _{thJA} 300 360	375	°C/W		
Wiaximum Junction-to-Ambient -, -	Steady State		360	450	C/VV	

Notes

- a. Maximum under steady state conditions is 450 °C/W.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.



www.vishay.com

Vishay Siliconix

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static	01111202	1201 001121110110			III O CI	0	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30	_	_	V	
V _{DS} Temperature Coefficient			-	-21	_		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	_	3	_	mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	-0.6	-	-1.5	V	
	- 03(11)	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		_	± 10		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	-	± 1	μΑ	
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	-	_	-1		
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} = -30 V, V _{GS} = 0 V, T _J = 85 °C	-	_	-10	\dashv	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-8	_	_	Α	
	·D(011)	$V_{GS} = -4.5 \text{ V}, I_D = -1.4 \text{ A}$	_	0.083	0.100	Ω Ω	
Drain-Source On-State Resistance a	R _{DS(on)}	$V_{GS} = -3.7 \text{ V}, I_D = -1.3 \text{ A}$	_	0.093	0.112		
Drain Goardo en Glate Heelelanes	03(011)	$V_{GS} = -2.5 \text{ V}, I_D = -0.7 \text{ A}$	_	0.108	0.140		
Forward Transconductance	9 _{fs}	$V_{DS} = -15 \text{ V}, I_D = -1.4 \text{ A}$	_	10	-	S	
Dynamic b	915	103 10 1, 10 11111					
Input Capacitance	C _{iss}		-	750	_		
Output Capacitance	C _{oss}	V _{DS} = -15 V, V _{GS} = 0 V, f = 1 MHz	-	67	-	pF	
Reverse Transfer Capacitance	C _{rss}		-	60	-		
	Q _g	V _{DS} = -15 V, V _{GS} = -10 V, I _D = -1.4 A	-	17	26		
Total Gate Charge		50 . 60 . 5	-	8.1	13	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -1.4 \text{ A}$	-	1.2	_		
Gate-Drain Charge	Q _{gd}	20	-	2.2	_		
Gate Resistance	R _g	f = 1 MHz	3.6	18	36	Ω	
Turn-On Delay Time	t _{d(on)}		-	22	33		
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 13 \Omega$	-	33	50		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -1.15 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	58	87		
Fall Time	t _f		-	30	45		
Turn-On Delay Time	t _{d(on)}		-	5	10	ns	
Rise Time	t _r	V_{DD} = -15 V, R_L = 13 Ω	-	20	30		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -1.15 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	80	120		
Fall Time	t _f		-	30	45		
Drain-Source Body Diode Characteri			L	l.	l		
Pulse Diode Forward Current ^a	I _{SM}		-	-	-8	Α	
Body Diode Voltage	V _{SD}	I _S = -1.15 A	-	-0.75	-1.2	V	
Body Diode Reverse Recovery Time	t _{rr}		-	16	24	ns	
Body Diode Reverse Recovery Charge	dy Diode Reverse Recovery Charge Q _{rr}		-	7	14	nC	
Reverse Recovery Fall Time	t _a	I _F = -1.15 A, dI/dt = 100 A/μs	-	9	-		
Reverse Recovery Rise Time	t _b	┦ !		7	-	ns	

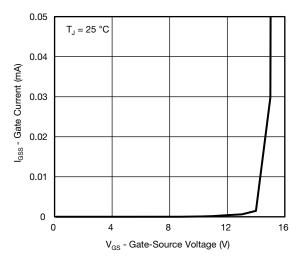
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

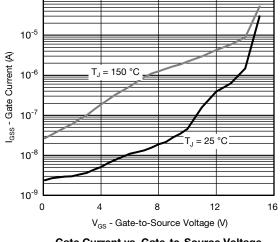
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

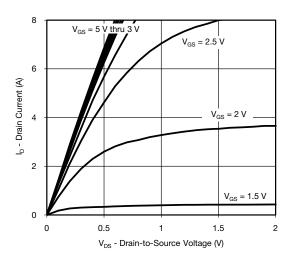


Gate Current vs. Gate-Source Voltage

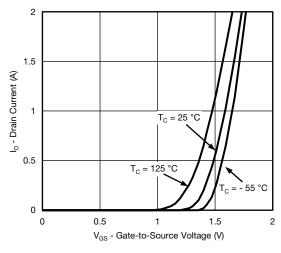


10⁻⁴

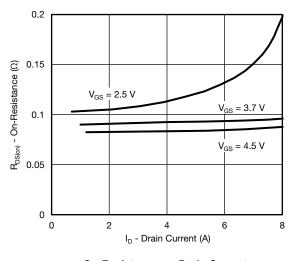
Gate Current vs. Gate-to-Source Voltage

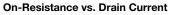


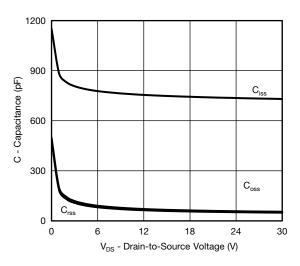
Output Characteristics



Transfer Characteristics Curves vs. Temperature



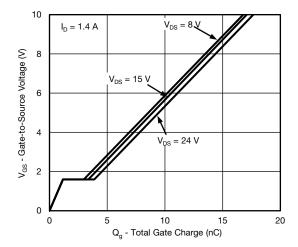




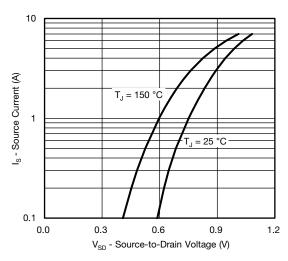
Capacitance



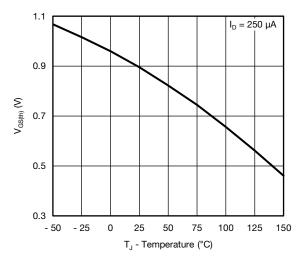
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



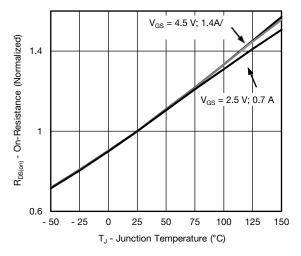
Gate Charge



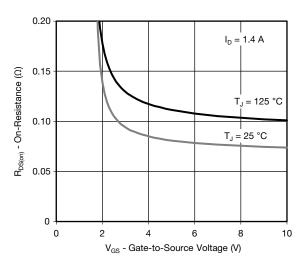
Source-Drain Diode Forward Voltage



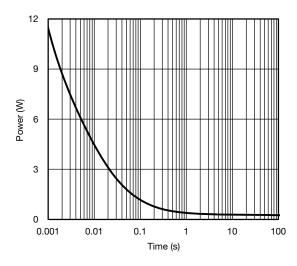
Threshold Voltage



On-Resistance vs. Junction Temperature



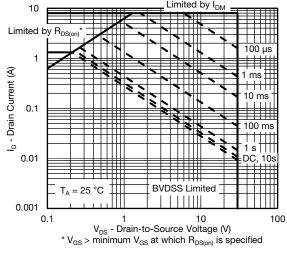
On-Resistance vs. Gate-to-Source Voltage

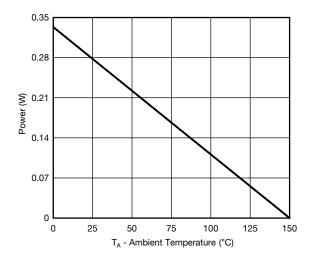


Single Pulse Power, Junction-to-Ambient



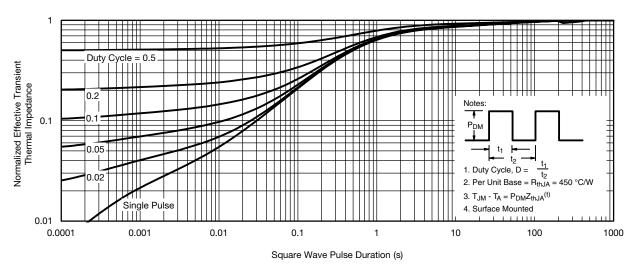
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





Safe Operating Area, Junction-to-Ambient

Power Junction-to-Ambient

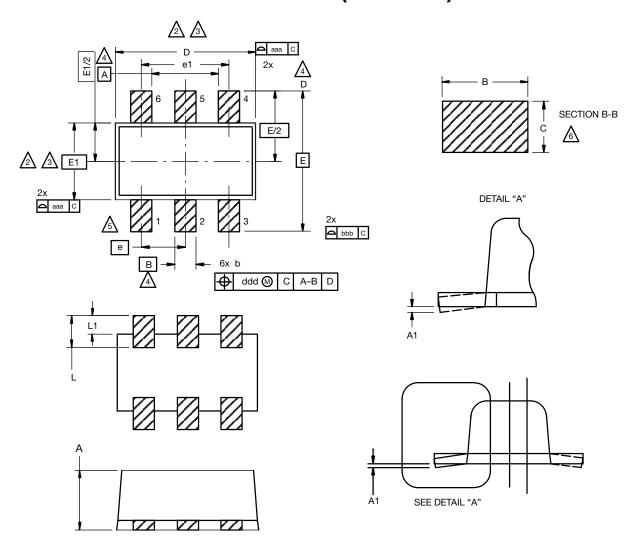


Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg262966.



SC-89 6-Leads (SOT-563F)



Notes

1. Dimensions in millimeters.

Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.

Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

ADatums A, B and D to be determined 0.10 mm from the lead tip.

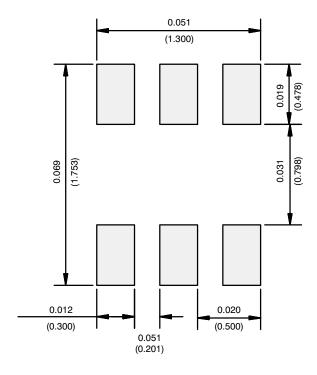
A Terminal numbers are shown for reference only.

These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	0.56	0.58	0.60		
A1	0	0.02	0.10		
b	0.15	0.22	0.30		
С	0.10	0.14	0.18		
D	1.50	1.60	1.70		
E	1.50	1.60	1.70		
E1	1.15	1.20	1.25		
е	0.45	0.50	0.55		
e1	0.95	1.00	1.05		
L	0.25	0.35	0.50		
L1	0.10	0.20	0.30		
C14-0439-Rev. C, 11-Aug-14 DWG: 5880					



RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



Legal Disclaimer Notice

Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000