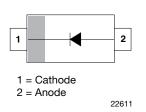


## Vishay Semiconductors

# **Small Signal Fast Switching Diode**





#### **MARKING** (example only)



22610

Bar = cathode marking XY = type code

#### **MECHANICAL DATA**

Case: SOD-323

Weight: approx. 4.3 mg
Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

#### **FEATURES**

 These diodes are also available in other case styles including the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148, and the SOT-23 case with the type designation IMBD4148-V





- · Silicon epitaxial planar diode
- · Fast switching diodes
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

PARTS TABLE						
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS		
1N4148WS-V	1N4148WS-V-GS18 or 1N4148WS-V-GS08	A2	Single diode	Tape and reel		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		$V_{R}$	75		
Repetitive peak reverse voltage		V <sub>RRM</sub> 100		¬	
Average rectified current half wave rectification with resistive load <sup>(1)</sup>	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Surge forward current	t < 1 s and T <sub>j</sub> = 25 °C	I <sub>FSM</sub>	350	1	
Power dissipation (1)		P <sub>tot</sub>	200	mW	

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.



# www.vishay.com Vishay Semiconductors

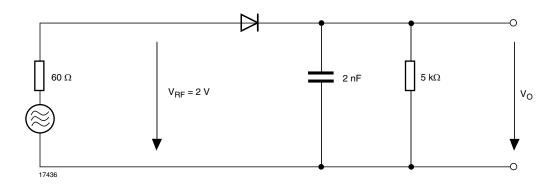
<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	650	K/W	
Junction temperature		T <sub>j</sub>	150		
Operating temperature range		T <sub>j</sub>	- 55 to + 150	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150		

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>			1000	mV
Forward voltage	I <sub>F</sub> = 100 mA	$V_{F}$			1200	
	V <sub>R</sub> = 20 V	I <sub>R</sub>			25	nA
Leakage current	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μΑ
Leakage current	V <sub>R</sub> = 100 V	I <sub>R</sub>			100	
	$V_R = 20 \text{ V}, T_j = 150 ^{\circ}\text{C}$	I <sub>R</sub>			50	
Diode capacitance	$V_F = V_R = 0 V$	C <sub>D</sub>			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V <sub>fr</sub>			2.5	V
Reverse recovery time	$I_F$ = 10 mA, $i_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			4	ns
Rectification efficiency	f = 100 MHz, V <sub>RF</sub> = 2 V	ην	0.45			

### RECTIFICATION EFFICIENCY MEASUREMENT CIRCUIT



# Vishay Semiconductors

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

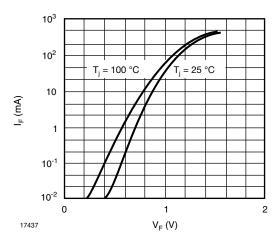


Fig. 1 - Forward Characteristics

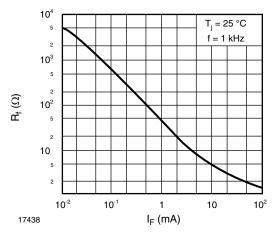


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

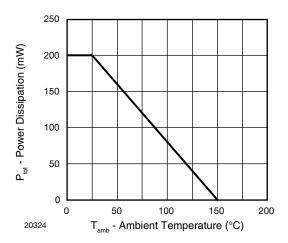


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

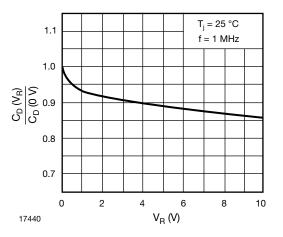


Fig. 4 - Relative Capacitance vs. Reverse Voltage

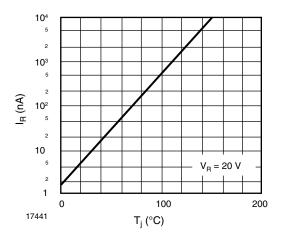


Fig. 5 - Leakage Current vs. Junction Temperature

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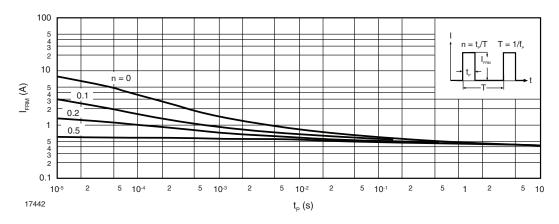
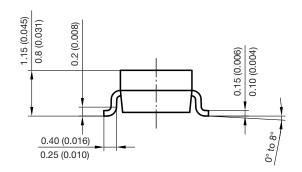
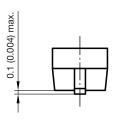
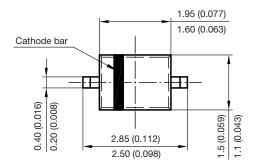


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

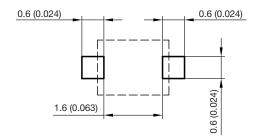
### PACKAGE DIMENSIONS in millimeters (inches): SOD-323







Foot print recommendation:



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Vishay

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