ROHS

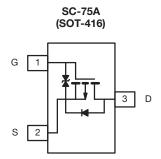
HALOGEN

**FREE** 





# N-Channel 1.5 V (G-S) MOSFET



Marking Code: G

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	20				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	5				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 2.5 \text{ V}$	7				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 1.8 \text{ V}$	9				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 1.5 \text{ V}$	10				
Q <sub>g</sub> typ. (nC)	750				
I <sub>D</sub> (A)	200				
Configuration	Single				

#### **FEATURES**

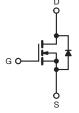
- TrenchFET® power MOSFETs: 1.5 V rated
- · Low-side switching
- Low on-resistance: 5 W
- Low threshold: 0.9 V (typ.)
- Fast switching speed: 35 ns
- Enhance power dissipation and lower RthJC
- 2000 V ESD protection
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **BENEFITS**

- Ease in driving switches
- Low offset (Error) voltage
- · Low-voltage operation
- · High-speed circuits
- Low battery voltage operation

#### **APPLICATIONS**

- Drivers: relays, solenoids, lamps, hammers, displays, memories
- Battery operated systems
- · Power supply converter circuits
- Load/power switching cell phones, pagers



N-Channel MOSFET

ORDERING INFORMATION				
Package	SC-75A			
Lead (Pb)-free and halogen-free	Si1032R-T1-GE3			

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)							
			SI1032R		SI1032X b		
PARAMETER		SYMBOL	5 s	STEADY STATE	5 s	STEADY STATE	UNIT
Drain-source voltage		$V_{DS}$	20				V
Gate-source voltage		V <sub>GS</sub>	± 6			\ \ \	
Continuous drain surrent /T 150 °C\ 3	T <sub>A</sub> = 25 °C	I <sub>D</sub>	200	140	210	200	
Continuous drain current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		110	100	150	140	
Pulsed drain current <sup>a</sup>		I <sub>DM</sub>	500		(	600	mA
Continuous source current (diode conduction) a		Is	250	200	300	240	
Maximum navvey dissination 3 for CC 75	T <sub>A</sub> = 25 °C	0	280	250	340	300	mW
Maximum power dissipation <sup>a</sup> for SC-75	T <sub>A</sub> = 85 °C	$P_{D}$	145	130	170	150	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150				°C
Gate-source ESD rating (HBM, method 3015)		ESD	2000		V		

#### Note

- a. Surface mounted on FR4 board
- b. Si1032X, product End of Life November 2024



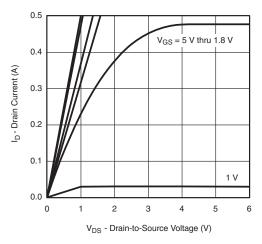
<b>SPECIFICATIONS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.40	0.7	1.2	V	
Gate-body leakage	lass	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 2.8 \text{ V}$	-	± 0.5	± 1.0	μΑ	
Gate-body leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	± 1.0	± 3.0		
Zero gate voltage drain current	1	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1		
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$	-	-	10		
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	250	-	-	mA	
		$V_{GS} = 4.5 \text{ V}, I_D = 200 \text{ mA}$	-	-	5	Ω	
Drain-source on-state resistance <sup>a</sup>	D	$V_{GS} = 2.5 \text{ V}, I_D = 175 \text{ mA}$	-	-	7		
Drain-source on-state resistance "	R <sub>DS(on)</sub>	$V_{GS} = 1.8 \text{ V}, I_D = 150 \text{ mA}$	-	-	9		
		$V_{GS} = 1.5 \text{ V}, I_D = 40 \text{ mA}$	-	-	10		
Forward transconductance a	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 200 \text{ mA}$	-	0.5	-	S	
Diode forward voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 150 mA, V <sub>GS</sub> = 0 V	-	-	1.2	V	
Dynamic <sup>b</sup>							
Total gate charge	$Q_g$		-	750	-		
Gate-source charge	$Q_{gs}$	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 250 \text{ mA}$	-	75	-	рC	
Gate-drain charge	$Q_{gd}$		-	225	-		
Turn-on delay time	t <sub>d(on)</sub>		-	-	50		
Rise time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 47 $\Omega$	-	-	25	ns	
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 200 \text{ mA}, V_{GEN} = 4.5 \text{ V}, R_g = 10 \Omega$	-	-	50	115	
Fall time	t <sub>f</sub>		-	-	25		

#### Notes

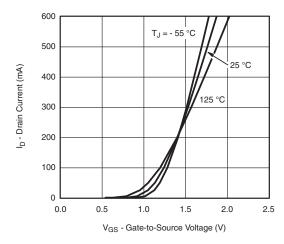
- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



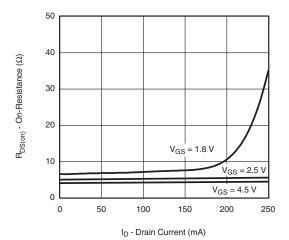
**Output Characteristics** 



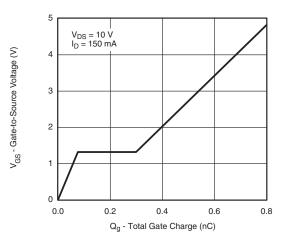
**Transfer Characteristics** 



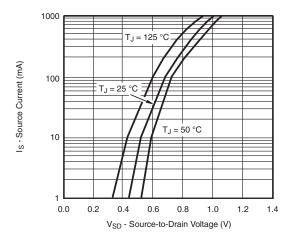
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



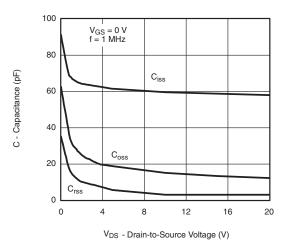
#### On-Resistance vs. Drain Current



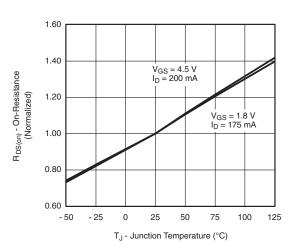
**Gate Charge** 



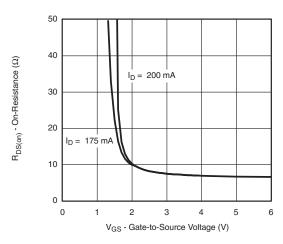
Surge-Drain Diode Forward Voltage



Capacitance



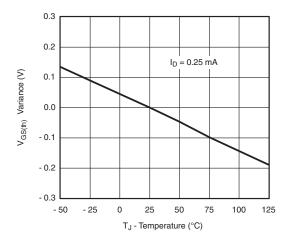
On-Resistance vs. Gate-to-Source Voltage

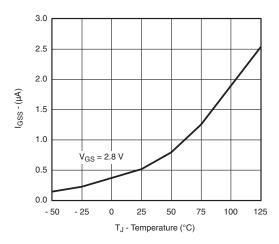


On-Resistance vs. Gate-to-Source Voltage



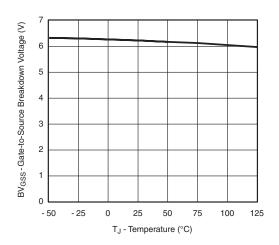
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



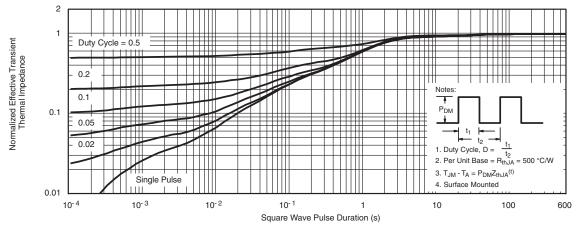


Threshold Voltage Variance vs. Temperature





BV<sub>GSS</sub> vs. Temperature

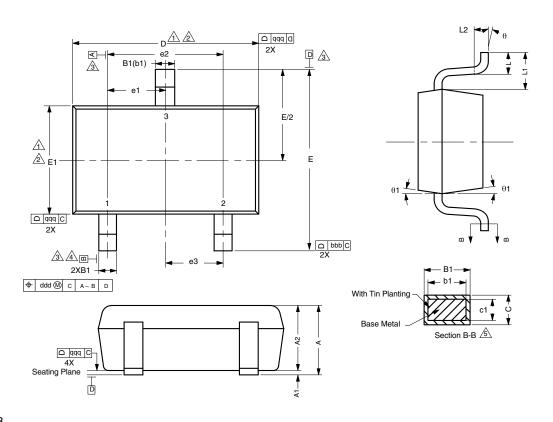


Normalized Thermal Transient Impedance, Junction-to-Ambient (SC-75A, Si1032R Only)

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## SC-75A: 3 Leads



DWG: 5868

#### Notes

Dimensions in millimeters will govern.

- Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
- Datums A, B and D to be determined 0.10 mm from the lead tip.

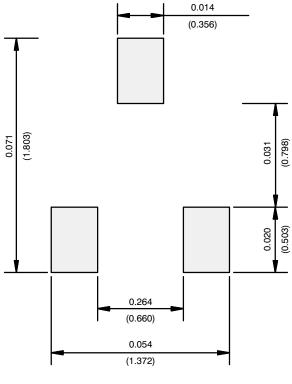
  Atterminal positions are shown for reference only.
- These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES
aaa	0.10
bbb	0.10
ccc	0.10
ddd	0.10

DIM.	MILLIMETERS			NOTE	
Dilvi.	MIN.	NOM.	MAX.	NOTE	
Α	-	-	0.80		
A1	0.00	-	0.10		
A2	0.65	0.70	0.80		
B1	0.19	-	0.24	5	
b1	0.17	-	0.21		
С	0.13	-	0.15	5	
c1	0.10	-	0.12	5	
D	1.48	1.575	1.68	1, 2	
Е	1.50	1.60	1.70		
E1	0.66	0.76	0.86	1, 2	
e1		0.50 BSC			
e2		1.00 BSC			
e3		0.50 BSC			
L	0.15	0.205	0.30		
L1	0.40 ref.				
L2	0.15 BSC				
q	0°	-	8°		
q1	4°	-	10°		



### **RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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