

RoHS

HALOGEN FREE

## Silicon PIN Photodiode



#### **FEATURES**

Package type: surface-mount





• Radiant sensitive area (in mm<sup>2</sup>): 7.5

· High photo sensitivity

· High radiant sensitivity

• Suitable for visible and near infrared radiation

• Fast response times

• Angle of half sensitivity:  $\varphi = \pm 65^{\circ}$ 

• Floor life: 168 h, MSL 3, according to J-STD-020

• Lead (Pb)-free reflow soldering

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **APPLICATIONS**

· High speed photo detector

### **DESCRIPTION**

VBPW34S and VBPW34SR are high speed and high sensitive PIN photodiodes. It is a surface mount device (SMD) including the chip with a 7.5 mm<sup>2</sup> sensitive area detecting visible and near infrared radiation.

PRODUCT SUMMARY			
COMPONENT	I <sub>ra</sub> (μΑ)	φ <b>(°)</b>	λ <sub>0.1</sub> (nm)
VBPW34S	55	± 65	430 to 1100
VBPW34SR	55	± 65	430 to 1100

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VBPW34S	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Gullwing	
VBPW34SR	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Reverse gullwing	

### Note

· MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	60	V	
Power dissipation	T <sub>amb</sub> ≤ 25 °C	P <sub>V</sub>	215	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C	
Soldering temperature	According to reflow solder profile Fig. 8	$T_{sd}$	260	°C	
Thermal resistance junction to ambient		$R_{thJA}$	350	K/W	



PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	$V_{F}$		1	1.3	V
Breakdown voltage	$I_R = 100  \mu A,  E = 0$	V <sub>(BR)</sub>	60	-	-	V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>	-	2	30	nA
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0	$C_{D}$	-	70	-	pF
	V <sub>R</sub> = 3 V, f = 1 MHz, E = 0	C <sub>D</sub>	-	25	40	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	Vo	-	350	-	mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	$TK_Vo$	-	-2.6	-	mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	l <sub>k</sub>	-	50	-	μΑ
Temperature coefficient of I <sub>k</sub>	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK <sub>Ik</sub>	-	0.1	-	%/K
Reverse light current	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm},$ $V_{R} = 5 \text{ V}$	I <sub>ra</sub>	45	55	-	μΑ
Angle of half sensitivity		φ	-	± 65	-	0
Wavelength of peak sensitivity		$\lambda_{p}$	-	940	-	nm
Range of spectral bandwidth		λ <sub>0.1</sub>	-	430 to 1100	-	nm
Noise equivalent power	$V_R = 10 \text{ V}, \lambda = 950 \text{ nm}$	NEP	-	4 x 10 <sup>-14</sup>	-	W/√Hz
Rise time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t <sub>r</sub>	-	100	-	ns
Fall time	$V_{R} = 10 \text{ V}, R_{L} = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t <sub>f</sub>	-	100	-	ns

## **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

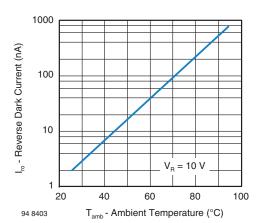


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

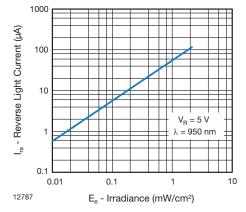


Fig. 3 - Reverse Light Current vs. Irradiance

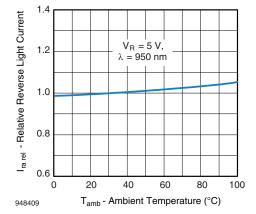


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

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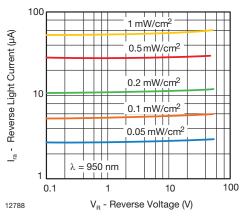


Fig. 4 - Reverse Light Current vs. Reverse Voltage

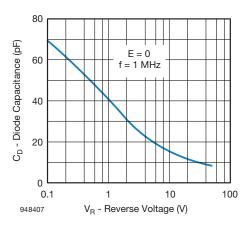


Fig. 5 - Diode Capacitance vs. Reverse Voltage

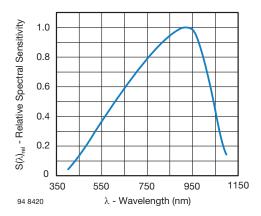


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

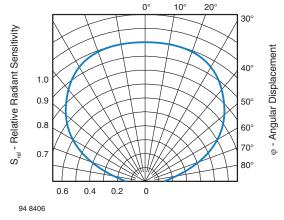
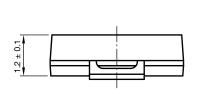
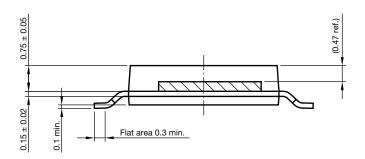
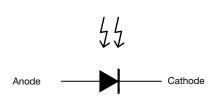


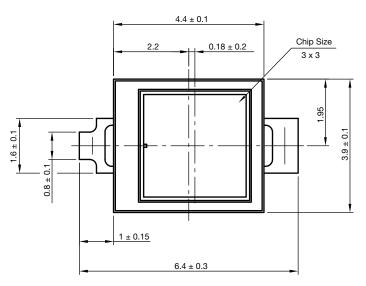
Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

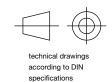
## PACKAGE DIMENSIONS FOR VBPW34S in millimeters

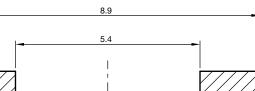












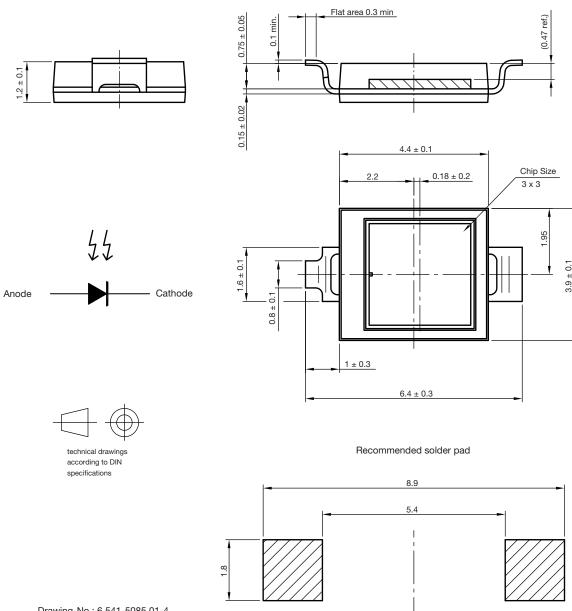
Recommended solder pad

Drawing-No.: 6.541-5086.01-4

Issue: 1; 15.04.10

22105

## **PACKAGE DIMENSIONS FOR VBPW34SR** in millimeters

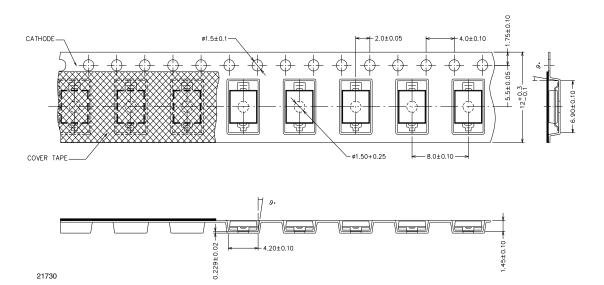


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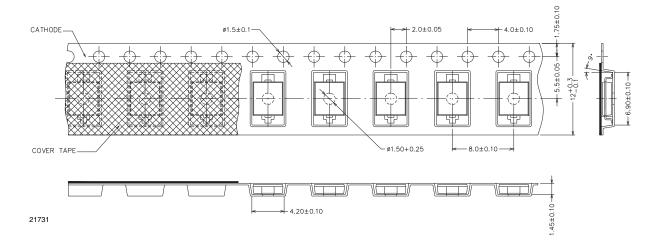
Issue: 1; 15.04.10

22104

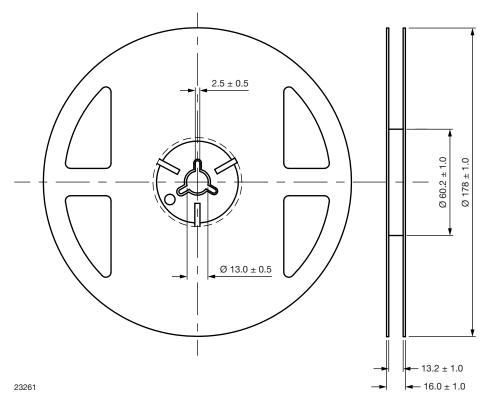
### **TAPING DIMENSIONS FOR VBPW34S** in millimeters



### **TAPING DIMENSIONS FOR VBPW34SR** in millimeters



## **REEL DIMENSIONS FOR VBPW34S AND VBPW34SR** in millimeters



### **SOLDER PROFILE**

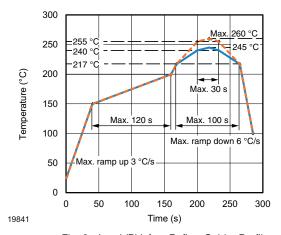


Fig. 8 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

## **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 %

or

96 h at 60 °C (+ 5 °C), RH < 5 %



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Vishay

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