



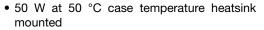
50 W Power Resistor Thick Film Technology



LINKS TO ADDITIONAL RESOURCES



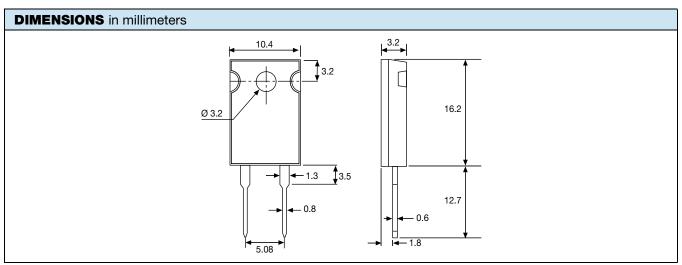
FEATURES





- Direct mounting ceramic on heatsink
- Broad resistance range: 0.010 Ω to 450 k Ω
- Non inductive
- TO-220 package: compact and easy to mount
- AEC-Q200 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

LTA series are the extension of RTO types. We used the direct ceramic mounting design (no metal tab) of our RCH power resistors applied to semiconductor packages.



Note

• Tolerances unless stated: ± 0.3 mm

STANDA	STANDARD ELECTRICAL SPECIFICATIONS						
MODEL	SIZE	RESISTANCE RANGE Ω	RATED POWER P _{50 °C} W	LIMITING ELEMENT VOLTAGE U _L V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C	$\begin{array}{c} \textbf{CRITICAL} \\ \textbf{RESISTANCE} \\ \Omega \end{array}$
LTA 50	TO-220	0.01 to < 0.5	50	500	5, 10	250, 700, 900	n/a
LIA 50	10-220	0.5 to 450K	50	500	1, 2, 5, 10	150	5K

MECHANICAL SPECIFICATIONS			
Mechanical Protection	Molded		
Resistive Element	Thick film		
Substrate	Alumina		
Connections	Tinned copper		
Weight	2 g max.		
Mounting Torque	1 Nm		

ENVIRONMENTAL SPECIFICATIONS				
Temperature Range	-55 °C to +175 °C			
Climatic Category	55 / 155 / 56			
Flammability	IEC 60695-11-5 Application time: $t_a = 10 \text{ s}$ Burning duration: $t_b < 30 \text{ s}$			



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TECHNICAL SPECIFICATIONS				
Dissipation and Associated	Onto a heatsink			
Power Rating and Thermal Resistance of the Component	50 W at +50 °C (case temp.) R _{TH (j - c)} : 2.5 °C/W Free air: 2.5 W at +25 °C			
Temperature Coefficient	See Performance table			
Standard	± 150 ppm/°C			
Dielectric Strength MIL STD 202	1500 V _{RMS} - 1 min 10 mA max.			
Insulation Resistance	$\geq 10^4 \text{M}\Omega$			
Inductance	≤ 0.1 µH			

PERFORMANCE				
TESTS	CONDITIONS	REQUIREMENTS		
Momentary Overload	EN 60115-1 1.5 Pr / 5 s U _S < 1.5 U _L	± (0.5 % + 0.05 Ω)		
Load Life	EN 60115-1 1000 h Pr at +25 °C	\pm (1 % + 0.05 Ω)		
High Temperature Exposure	AEC-Q200 rev. E conditions: MIL-STD-202 method 108 1000 h, +175 °C, unpowered	$\pm (0.5 \% + 0.05 \Omega)$		
Temperature Cycling	AEC-Q200 rev. D conditions: JESD22 method JA-104 1000 cycles, -55 °C to +125 °C dwell time -15 min	± (1 % + 0.05 Ω)		
Humidity Bias	AEC-Q200 rev. E conditions: MIL-STD-202 method 103 1000 h, 85 °C, 85 % RH, 10 % operating power	± (1 % + 0.05 Ω)		
Operational Life	AEC-Q200 rev. D conditions: MIL-STD-202 method 108 1000 h, 90/30, powered, +125 °C	± (1 % + 0.05 Ω)		
ESD Human Body Model	AEC-Q200 rev. E conditions: AEC-Q200-002 25 kV _{AD}	\pm (0.5 % + 0.05 Ω)		
Vibration	AEC-Q200 rev. E conditions: MIL-STD-202 method 204 5 g's for 20 min, 12 cycles test from 10 Hz to 2000 Hz	\pm (0.2 % + 0.05 Ω)		
Mechanical Shock	AEC-Q200 rev. E conditions: MIL-STD-202 method 213 100 g's, 6 ms, 3.75 m/s 3 shocks/direction	± (0.2 % + 0.05 Ω)		
Terminal Strength	AEC-Q200 rev. E conditions: MIL-STD-202 method 211	± (0.25 % + 0.05 Ω)		

SPECIAL FEATURES					
Resistance Values	≥ 0.01	≥ 0.015	≥ 0.1	≥ 0.5	
Tolerances		5 %, 10 %		1 %, 2 %, 5 %, 10 %	
Typical Temperature Coefficient (-55 °C to +155 °C)	± 900 ppm/°C	± 700 ppm/°C	± 250 ppm/°C	± 150 ppm/°C	





CHOICE OF THE HEATSINK

The user must choose according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 175 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{R_{TH (j-c)} + R_{TH (c-h)} + R_{TH (h-a)}}^{(1)}$$

P: expressed in W.

 ΔT : difference between maximum working temperature and room temperature.

thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance

thermal resistance value measured between outer side of the resistor and upper side of the heatsink. This is the R_{TH (c - h)}: thermal resistance of the interface (grease, thermal pad), and the quality of the fastening device.

R_{TH (h - a)}: thermal resistance of the heatsink.

Example:

R_{TH (c - a)} for LTA 50 power rating 10 W at ambient temperature +25 °C

Thermal resistance R_{TH (i - c)}: 2.5 °C/W

Considering equation (1) we have:

$$\Delta T = 175 \, ^{\circ}C - 25 \, ^{\circ}C = 150 \, ^{\circ}C$$

$$R_{TH (j-c)} + R_{TH (c-h)} + R_{TH (h-a)} = \frac{\Delta T}{P} = \frac{150}{10} = 15 \text{ °C/W}$$

 $R_{TH (c-h)} + R_{TH (h-a)} = 15 \text{ °C/W} - 2.5 \text{ °C/W} = 12.5 \text{ °C/W}$

$$R_{TH (c-h)} + R_{TH (h-a)} = 15 °C/W - 2.5 °C/W = 12.5 °C/W$$

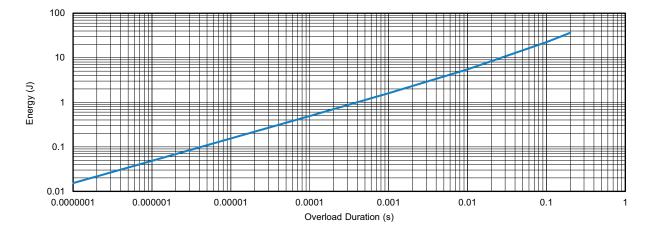
with a thermal grease $R_{TH (c-h)} = 1$ °C/W, we need a heatsink with $R_{TH (h-a)} = 11.5$ °C/W.

OVERLOADS

In any case the applied voltage must be lower than the maximum overload voltage of 750 V.

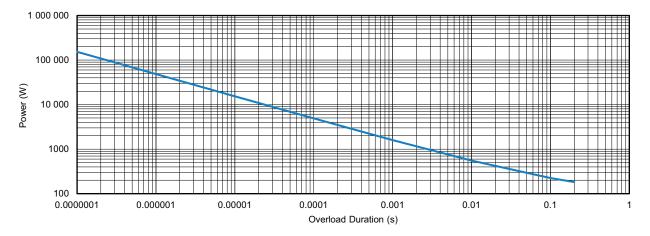
Accidental overload: the values indicated on the graph below are applicable to resistors in air or mounted onto a heatsink.

ENERGY CURVE





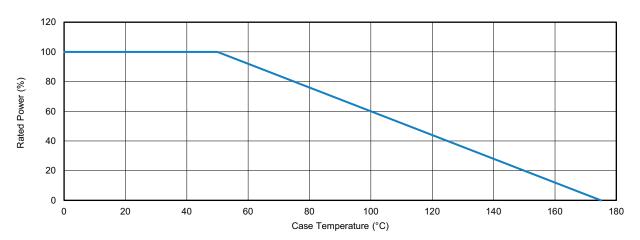
POWER CURVE



POWER RATING

The temperature of the case should be maintained within the limits specified.

To improve the thermal conductivity, surfaces in contact should be coated with a silicone grease and the torque applied on the screw for tightening should be around 1 Nm.



PACKAGING Tube of 50 units

MARKING

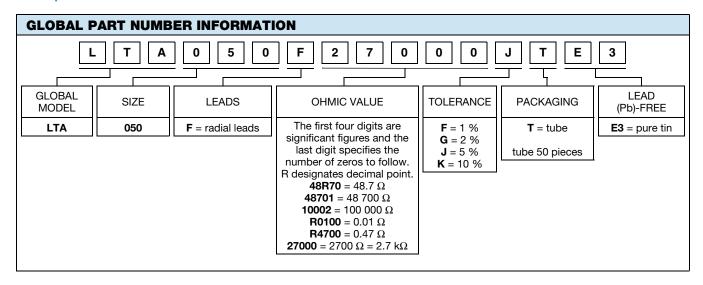
Model, style, resistance value (in Ω), tolerance (in %), manufacturing date, Vishay Sfernice trademark.

ORDERING INFORMATION							
LTA	50	F	2.7 k Ω	± 1 %	xxx	TU50	e3
MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	± 1 % ± 2 % ± 5 % ± 10 %	CUSTOM DESIGN optional on request: special TCR, shape etc.	PACKAGING	LEAD (Pb)-FREE



www.vishay.com

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RELATED DOCUMENTS				
APPLICATION NOTE				
Guidelines for Vishay Sfernice Resistive and Inductive Components	www.vishay.com/doc?52029			



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