Vishay Siliconix

# P-Channel 20 V (D-S) MOSFET

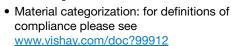


Marking Code: 25

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	-20					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = -10 V	0.028					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = -4.5 \text{ V}$	0.035					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = -2.5 \text{ V}$	0.052					
Q <sub>g</sub> typ. (nC)	7.0					
I <sub>D</sub> (A) <sup>a</sup>	-8					
Configuration	Single					

#### **FEATURES**

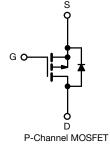
- TrenchFET® gen V p-channel MOSFET
- 100 % R<sub>g</sub> and UIS tested





#### **APPLICATIONS**

- · Load switches
- DC/DC converters
- Power management
- LED backlighting



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	Si3425DV-T1-GE3

<b>ABSOLUTE MAXIMUM RATINGS</b> (	T <sub>A</sub> = 25 °C, unles	ss otherwise	noted)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	-20	V
Gate-source voltage		$V_{GS}$	± 12	
Continuous drain current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>F</sub> = 25 °C		-8	
	T <sub>F</sub> = 70 °C	1 .	-6.8 <sup>a</sup>	
	T <sub>A</sub> = 25 °C	l <sub>D</sub>	-6.9 <sup>b, c</sup>	A
	T <sub>A</sub> = 70 °C		-5.5 <sup>b, c</sup>	
Pulsed drain current (V <sub>GS</sub> = 10 V, t = 100 μs)	I <sub>DM</sub>	-30	7	
Continuous source durin diada surrent	T <sub>F</sub> = 25 °C	,	-2.7	A
Continuous source-drain diode current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	-1.8 <sup>b, c</sup>	
Maximum power dissipation <sup>a</sup>	T <sub>F</sub> = 25 °C		3.0	
	T <sub>F</sub> = 70 °C	$P_{D}$	2.8	14/
	T <sub>A</sub> = 25 °C		2 b, c	W
	T <sub>A</sub> = 70 °C		1.3 b, c	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 5 s	R <sub>thJA</sub>	52	62.5	°C/W	
Maximum junction-to-foot (drain)	Steady state	$R_{thJF}$	34	41	1 C/W	

#### Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5
- d. Maximum under steady state conditions is 110 °C/W



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PARAMETER	METER SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static	•		I.	•		I.	
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA	-20	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = -10 mA	-	-14.2	-	\//90	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μA	-	3.2	-	mV/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-0.6	-	-1.5	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	-	-	± 100	nA	
7		V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V		-	-1		
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	-10	μΑ	
		V <sub>GS</sub> = -10 V, I <sub>D</sub> = -5 A	-	0.021	0.028		
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -3 \text{ A}$	-	0.0255	0.035	Ω	
	. ,	V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -2 A	-	0.035	0.052		
Forward transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -6 A	-	20	-	S	
Dynamic <sup>b</sup>	•						
Input capacitance	C <sub>iss</sub>		-	730	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	125	-		
Reverse transfer capacitance	C <sub>rss</sub>		-	100	-		
Total gate charge	Qg	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = -10 A	-	15	23	nC	
			-	7	11		
Gate-source charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$	-	1.8	-		
Gate-drain charge	$Q_{gd}$		-	1.8	-		
Gate resistance	$R_g$	f = 1 MHz	1.3	6.5	13	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	12	25		
Rise time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_L = 1 \Omega$	-	32	65	- ns	
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	22	45		
Fall time	t <sub>f</sub>		-	10	20		
Turn-on delay time	t <sub>d(on)</sub>		-	7	15		
Rise time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_L = 1 \Omega$	-	5	10	ns	
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	19	40		
Fall time	t <sub>f</sub>		-	7	15		
<b>Drain-Source Body Diode Characteristi</b>	cs						
Continous source-drain diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-2.7	А	
Pulse diode forward current <sup>a</sup>	I <sub>SM</sub>		-		-30		
Body diode voltage	V <sub>SD</sub>	$I_S = -5 \text{ A}, V_{GS} = 0 \text{ V}$	-	-0.81	-1.1	V	
Body diode reverse recovery charge	$Q_{rr}$		-	2.5	5	nC	
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = -10 A, dl/dt = 100 A/μs,	-	8	20		
Reverse recovery fall time	ta	T <sub>J</sub> = 25 °C	-	5	-	ns	
	t <sub>b</sub>	1		3			

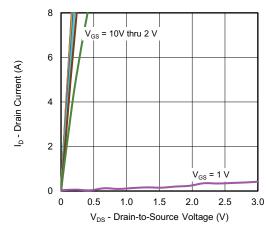
#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing

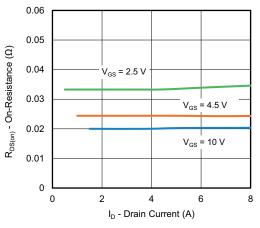
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



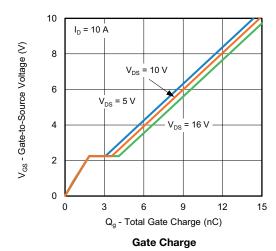
### TYPICAL CHARACTERISTICS (T<sub>A</sub>= 25 °C, unless otherwise noted)

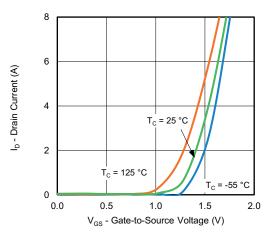


#### **Output Characteristics**

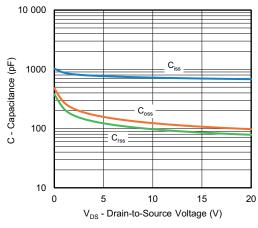


On-Resistance vs. Drain Current

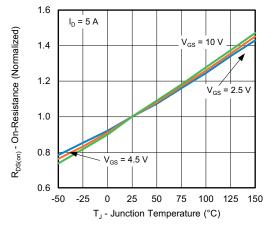




**Transfer Characteristics Curves vs. Temperature** 



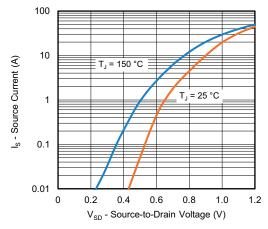
Capacitance



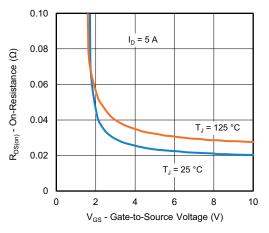
On-Resistance vs. Junction Temperature



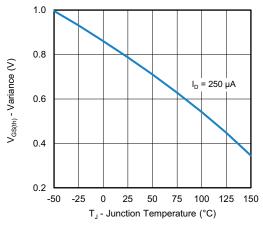
### TYPICAL CHARACTERISTICS (T<sub>A</sub>= 25 °C, unless otherwise noted)



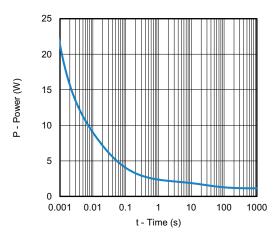
#### Source-Drain Diode Forward Voltage



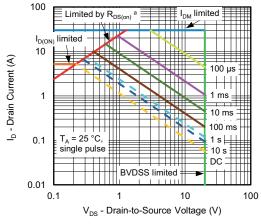
R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature



**Threshold Voltage** 



Single Pulse Power



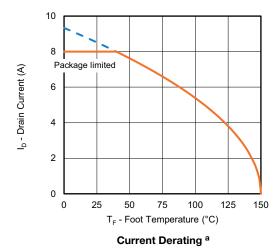
Safe Operating Area, Junction-to-Ambient

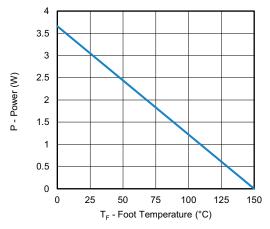
#### Note

a.  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise noted)





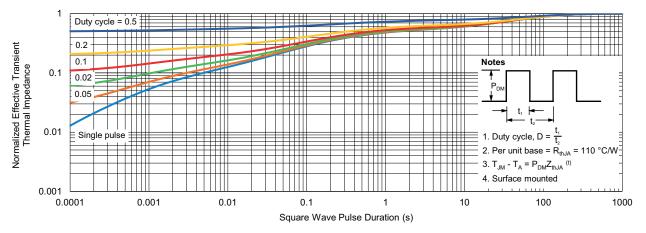
Power, Junction-to-Foot

#### Note

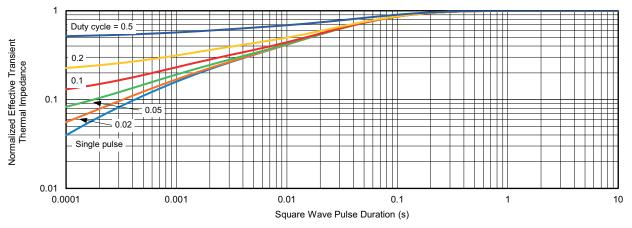
a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

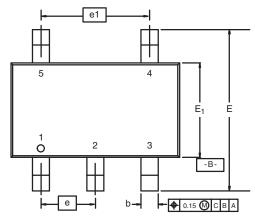
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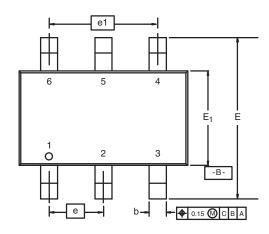




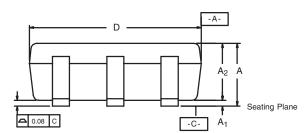
TSOP: 5/6-LEAD

**JEDEC Part Number: MO-193C** 

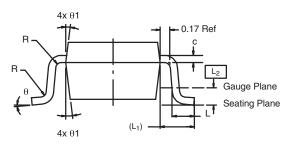




**5-LEAD TSOP** 





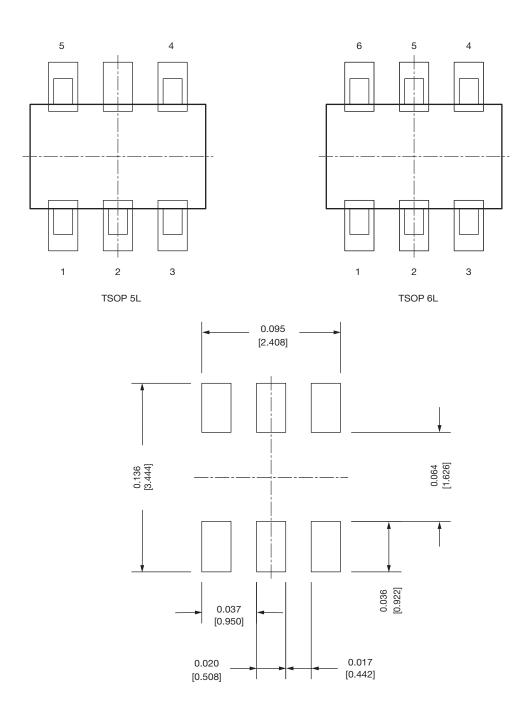


	MIL	LIMETER	RS	INCHES				
Dim	Min	Nom	Max	Min	Nom	Max		
Α	0.91	-	1.10	0.036	-	0.043		
A <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004		
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039		
b	0.30	0.32	0.45	0.012	0.013	0.018		
С	0.10	0.15	0.20	0.004	0.006	0.008		
D	2.95	3.05	3.10	0.116	0.120	0.122		
E	2.70	2.85	2.98	0.106	0.112	0.117		
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067		
е		0.95 BSC			0.0374 BSC			
e <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079		
L	0.32	-	0.50	0.012	-	0.020		
L <sub>1</sub>	0.60 Ref			0.024 Ref				
L <sub>2</sub>	0.25 BSC			0.010 BSC				
R	0.10	-	-	0.004	-	-		
θ	0°	4°	8°	0°	4°	8°		
θ1	7° Nom			7° Nom				
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540								

Document Number: 71200 18-Dec-06



## Recommended Land Pattern For TSOP-5L / TSOP-6L



#### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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