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Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET



Marking Code: 9SYXX

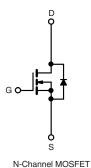
PRODUCT SUMMARY			
V _{DS} (V)	40		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0310		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.0360		
I _D (A)	7		
Configuration	Single		

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912







ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2318CES (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATING	S (T _C = 25 °C, unles	s otherwise noted	i)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	40	V	
Gate-source voltage		V_{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	1	7		
	T _C = 125 °C	I _D	4		
Continuous source current (diode conduction)		I _S	2.7	Α	
Pulsed drain current ^a		I _{DM}	28		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	13		
Single pulse avalanche energy	L = 0.1 min	E _{AS}	8.4	mJ	
Maximum power dissipation	T _C = 25 °C	- P _D	3	W	
	T _C = 125 °C		1	VV	
Operating junction and storage temperature	range	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount b	R_{thJA}	166	°C/W
Junction-to-foot (drain)		R _{thJF}	50	C/VV

Notes

- a. Pulse test; pulse width $\leq 300~\mu\text{s},$ duty cycle $\leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		-		ı	•	ı	L
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	· V _{GS} , I _D = 250 μA	1.5	2.0	2.5	V
Gate-source leakage	I _{GSS}	V _{DS} =	$0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 40 V	-	-	1	
Zero gate voltage drain current	I_{DSS}	$V_{GS} = 0 V$	$V_{DS} = 40 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	50	μΑ
		$V_{GS} = 0 V$	$V_{DS} = 40 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$	-	-	150	
On-state drain current ^a	$I_{D(on)}$	V _{GS} = 10 V	$V_{DS} \ge 5 V$	10	-	-	Α
		V _{GS} = 10 V	I _D = 7.9 A	-	0.0252	0.0310	
Drain-source on-state resistance ^a	D	$V_{GS} = 10 \text{ V}$	I _D = 7.9 A, T _J = 125 °C	-	-	0.0500	Ω
Diditi-Source off-State resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 7.9 A, T _J = 175 °C	-	-	0.0630	22
		$V_{GS} = 4.5 \text{ V}$	$I_D = 7.3 A$	-	0.0300	0.0360	
Forward transconductance b	9 _{fs}	V _{DS} :	= 15 V, I _D = 7.9 A	-	23	-	S
Dynamic ^b							
Input capacitance	C _{iss}			-	494	553	
Output capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = 20 \text{ V}, f = 1 \text{ MHz}$	-	82	99	pF
Reverse transfer capacitance	C _{rss}			-	34	46	
Total gate charge ^c	Qg			-	8.9	13	
Gate-source charge ^c	Q _{gs}	$V_{GS} = 10 \text{ V}$	$V_{DS} = 20 \text{ V}, I_{D} = 3.9 \text{ A}$	-	1.7	-	nC
Gate-drain charge ^c	Q_{gd}			-	1.4	-	
Gate resistance	Rg		f = 1 MHz	1.0	2.7	4.5	Ω
Turn-on delay time ^c	t _{d(on)}			-	7	11	
Rise time ^c	t _r		= 20 V, $R_L = 20 \Omega$	-	3	13	
Turn-off delay time ^c	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		-	14	18	ns
Fall time ^c	t _f			-	3	8.5	
Source-Drain Diode Ratings and Charact	eristics ^b						
Pulsed current ^a	I _{SM}			-	-	28	Α
Forward voltage	V _{SD}	I _F =	5.4 A, V _{GS} = 0 V	-	0.848	1.2	V
Body diode reverse recovery time	t _{rr}			-	12	24	ns
Body diode reverse recovery charge	Qrr			-	7	14	nC
Reverse recovery fall time	t _a] IF = 1.5	ο A, αι/αι = 100A/μs	-	9	-	
Reverse recovery rise time	t _b	1		-	3	-	ns
Body diode peak reverse recovery current	I _{RM(REC)}			-	-1.3	-	Α

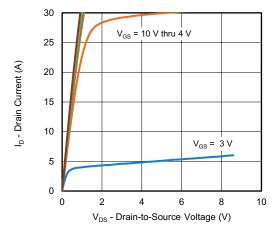
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

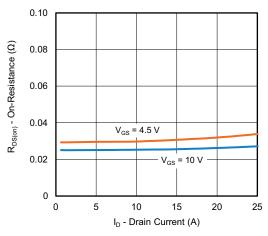
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



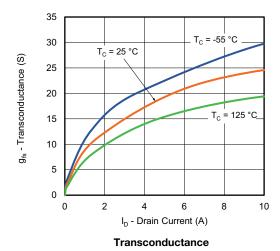
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Output Characteristics

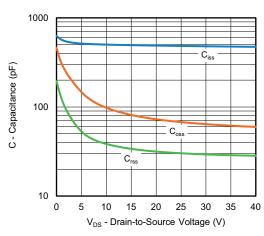


On-Resistance vs. Drain Current

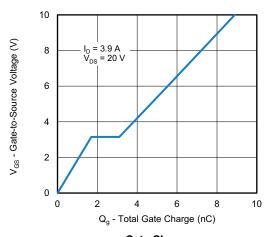


30 25 T_C = 25 °C T_C = 125 °C T_C = -55 °C 0 10 T_C = -55 °C 0 1 2 3 4 5 V_{GS} - Gate-to-Source Voltage (V)

Transfer Characteristics

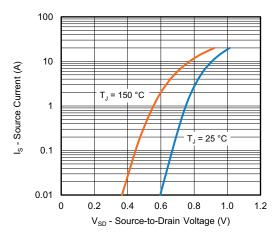


Capacitance

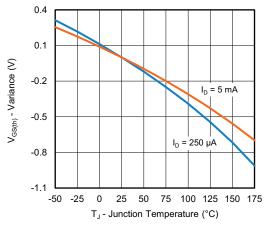




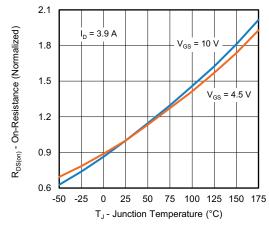
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



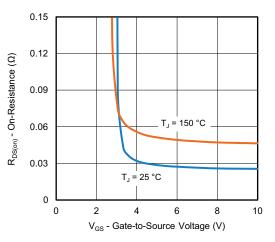
Source Drain Diode Forward Voltage



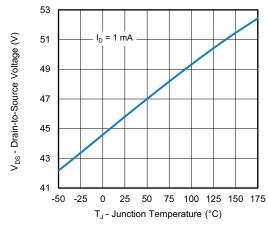
Threshold Voltage



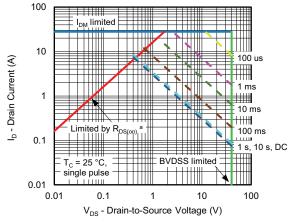
On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



Safe Operating Area

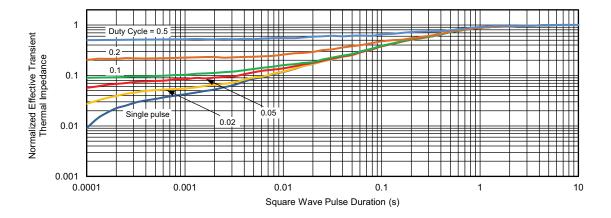
Note

a. V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

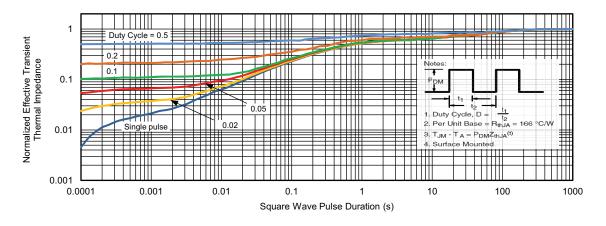
For technical questions, contact: automostech



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot



Normalized Thermal Transient Impedance, Junction-to-Ambient

Note

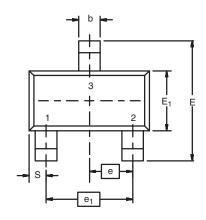
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

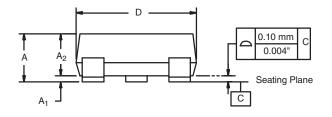
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

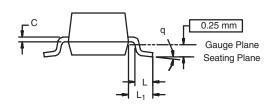
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62470.



SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	

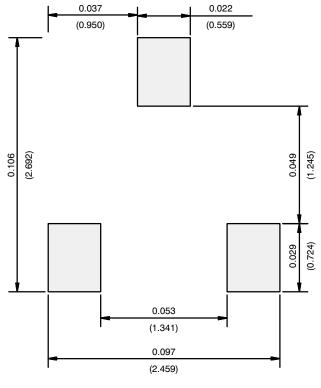
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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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