VS-6TQ035-M3, VS-6TQ040-M3, VS-6TQ045-M3

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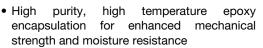
High Performance Schottky Rectifier, 6 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	6 A						
V_{R}	35 V, 40 V, 45 V						
V _F at I _F	0.53 V						
I _{RM} max.	7 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	8 mJ						
Package	TO-220AC 2L						
Circuit configuration	Single						

FEATURES

- 175 °C T_J operation
- High frequency operation
- · Low forward voltage drop





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	6	А				
V _{RRM}	Range	35 to 45	V				
I _{FSM}	t _p = 5 μs sine	690	А				
V _F	6 A _{pk} , T _J = 125 °C	0.53	V				
TJ	Range	-55 to +175	°C				

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-6TQ035-M3	VS-6TQ040-M3	VS-6TQ045-M3	UNITS				
Maximum DC reverse voltage	V_R	35	40	45	V				
Maximum working peak reverse voltage	V_{RWM}	33	40	45	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 164 °C	6	Α				
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	690	А			
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	140				
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		8	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_R$ typical		1.20	Α			



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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
Maximum forward voltage drop See fig. 1		6 A	T _{.1} = 25 °C	0.60			
	V _{FM} ⁽¹⁾	12 A	1]=25 0	0.73	V		
	VFM (*)	6 A	T _{.1} = 125 °C	0.53			
		12 A	1J = 125 C	0.64			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.8	mA		
See fig. 2	IRM ("	T _J = 125 °C	VR = nateu VR	7			
Threshold voltage	V _{F(TO)}	T - T movimum	T. T. W. C.				
Forward slope resistance	r _t	ij=ijiiiaxiiiiuiii	$T_J = T_J$ maximum		mΩ		
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal rang	400	pF			
Typical series inductance	L _S	Measured lead to lead 5 m	8	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs			

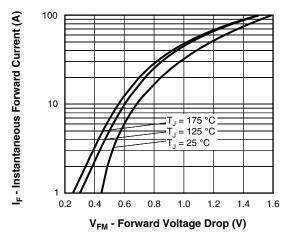
Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL TEST CONDITIONS		VALUES	UNITS					
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to +175	°C					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	2.2	°C/W					
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.50						
Approximate weight			2	g					
Approximate weight			0.07	oz.					
Mounting targue minimum			6 (5)	kgf · cm					
Mounting torque maximum			12 (10)	(lbf \cdot in)					
			6TQ035						
Marking device		Case style TO-220AC 2L	6TQ040						
			6TQ	045					

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100 T_J = 175 °C I_R - Reverse Current (mA) 10 = 150 °C = 100 °C 0.1 0.01 0.001 0.0001 15 20 25 30 35 40 0 10 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

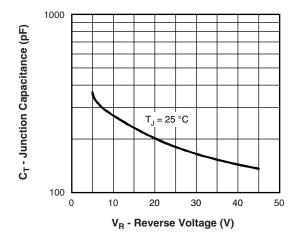


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

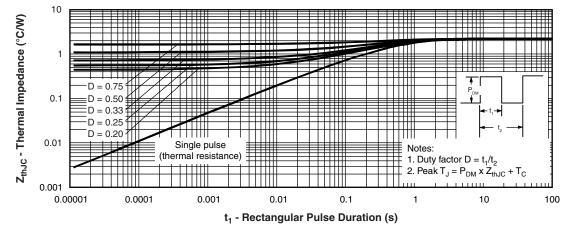


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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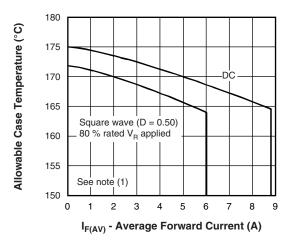


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

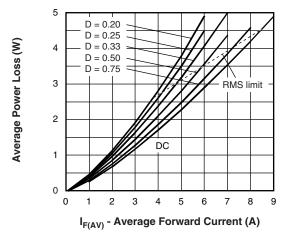


Fig. 6 - Forward Power Loss Characteristics

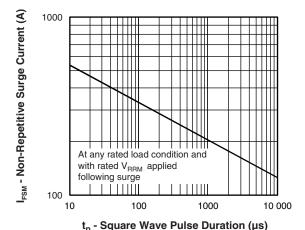


Fig. 7 - Maximum Non-Repetitive Surge Current

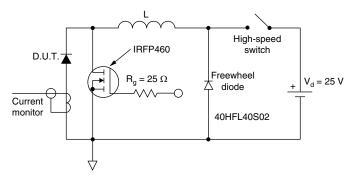


Fig. 8 - Unclamped Inductive Test Circuit

Note

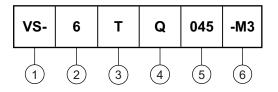
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (6 = 6 A)

3 - Package:

T = TO-220

Schottky "Q" series

035 = 35 V 040 = 40 V

Voltage ratings

045 = 45 V

6 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-6TQ035-M3	50	Antistatic plastic tubes							
VS-6TQ040-M3	50	Antistatic plastic tubes							
VS-6TQ045-M3	50	Antistatic plastic tubes							

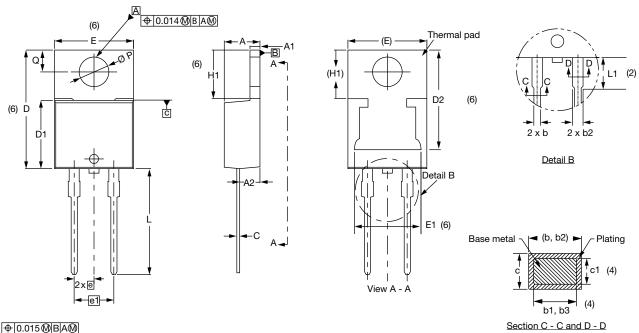
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?96156</u>							
Part marking information	www.vishay.com/doc?95391						

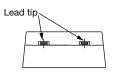


Vishay Semiconductors

TO-220AC 2L

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIMETERS		RS INCHES		NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			Е	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355				•	•			

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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