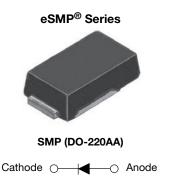


Ultrafast Rectifier, 1 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	1 A				
V_{R}	100 V, 200 V				
V _F at I _F	0.69 V				
I _{FSM}	40 A				
t _{rr} (typ.)	23 ns				
T _J max.	175 °C				
Package	SMP (DO-220AA)				
Circuit configuration	Single				

FEATURES

- · Very low profile typical height of 1.0 mm
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- Low leakage current



- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- For PFC, CRM snubber operation
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber industrial and automotive applications.

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002, meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse	VS-1ENH01-M3	M		100	
voltage	VS-1ENH02-M3	V_{RRM}		200	V
Average rectified forward	current	I _{F(AV)}	T _C = 168 °C	1	۸
Non-repetitive peak surge	current	I _{FSM}	T _J = 25 °C, 10 ms sine pulse	40	А
Operating junction and st	orage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage,	VS-1ENH01-M3	V _{BR} ,	3, 1 100		-	-	
blocking voltage	VS-1ENH02-M3	V_R^{BR} , $I_R = 100 \mu A$		200	-	-	\/
Famous valtage		V	I _F = 1 A	-	0.86	0.92	V
Forward voltage		V _F	I _F = 1 A, T _J = 150 °C	-	0.69	0.74	
Reverse leakage current		,	V _R = V _R rated	-	-	2	
		IR	T _J = 150 °C, V _R = V _R rated	-	-	20	μA
Junction capacitance		C _T	V _R = 200 V	-	8	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	23	-	
Poverse recovery time	t _{rr}	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		=	-	28	
Reverse recovery time		T _J = 25 °C	125 °C 25 °C 125 °C 125 °C 125 °C 125 °C 125 °C 125 °C	-	14	=.	- ns - A
		T _J = 125 °C		-	22	-	
Peak recovery current I _{RF}	I _{RRM}	T _J = 25 °C		-	1.7	=.	
		T _J = 125 °C		-	2.7	-	
Daversa receiver charge	0	T _J = 25 °C		-	10	-	nC
Reverse recovery charge	narge Q _{rr}	T _J = 125 °C		-	29	=.	T IIC

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction mount		R _{thJM} ⁽¹⁾	Infinite heatsink	-	7	9	°C/W
Thermal resistance, junction to ambient		R _{thJA}	PCB footprint 4.8 mm x 4.8 mm	-	107	-	C/VV
Approximate weight					0.024		g
Marking daying	VS-1ENH01-M3		Case style SMP (DO-220AA)	1H1			
Marking device	VS-1ENH02-M3		Case style SiviP (DO-220AA)	1H2			

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

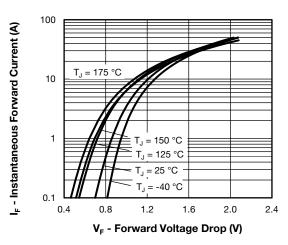


Fig. 1 - Typical Forward Voltage Drop Characteristics

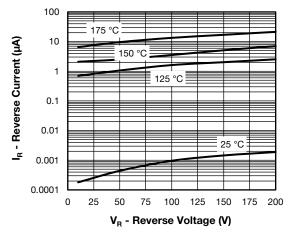


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

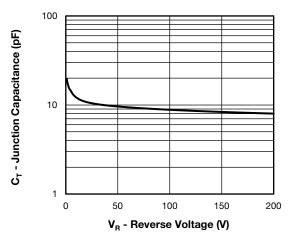


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

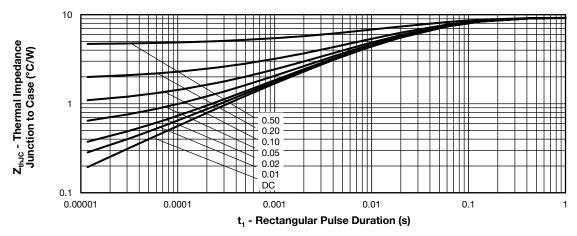


Fig. 4 - Transient Thermal Impedance, Junction to Case

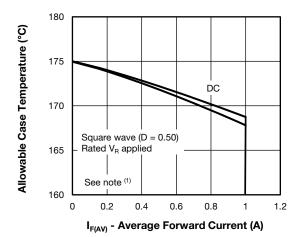


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

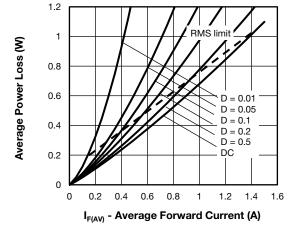
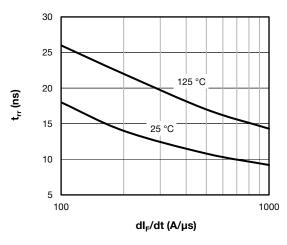


Fig. 6 - Forward Power Loss Characteristics

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5);} \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$





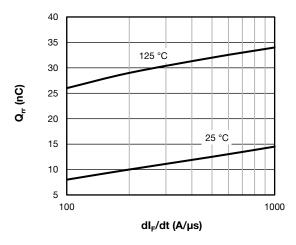
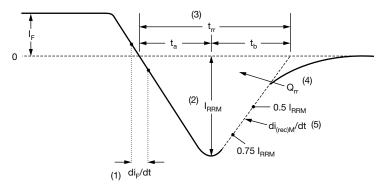


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) $\rm I_{RRM}$ peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

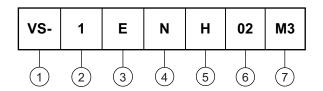
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (1 = 1 A)

Circuit configuration:

E = single diode

4 - N = SMP package

5 - Process type,

H = ultrafast recovery

6 - Voltage code (02 = 200 V)

7 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

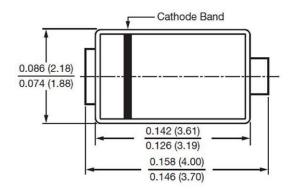
ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-1ENH01-M3/84A	84A	3000	7" diameter plastic tape and reel			
VS-1ENH01-M3/85A	85A	10 000	13" diameter plastic tape and reel			
VS-1ENH02-M3/84A	84A	3000	7" diameter plastic tape and reel			
VS-1ENH02-M3/85A	85A	10 000	13" diameter plastic tape and reel			

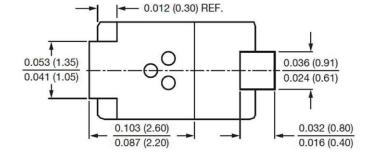
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96547</u>				
Part marking information	www.vishay.com/doc?96574			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96550			

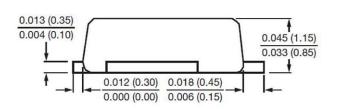


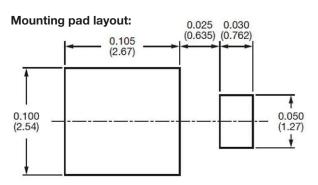
SMP (DO-220AA)

DIMENSIONS in inches (millimeters)











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Vishay

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