

Ultrafast Rectifier, 2 x 5 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 5 A			
V_{R}	600 V			
V _F at I _F	1 V			
t _{rr}	35 ns			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

FEATURES

Ultrafast recovery time, reduced Q_{rr}, and soft



RoHS

COMPLIANT

HALOGEN FREE

- 175 °C maximum operating junction temperature
- For PFC CRM / CCM, snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		V_{RRM}		600	V
Average restified forward average	per device	I _{F(AV)}	T _{solder pad} = 153 °C	10	
Average rectified forward current	per diode			5	Α
New repetitive peak arrange arrange	per device		T _J = 25 °C, 6 ms square pulse	110	A
Non-repetitive peak surge current	per diode	IFSM		60	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	$I_R = 100 \mu A$	600	-	-	
	orward voltage, per diode V _F	I _F = 5 A	-	1.2	1.5	V
Forward voltage, per diode		I _F = 5 A, T _J = 150 °C	-	1	1.25	
Deverage legisers assured to a diada			-	-	3	
Reverse leakage current, per diode I _R	T _J = 150 °C, V _R = V _R rated	-	15	150	μA	
Junction capacitance, per diode	C _T	V _R = 600 V	-	6	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50$	$A/\mu s$, $V_R = 30 V$	ı	35	-	ns - A
Poverse recovery time	+	$I_F = 0.5 A, I_R = 1 A,$	I _{rr} = 0.25 A	1	-	35	
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 5 \text{ A},$ $dI_F/dt = 500 \text{ A/}\mu\text{s},$ $V_R = 400 \text{ V}$	-	45	-	
		T _J = 125 °C		-	70	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	7	-	
	IRRM	T _J = 125 °C		-	10	-	
Deviana vaccioni cherce	ge Q _{rr}	T _J = 25 °C		-	160	-	nC
Reverse recovery charge		T _J = 125 °C		-	370	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, per diode junction to mount	R _{thJM}		-	2.4	3.3	°C/W
Approximate weight				0.55		g
Marking device		Case style SMPD (TO-263AC)		10CE	DU06	

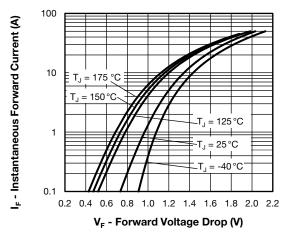


Fig. 1 - Typical Forward Voltage Drop Characteristics

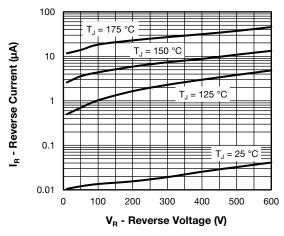


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

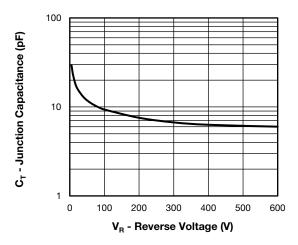


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

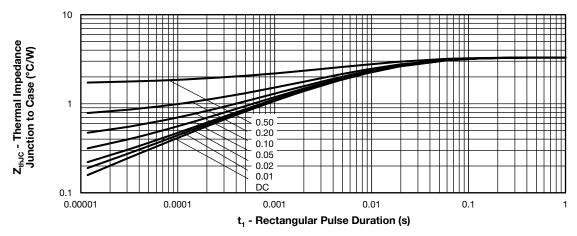


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

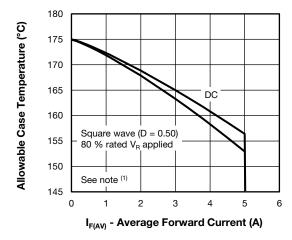


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

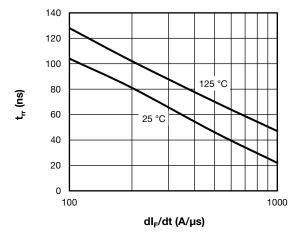


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = rated V_R$

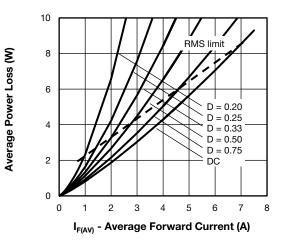


Fig. 7 - Forward Power Loss Characteristics

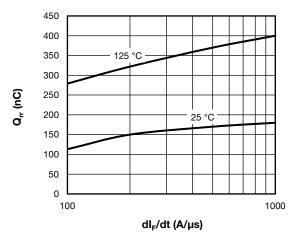
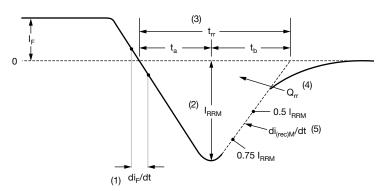


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

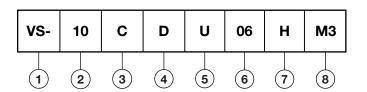
(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (10 A)

3 - Circuit configuration:

C = common cathode

4 - D = SMPD package

5 - Process type,

U = ultrafast recovery

6 - Voltage code (06 = 600 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

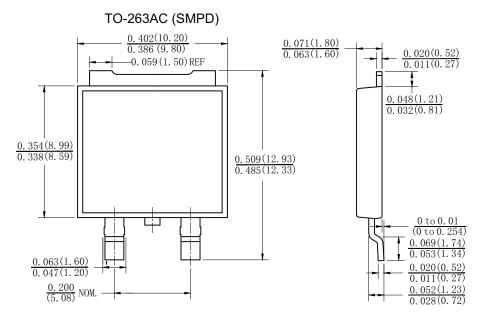
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-10CDU06HM3/I	2000	2000	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95604			
Part marking information	www.vishay.com/doc?95566			
Packaging information	www.vishay.com/doc?88869			

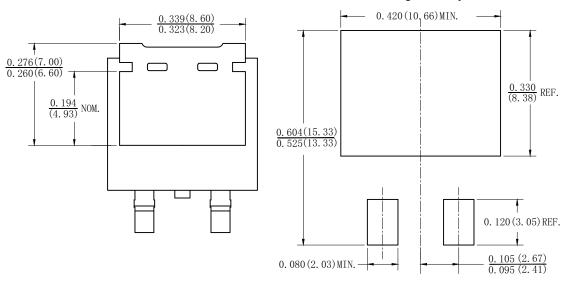


TO-263AC (SMPD)

DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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Vishay

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