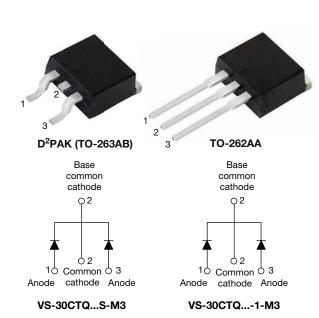
VS-30CTQ0.0S-M3, VS-30CTQ0.0-1-M3 Series

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High Performance Schottky Rectifier, 2 x 15 A



PRIMARY CHARACTERISTICS						
I _{F(AV)}	2 x 15 A					
V_{R}	50 V, 60 V					
V _F at I _F	0.56 V					
I _{RM} typ.	45 mA at 125 °C					
T _J max.	150 °C					
E _{AS}	13 mJ					
Package	D ² PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

FEATURES

- 150 °C T_J operation
- Center tap configuration
- · Very low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES UNITS								
I _{F(AV)}	Rectangular waveform	30	Α					
V _{RRM}		50/60	V					
I _{FSM}	t _p = 5 μs sine	1000	Α					
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.56	V					
TJ	Range	-55 to +150	°C					

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-30CTQ050S-M3 VS-30CTQ060S-M3 VS-30CTQ060-1-M3 UNITS							
Maximum DC reverse voltage	V_R	50	60	V			
Maximum working peak reverse voltage	V_{RWM}	50	00	V			



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ABSOLUTE MAXIMUM RATINGS									
PARAMETER	PARAMETER		TEST CONDITIONS		VALUES	UNITS			
Maximum average					30				
forward current See fig. 5	per leg	I _{F(AV)}	50 % duty cycle at T _C = 105 °C, rectangular waveform		15				
Maximum peak one cycle r	on-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	1000	А			
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	260				
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25$ °C, $I_{AS} = 1.50$ A, L = 11.5 mH		13	mJ			
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.50	А			

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
Maximum forward voltage drop per leg See fig. 1		15 A	T _{.1} = 25 °C	0.62	V		
	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.82			
	V FM (1)	15 A	T _{.1} = 125 °C	0.56			
		30 A	1J = 125 C	0.71			
Marian and a land and a summer and a summer	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	0.80	mA		
Maximum reverse leakage current per leg		T _J = 125 °C	V _R = Rated V _R	160			
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	45	mA		
Threshold voltage	V _{F(TO)}	T T mayimum		0.39	V		
Forward slope resistance	r _t	$T_J = T_J$ maximum		8.47	mΩ		
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal ran	720	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 r	8.0	nΗ			
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _R				

Note

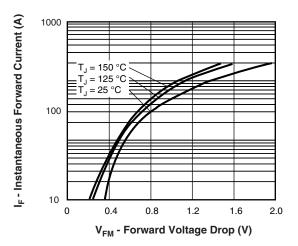
 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range)	T _J , T _{Stg}		-55 to 150	°C			
Maximum thermal resistance, junction to case per leg		Р	DC operation	3.25	°C/W			
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.63				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque maximum				12 (10)	(lbf · in)			
Marking device			Case style D ² PAK (TO-263AB)	30CT0	Q050S Q060S			
			Case style TO-262AA	30CTC				



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1000 100 150 °C I_R -Reverse Current (mA) T_J = 125 °C 10 = 100 °C = 75 °C 0.1 = 50 °C: T₁ = 25 °C 0.01 0.001 60 0 10 30 40 50 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

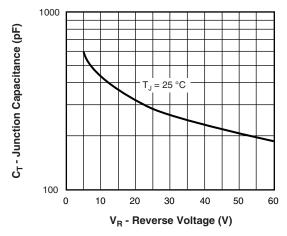


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

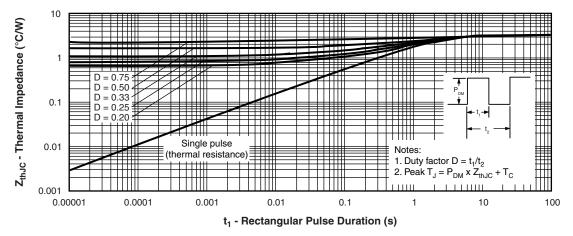


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



Allowable Case Temperature (°C)

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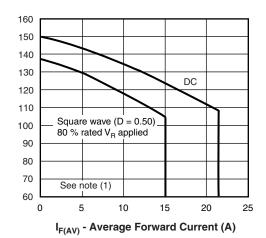


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

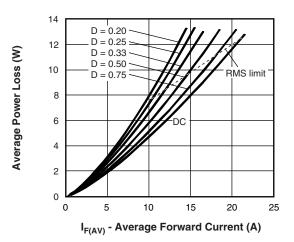


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

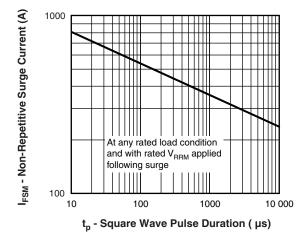


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

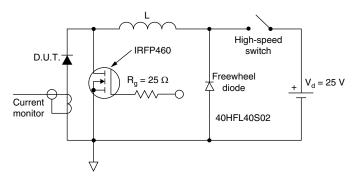


Fig. 8 - Unclamped Inductive Test Circuit

Note

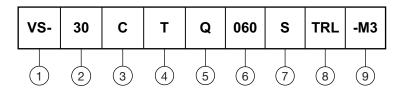
1) Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V

VS-30CTQ0.0S-M3, VS-30CTQ0.0-1-M3 Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 A)

3 - Circuit configuration: C = common cathode

- T = TO-220

- Schottky "Q" series

6 - Voltage ratings - 050 = 50 V 060 = 60 V

S = D²PAK (TO-263AB)

• -1 = TO-262AA

8 - • None = tube

• TRL = tape and reel (left oriented - for D²PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D²PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-30CTQ050S-M3	50	Antistatic plastic tubes						
VS-30CTQ050STRR-M3	800	13" diameter plastic tape and reel						
VS-30CTQ050STRL-M3	800	13" diameter plastic tape and reel						
VS-30CTQ050-1-M3	50	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS						
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164				
Dimensions	TO-262AA	www.vishay.com/doc?96165				
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444				
Part marking information	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?96424				



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



CVMPOL	SYMBOL MILLIMETER		LIMETERS INCHES		NCHES NOTES		NOTES SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

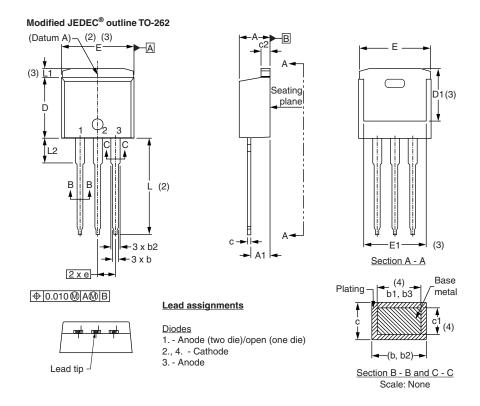
- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.10	D BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.36	3.71	0.132	0.146	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019 1 Document Number: 95419



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