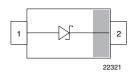


Vishay Semiconductors

Small Signal Schottky Diode





LINKS TO ADDITIONAL RESOURCES



MECHANICAL DATA

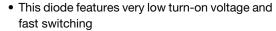
Case: SOD-523

Weight: approx. 1.4 mg

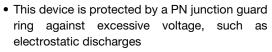
Molding compound flammability rating: UL 94 V-0 **Terminals:** high temperature soldering guaranteed:

260 °C/10 s at terminals **Packaging codes / options:**08/8K per 7" reel (8 mm tape)

FEATURES









• AEC-Q101 qualified available

• Space saving SOD-523 package

ROHS COMPLIANT HALOGEN FREE GREEN

Base P/N-G3 - RoHS-compliant, commercial grade

• Base P/N-HG3 - RoHS-compliant, AEC-Q101 qualified

 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS		
BAS40-02V	BAS40-02V-G3-08	no Single		:W	Tapo and rool		
	BAS40-02V-HG3-08	yes	Sirigle		Tape and reel		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Repetitive peak reverse voltage		V_{RRM}	40	V	
Forward continuous current		I _F	120	mA	
Surge forward current $t_p = 10 \text{ ms square wave, } T_j = 25 ^{\circ}\text{C}$ prior to surge		I _{FSM}	600	mA	
Power dissipation	on FR-4 board with recommended soldering footprint	Pt _{ot}	150	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT		
Thermal resistance junction to ambient air	on FR-4 board according to JEDEC® 51-3 with recommended soldering footprint	R _{thJA}	680	K/W		
Thermal resistance junction to lead		R_{thJL}	480	K/W		
Junction temperature		Tj	125	°C		
Operating temperature range		T _{op}	-55 to +125	°C		
Storage temperature range		T _{stg}	-55 to +150	°C		

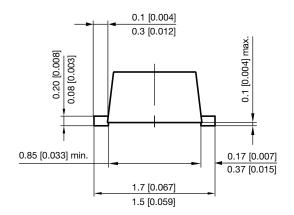
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I _R = 10 μA (pulsed)	V _(BR)	40			V
Leakage current	Pulse test $V_R = 30 \text{ V}$, $t_p < 300 \mu\text{s}$	I _R		20	100	nA
Forward voltage	Pulse test t _p < 300 μs, I _F = 1 mA	V_{F}			380	mV
Forward voltage	Pulse test $t_p < 300 \mu s$, $I_F = 40 \text{ mA}$	V_{F}			1000	mV
Diode capacitance	$V_R = 0 V, f = 1 MHz$	C_D		4	5	pF
Reverse recovery time	$I_F = 10 \text{ mA}, I_R = 10 \text{ mA}, I_{rr} = 1 \text{ mA}, R_L = 100 \Omega$	t _{rr}			5	ns

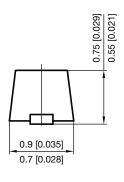


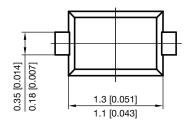
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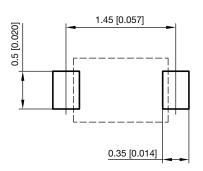
PACKAGE DIMENSIONS in millimeters [inches]: SOD-523







Footprint recommendation:



Document no.: S8-V-3880.02-003 (4) Created - Date: 04. April 2017 Rev. 4 - Date: 03. Aug. 2020 23093



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