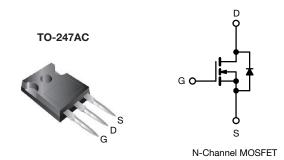


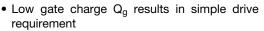
Vishay Siliconix

# **Power MOSFET**



PRODUCT SUMMARY				
V <sub>DS</sub> (V)	600			
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V 0.24			
Q <sub>g</sub> (max.) (nC)	150			
Q <sub>gs</sub> (nC)	45			
Q <sub>gd</sub> (nC)	76			
Configuration	Single			

### **FEATURES**





Improved gate, avalanche and dynamic dV/dt ruggedness

- RoHS'
- Fully characterized capacitance and avalanche voltage and current
- Enhanced body diode dV/dt capability
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

## **BENEFITS**

- · Hard switching primary or PFS switch
- Switch mode power supply (SMPS)
- Uninterruptable power supply
- · High speed power switching
- Motor drive

ORDERING INFORMATION		
Package	TO-247AC	
Lead (Pb)-free	IRFP22N60KPbF	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER			SYMBOL	LIMIT	UNIT
Drain-source voltage			$V_{DS}$	600	V
Gate-source voltage			$V_{GS}$	± 30	<b> </b>
Continuous drain current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 25 °C		22	
Continuous drain current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	14	Α
Pulsed drain current <sup>a</sup>			I <sub>DM</sub>	88	
Linear derating factor				2.9	W/°C
Single pulse avalanche energy b			E <sub>AS</sub>	380	mJ
Repetitive avalanche current a			I <sub>AR</sub>	22	А
Repetitive avalanche energy a			E <sub>AR</sub>	37	mJ
Maximum power dissipation	T <sub>C</sub> = 25 °C		$P_D$	370	W
Peak diode recovery dV/dt c			dV/dt	15	V/ns
Operating junction and storage temperature range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	- °C
Soldering recommendations (peak temperature) <sup>d</sup>	for 10 s			300	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Starting  $T_J$  = 25 °C, L = 1.5 mH,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 22 A (see fig. 12)
- c.  $I_{SD} \le 22$  A,  $dI/dt \le 360$  A/µs,  $V_{DD} \le V_{DS}$ ,  $T_{J} \le 150$  °C
- d. 1.6 mm from case

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THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R <sub>thJA</sub>	-	40	
Case-to-sink, flat, greased surface	R <sub>thCS</sub>	0.24	=	°C/W
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	0.34	

PARAMETER	SYMBOL	TES	ST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							•
Drain-source breakdown voltage	$V_{DS}$	V <sub>GS</sub> = 0 V, I <sub>D</sub> :	= 250 μA	600	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference to	25 °C, I <sub>D</sub> = 1 mA <sup>d</sup>	-	0.30	-	V/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D}$	= 250 µA	3.0	-	5.0	V
Gate-source leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ± 30 V		-	-	± 100	nA
Zana mata waltana aluain ayumant		$V_{DS} = 600 \text{ V},$	V <sub>GS</sub> = 0 V	1	-	50	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 480 \text{ V},$	V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	250	μA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 13 A <sup>b</sup>	1	0.240	0.280	Ω
Forward transconductance	9 <sub>fs</sub>	$V_{DS} = 50 \text{ V}, I_{D}$	= 13 A b	11	-	-	S
Dynamic							•
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0 V$ ,		-	3570	-	
Output capacitance	C <sub>oss</sub>	$V_{DS} = 25 V$ ,		-	350	-	1
Reverse transfer capacitance	C <sub>rss</sub>	f = 1.0 MHz, see fig. 5		-	36	-	- pF
Outside seasonites		$V_{DS} = 1.0 \text{ V}, f = 1.0 \text{ MHz}$ $V_{DS} = 480 \text{ V}, f = 1.0 \text{ MHz}$		1	4710	-	
Output capacitance	$C_{oss}$			-	92	-	
Effective output capacitance	C <sub>oss</sub> eff.	V <sub>DS</sub> = 0 V to 480 V	-	180	-		
Total gate charge	Qg			1	-	150	nC
Gate-source charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$I_D = 22 \text{ A}, V_{DS} = 480 \text{ V}$ see fig. 6 and 13 b	1	-	45	
Gate-drain charge	$Q_{gd}$	See fig. 6 and 16		-	-	76	
Turn-on delay time	t <sub>d(on)</sub>	$V_{DD} = 300 \text{ V}, I_D = 22 \text{ A}, \\ R_g = 6.2, V_{GS} = 10 \text{ V}, \\ \text{see fig. } 10^{\text{ b}}$		1	26	-	ns
Rise time	t <sub>r</sub>			-	99	-	
Turn-off delay time	t <sub>d(off)</sub>			-	48	-	
Fall time	t <sub>f</sub>			-	37	-	
Drain-Source Body Diode Characteristic	cs						•
Continuous source-drain diode current	I <sub>S</sub>	MOSFET sym	bol	-	-	22	
Pulsed diode forward current <sup>a</sup>	I <sub>SM</sub>	showing the integral revers p - n junction	g   1   1   1	-	-	88	А
Body diode voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C, I <sub>S</sub>	= 22 A, V <sub>GS</sub> = 0 V <sup>b</sup>	-	-	1.5	V
		T <sub>J</sub> = 25 °C	-	590	890		
Body diode reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C			670	1010	ns
	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	dl/dt = 100 A/µs b	-	7.2	11	
Body diode reverse recovery charge		T <sub>J</sub> =1 25 °C	1	-	8.5	13	μC
Reverse recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	1	-	26	39	
Forward turn-on time	t <sub>on</sub>	-	on time is negligible (turn-on	is domina	ated by I	and Ln	1

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %
- c.  $C_{oss}$  eff. is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DS}$



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

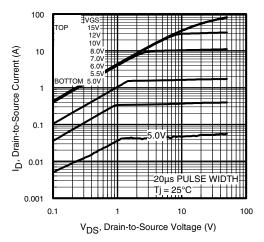


Fig. 1 - Typical Output Characteristics

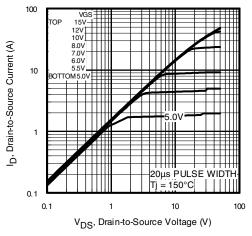


Fig. 2 - Typical Output Characteristics

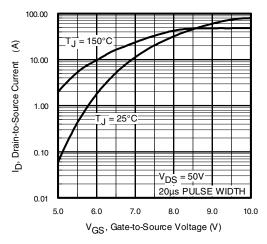


Fig. 3 - Typical Transfer Characteristics

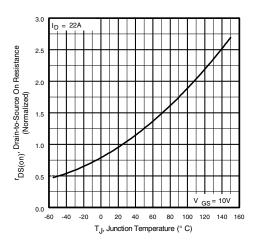


Fig. 4 - Normalized On-Resistance vs. Temperature

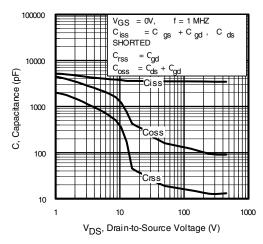


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

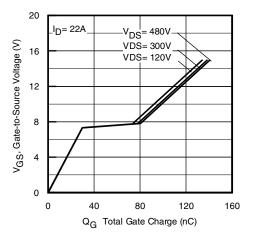


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



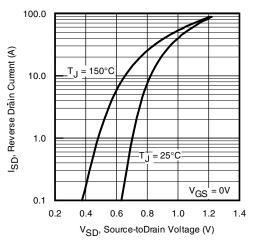


Fig. 7 - Typical Source-Drain Diode Forward Voltage

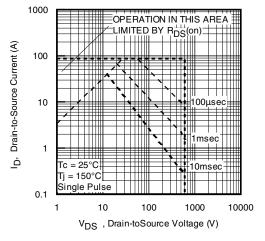


Fig. 8 - Maximum Safe Operating Area

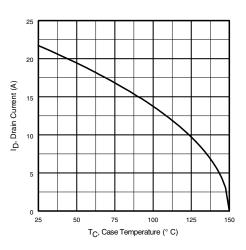


Fig. 9 - Maximum Drain Current vs. Case Temperature

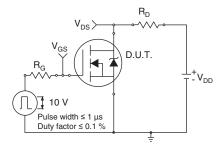


Fig. 10a - Switching Time Test Circuit

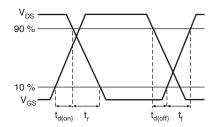


Fig. 10b - Switching Time Waveforms

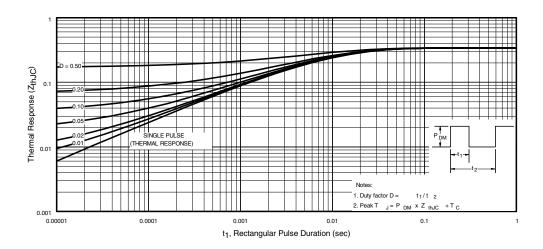
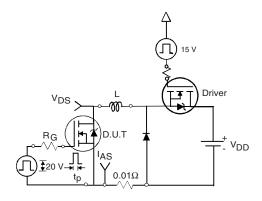




Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



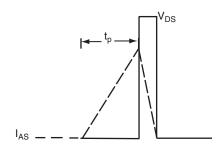


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

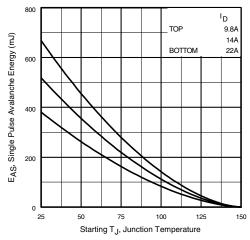


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

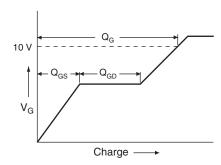


Fig. 13a - Basic Gate Charge Waveform

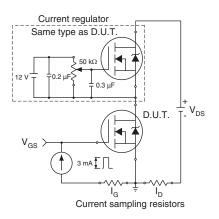
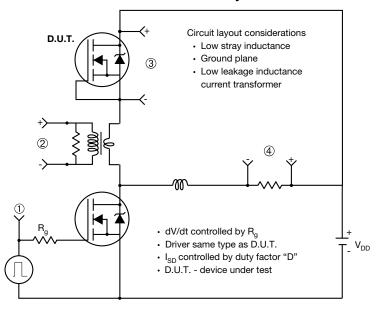


Fig. 13b - Gate Charge Test Circuit



## Peak Diode Recovery dV/dt Test Circuit



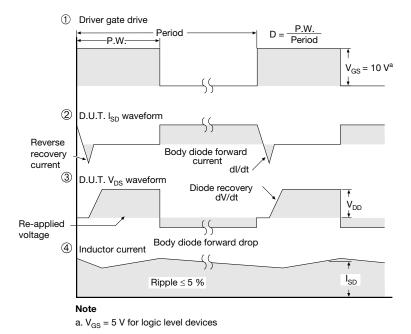


Fig. 14 - For N-Channel

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# **TO-247AC (High Voltage)**

### **VERSION 1: FACILITY CODE = 9**







Section C--C,D--D,E--E

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
Α	4.83	5.21	
A1	2.29	2.55	
A2	1.50	2.49	
b	1.12	1.33	
b1	1.12	1.28	
b2	1.91	2.39	6
b3	1.91	2.34	
b4	2.87	3.22	6, 8
b5	2.87	3.18	
С	0.55	0.69	6
c1	0.55	0.65	
D	20.40	20.70	4

	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
D1	16.25	16.85	5
D2	0.56	0.76	
E	15.50	15.87	4
E1	13.46	14.16	5
E2	4.52	5.49	3
е	5.44	BSC	
L	14.90	15.40	
L1	3.96	4.16	6
ØΡ	3.56	3.65	7
Ø P1	7.19 ref.		
Q	5.31	5.69	
S	5.54	5.74	

- (1) Package reference: JEDEC® TO247, variation AC
- (2) All dimensions are in mm
- (3) Slot required, notch may be rounded
- (4) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- (7) Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition

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### **VERSION 2: FACILITY CODE = Y**



	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
Α	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

	MILLIN		
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
E	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.2	254	
L	14.20	16.25	
L1	3.71	4.29	
ØР	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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## **VERSION 3: FACILITY CODE = N**



	MILLIM	IETERS
DIM.	MIN.	MAX.
Α	4.65	5.31
A1	2.21	2.59
A2	1.17	1.37
b	0.99	1.40
b1	0.99	1.35
b2	1.65	2.39
b3	1.65	2.34
b4	2.59	3.43
b5	2.59	3.38
С	0.38	0.89
c1	0.38	0.84
D	19.71	20.70
D1	13.08	-

	MILLIMETERS		
DIM.	MIN.	MAX.	
D2	0.51	1.35	
E	15.29	15.87	
E1	13.46	-	
е	5.46	BSC	
k	0.254		
L	14.20	16.10	
L1	3.71	4.29	
N	7.62	BSC	
Р	3.56	3.66	
P1	=	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

ECN: E20-0545-Rev. F, 19-Oct-2020

DWG: 5971

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



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