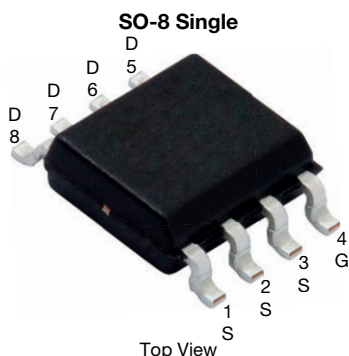


Automotive N-Channel 150 V (D-S) 175 °C MOSFET

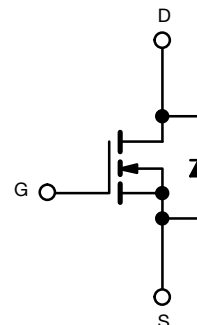


FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE



N-Channel MOSFET

PRODUCT SUMMARY

V _{DS} (V)	150
R _{DS(on)} (Ω) at V _{GS} = 10 V	0.085
I _D (A)	19
Configuration	Single
Package	SO-8

ORDERING INFORMATION

Package	SO-8
Lead (Pb)-free and halogen-free	SQ4080EY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V _{DS}	150	V
Gate-source voltage	V _{GS}	± 20	
Continuous drain current	I _D	18	A
		10	
Continuous source current (diode conduction)	I _S	6.5	
Pulsed drain current ^a	I _{DM}	72	
Single pulse avalanche current	I _{AS}	20	
Single pulse avalanche energy	E _{AS}	20	mJ
Maximum power dissipation ^a	P _D	7.1	W
		2.4	
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction to ambient	R _{thJA}	85	°C/W
Junction to case (drain)	R _{thJF}	25	

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
b. When mounted on 1" square PCB (FR4 material)



SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		150	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		2.5	3	4	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 150 V	-	-	1	μA
		V _{GS} = 0 V	V _{DS} = 150 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 150 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	30	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 10 A	-	0.070	0.085	Ω
		V _{GS} = 10 V	I _D = 10 A, T _C = 125 °C	-	-	0.167	
		V _{GS} = 10 V	I _D = 10 A, T _C = 175 °C	-	-	0.220	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 10 A		-	51	-	S
Dynamic ^b							
Input Capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 75 V, f = 1 MHz	-	1100	1590	pF
Output Capacitance	C _{oss}			-	75	130	
Reverse Transfer Capacitance	C _{rss}			-	33	55	
Total Gate Charge ^c	Q _g	V _{GS} = 10 V	V _{DS} = 75 V, I _D = 5 A	-	25	33	nC
Gate-Source Charge ^c	Q _{gs}			-	5.2	-	
Gate-Drain Charge ^c	Q _{gd}			-	8.7	-	
Gate Resistance	R _g	f = 1 MHz		0.2	0.4	1	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 20 V, R _L = 20 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _g = 6 Ω		-	11.4	15	ns
Rise Time ^c	t _r			-	3.2	4.5	
Turn-Off Delay Time ^c	t _{d(off)}			-	19.1	25	
Fall Time ^c	t _f			-	2.5	3.2	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	72	A
Forward Voltage	V _{SD}	I _F = 3.5 A, V _{GS} = 0		-	0.75	1.1	V

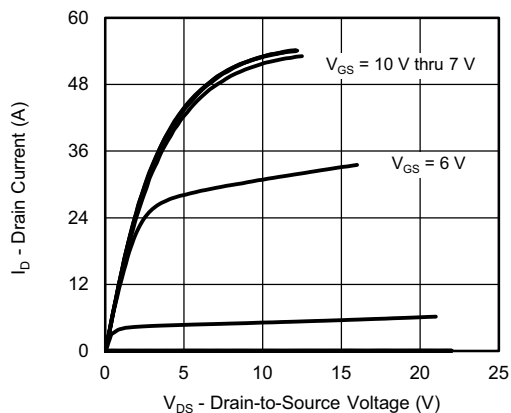
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

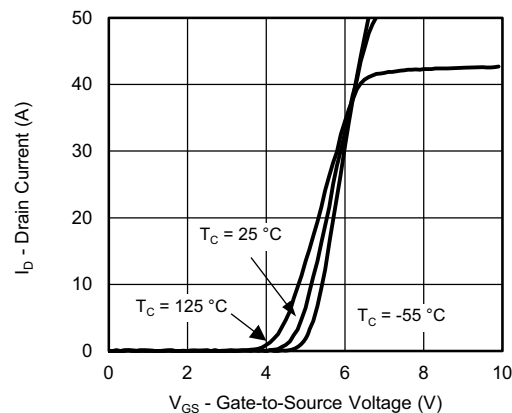
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



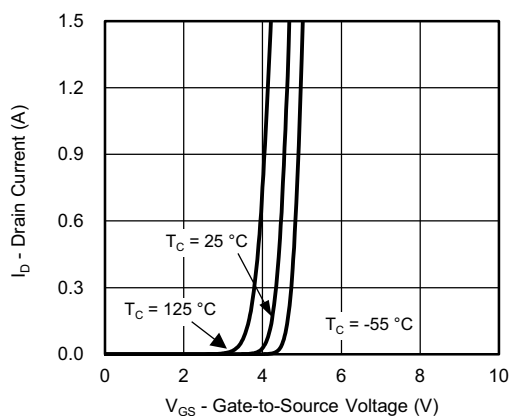
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



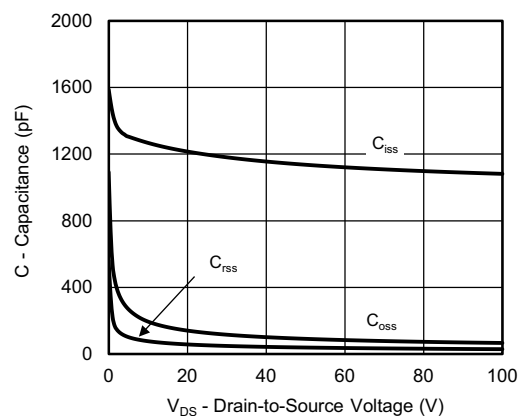
Output Characteristics



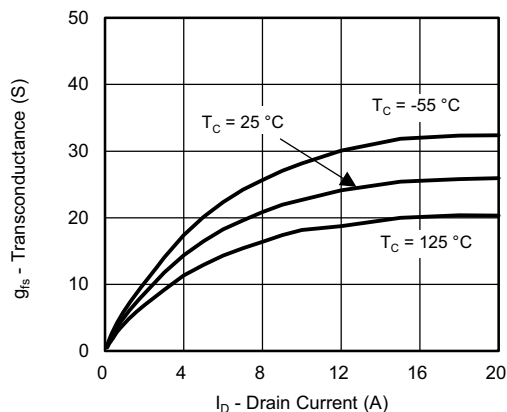
Transfer Characteristics



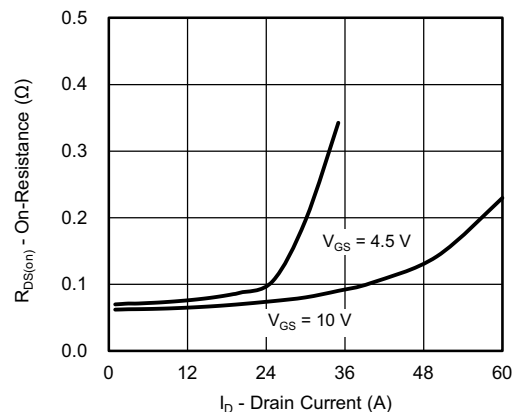
Transfer Characteristics



Capacitance



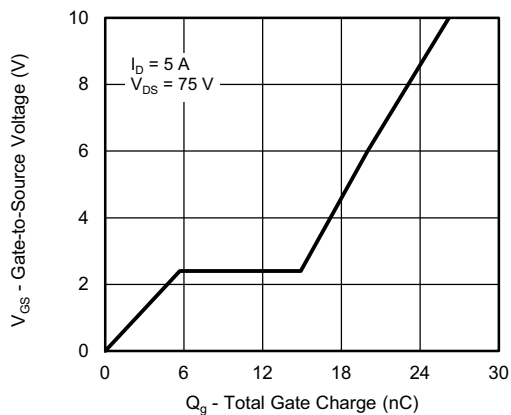
Transconductance



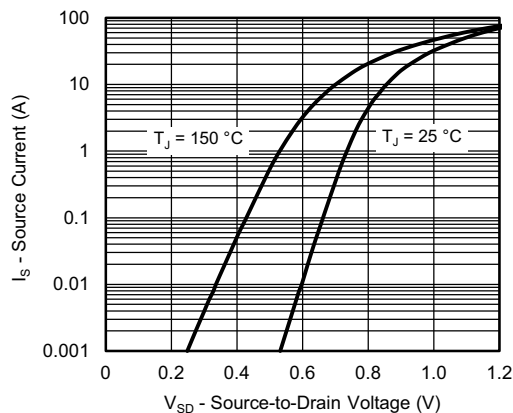
On-Resistance vs. Drain Current



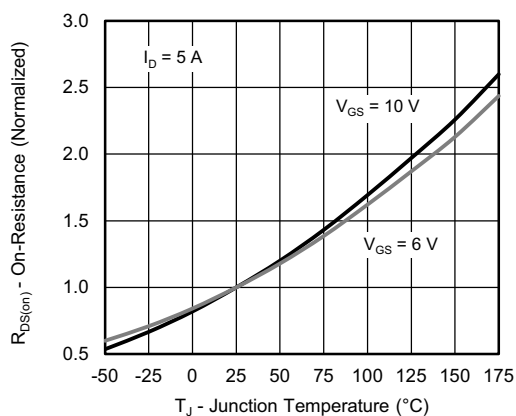
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



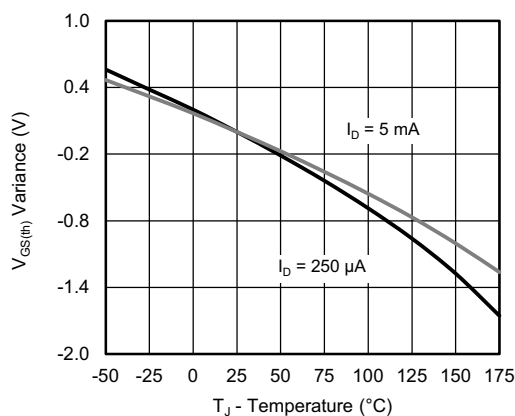
Gate Charge



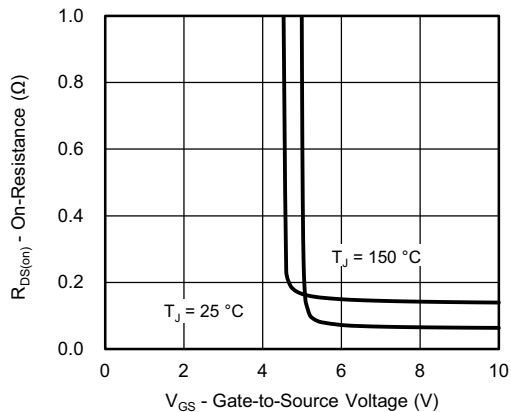
Source Drain Diode Forward Voltage



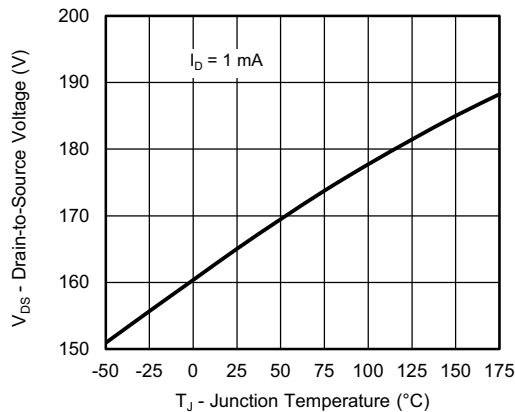
On-Resistance vs. Junction Temperature



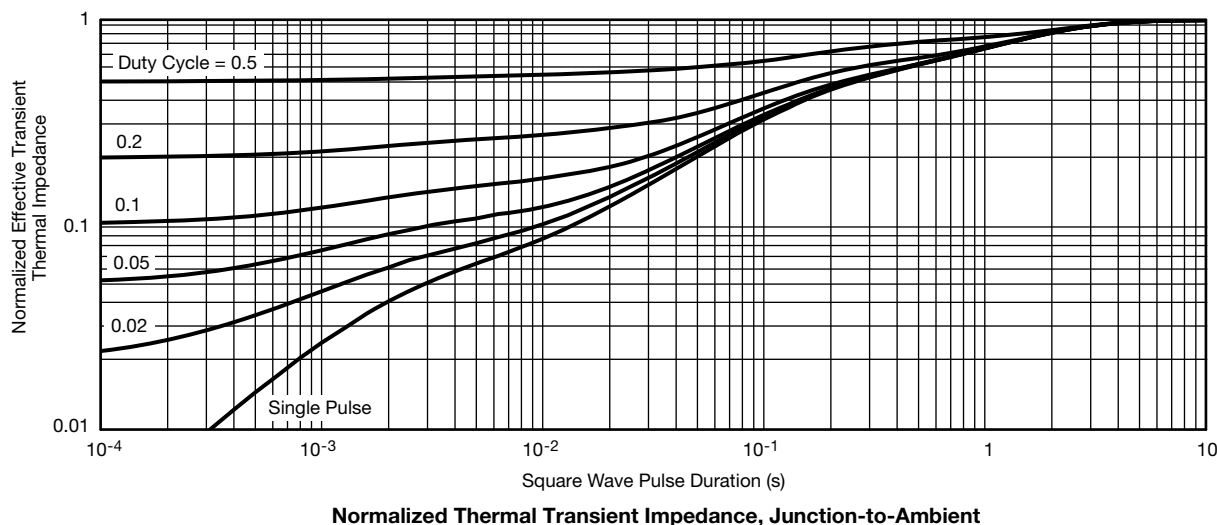
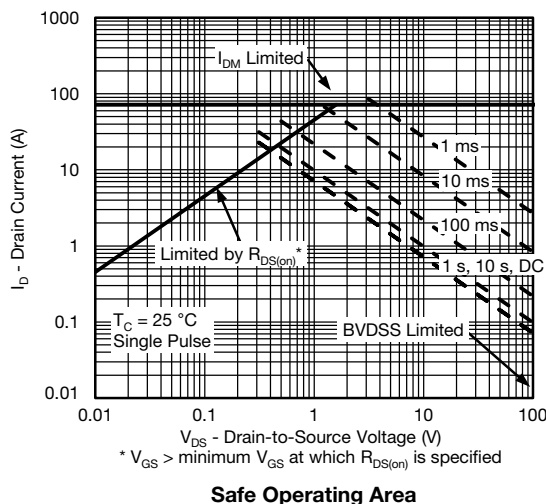
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

**THERMAL RATINGS** ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)**Note**

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction-to-Foot ($25\text{ }^{\circ}\text{C}$)
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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