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Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

High Voltage, Input Rectifier Diode, 10 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	10 A						
V_{R}	800 V to 1200 V						
V _F at I _F	1.1 V						
I _{FSM}	160 A						
T _J max.	150 °C						
Package	TO-220AC 2L						
Circuit configuration	Single						

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- · Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS									
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS						
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	12.0	16.0	А						

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I _{F(AV)}	Sinusoidal waveform	10	A							
V _{RRM}		800/1200	V							
I _{FSM}		160	A							
V _F	10 A, T _J = 25 °C	1.1	V							
TJ		-40 to +150	°C							

VOLTAGE RATINGS									
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA						
VS-10ETS08-M3	800	900	0.5						
VS-10ETS12-M3	1200	1300	0.5						

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180° conduction half sine wave	10						
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	135	Α					
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	160						
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	91	A ² s					
waximum i-t for fusing		10 ms sine pulse, no voltage reapplied 130		A 5					
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	1300	A²√s					



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ELECTRICAL SPECIFICATIONS								
PARAMETER	VALUES	UNITS						
Maximum forward voltage drop	V_{FM}	10 A, T _J = 25 °C	1.1	V				
Forward slope resistance	r _t	T _{.1} = 150 °C	20	mΩ				
Threshold voltage	V _{F(TO)}	1j = 150 C	0.82	V				
Maximum reverse leakage current	1	$T_J = 25 ^{\circ}\text{C}$ $V_B = \text{Rated } V_{BBM}$		0.05	mA			
Maximum reverse leakage current	IRM	T _J = 150 °C	VR = nated VRRM	0.50	IIIA			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5					
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA}		62	°C/W				
Soldering temperature	T _S		240	°C				
Approximate weight			2	g				
Approximate weight			0.07	OZ.				
Marking device		O	10ETS08					
ividikilig device		Case style TO-220AC 2L	10ETS12					

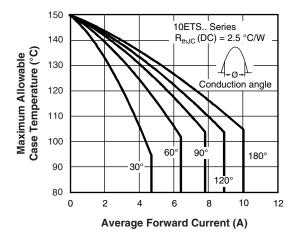


Fig. 1 - Current Rating Characteristics

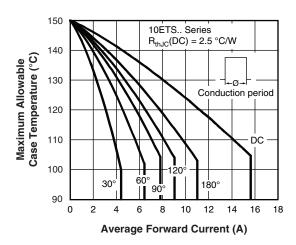


Fig. 2 - Current Rating Characteristics

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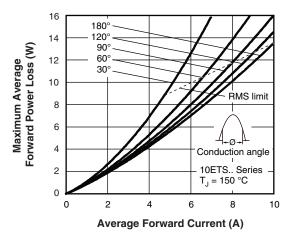
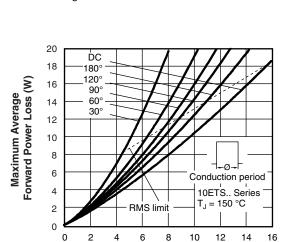


Fig. 3 - Forward Power Loss Characteristics



Average Forward Current (A) Fig. 4 - Forward Power Loss Characteristics

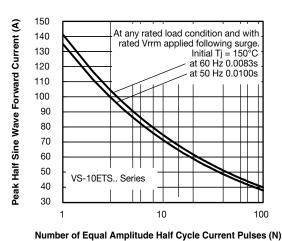


Fig. 5 - Maximum Non-Repetitive Surge Current

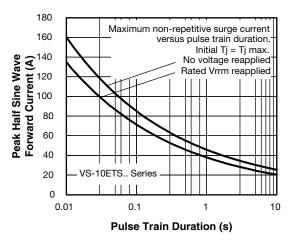


Fig. 6 - Maximum Non-Repetitive Surge Current

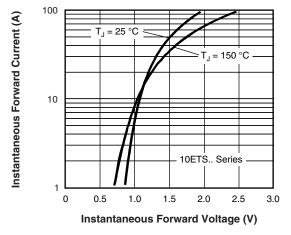


Fig. 7 - Forward Voltage Drop Characteristics

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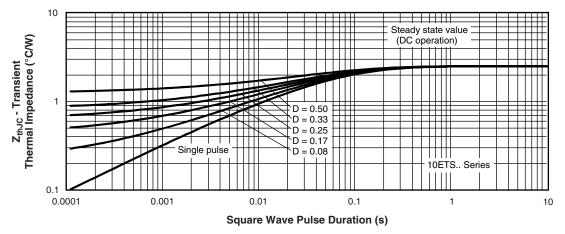
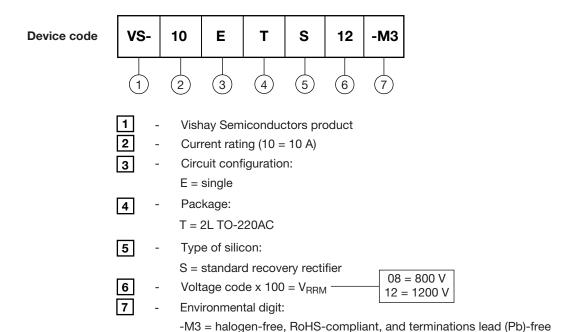


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)								
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-10ETS08-M3	50	Antistatic plastic tubes						
VS-10ETS12-M3	50	Antistatic plastic tubes						

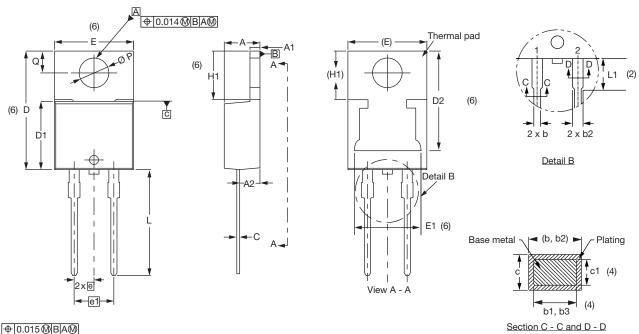
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96156					
Part marking information	www.vishay.com/doc?95391					

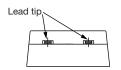


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2L TO-220AC

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355				•	•		•	

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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