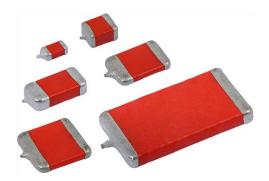


Solid Tantalum Chip Capacitors TANTAMOUNT™, Low Profile, Conformal Coated

Application Specific Pulse Capacitor for Wireless Modems



LINKS TO ADDITIONAL RESOURCES







FEATURES

- Robust design for use in wireless modem applications
- Designed specifically for pulsed operation
- 100 % surge current tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



ROHS
COMPLIANT
HALOGEN
FREE
GREEN

(5-2008)

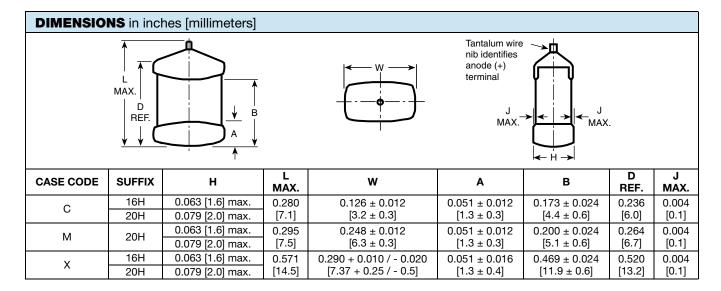
PERFORMANCE CHARACTERISTICS

Operating Temperature: -55 °C to +125 °C (above 40 °C, voltage derating is required) Capacitance Tolerance: \pm 20 % standard Capacitance Range: 330 μ F to 2200 μ F Voltage Rating: 6.3 V_{DC} to 10 V_{DC} Moisture Sensitivity Level 2a

592W	757	X0	010	M	2	Т	20H
TYPE	CAPACITANCE	CAPACITANCE TOLERANCE	DC VOLTAGE RATING AT +85 °C	CASE CODE	TERMINATION	REEL SIZE AND PACKAGING	SUFFIX
	This is expressed in picofarads. The first two digits are the significant figures. The third is the number of zeros to follow.	X0 = ± 20 %	This is expressed in volts. To complete the three-digit block, zeros precede the voltage rating. A decimal point is indicated by an "R" (6R3 = 6.3 V).	See Ratings and Case Codes table	2 = 100 % tin	T = 7" [178 mm] reel	Maximum height (mm see dimensions

Note

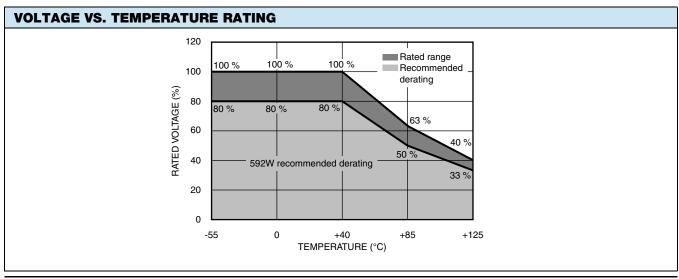
Preferred tolerance and reel sizes are in bold.
 We reserve the right to supply higher voltage ratings and tighter capacitance tolerance capacitors in the same case size



RATINGS AND CASE CODES					
μF	6.3 V	8.2 V	10 V		
330			C_2.0		
470	C_1.6	C_2.0			
680					
750			M_2.0		
1000			X_2.0		
2200	X_1.6				

STANDARD R	ATINGS					
CAPACITANCE (μF)	CASE CODE	PART NUMBER	MAX. HEIGHT (mm)	MAX. DCL AT +25 °C (μA)	MAX. DF AT +25 °C 120 Hz (%)	MAX. ESR AT +25 °C 100 kHz (Ω)
		6.3 V _{DC} AT +40 °C, 4.0	V _{DC} AT +85 °C; 2.5	V _{DC} AT +125 °C		
470	С	592W477X06R3C2T16H	1.6	30	14	0.200
2200	Χ	592W228X06R3X2T16H	1.6	139	45	0.070
		8.2 V _{DC} AT +40 °C; 5.2	2 V _{DC} AT +85 °C, 3.3	V _{DC} AT +125 °C		
470	С	592W477X08R2C2T20H	2.0	57	20	0.100
		10 V _{DC} AT +40 °C; 6.3	V _{DC} AT +85 °C, 4.0	V _{DC} AT +125 °C		
330	С	592W337X0010C2T20H	2.0	33	20	0.100
750	М	592W757X0010M2T20H	2.0	75	35	0.100
1000	X	592W108X0010X2T20H	2.0	100	35	0.080

ELECTRICAL PERFORMANCE CHARACTERISTICS							
ITEM	PERFORMANCE (CHARACTERISTICS					
Category temperature range	-55 °C to +125 °C	(with voltage derating)					
Capacitance tolerance	± 20 %, ± 10 % (at	t 120 Hz) 2 V _{RMS} at +25 °	C using a capacit	ance bridge			
Dissipation factor (at 120 Hz)	Limits per Standar	d Ratings table. Tested vi	ia bridge method	, at 25 °C, 120 F	łz		
ESR (100 kHz)	Limits per Standar	d Ratings table. Tested vi	ia bridge method	, at 25 °C, 100 k	Hz		
Leakage current		After application of RV applied to capacitors for 5 min using a steady source of power with 1 k Ω resistor in series with the capacitor under test, leakage current at 25 °C is not more than described in.					
	Rated voltage - 55 °C / + 40 °C 10 V 8.2 V 6.3 V 4.0 V						
Operation temperatures	Category voltage	+ 40 °C / + 85 °C	6.3 V	5.2 V	4.0 V	2.5 V	
	Category voltage	+ 85 °C / + 125 °C	4 V	3.3 V	2.5 V	1.6 V	





POWER DISSIP	POWER DISSIPATION					
CASE CODE HEIGHT MAXIMUM PERMISSIBLE POWER DISSIPATION AT +25 °C (W) IN FREE						
С	16H	0.100				
С	20H	0.110				
М	20H	0.175				
Х	16H	0.170				
Х	20H	0.175				

STANDARD PACKAGING QUANTITY					
CASE CODE	HEIGHT	UNITS PER REEL, 7" REEL			
С	Any	1000			
M	Any	1000			
X	Any	500			

PRODUCT INFORMATION				
Conformal Coated Guide				
Pad Dimensions	www.vishay.com/doc?40150			
Packaging Dimensions				
Moisture Sensitivity (MSL)	www.vishay.com/doc?40135			
SELECTOR GUIDES				
Solid Tantalum Selector Guide	www.vishay.com/doc?49053			
Solid Tantalum Chip Capacitors	www.vishay.com/doc?40091			
FAQ				
Frequently Asked Questions	www.vishay.com/doc?40110			

Guide for Conformal Coated Tantalum Capacitors

INTRODUCTION

Tantalum electrolytic capacitors are the preferred choice in applications where volumetric efficiency, stable electrical parameters, high reliability, and long service life are primary considerations. The stability and resistance to elevated temperatures of the tantalum / tantalum oxide / manganese dioxide system make solid tantalum capacitors an appropriate choice for today's surface mount assembly technology.

Vishay Sprague has been a pioneer and leader in this field, producing a large variety of tantalum capacitor types for consumer, industrial, automotive, military, and aerospace electronic applications.

Tantalum is not found in its pure state. Rather, it is commonly found in a number of oxide minerals, often in combination with Columbium ore. This combination is known as "tantalite" when its contents are more than one-half tantalum. Important sources of tantalite include Australia, Brazil, Canada, China, and several African countries. Synthetic tantalite concentrates produced from tin slags in Thailand, Malaysia, and Brazil are also a significant raw material for tantalum production.

Electronic applications, and particularly capacitors, consume the largest share of world tantalum production. Other important applications for tantalum include cutting tools (tantalum carbide), high temperature super alloys, chemical processing equipment, medical implants, and military ordnance.

Vishay Sprague is a major user of tantalum materials in the form of powder and wire for capacitor elements and rod and sheet for high temperature vacuum processing.

THE BASICS OF TANTALUM CAPACITORS

Most metals form crystalline oxides which are non-protecting, such as rust on iron or black oxide on copper. A few metals form dense, stable, tightly adhering, electrically insulating oxides. These are the so-called "valve" metals and include titanium, zirconium, niobium, tantalum, hafnium, and aluminum. Only a few of these permit the accurate control of oxide thickness by electrochemical means. Of these, the most valuable for the electronics industry are aluminum and tantalum.

Capacitors are basic to all kinds of electrical equipment, from radios and television sets to missile controls and automobile ignitions. Their function is to store an electrical charge for later use.

Capacitors consist of two conducting surfaces, usually metal plates, whose function is to conduct electricity. They are separated by an insulating material or dielectric. The dielectric used in all tantalum electrolytic capacitors is tantalum pentoxide.

Tantalum pentoxide compound possesses high-dielectric strength and a high-dielectric constant. As capacitors are being manufactured, a film of tantalum pentoxide is applied to their electrodes by means of an electrolytic process. The film is applied in various thicknesses and at various voltages and although transparent to begin with, it takes on different colors as light refracts through it. This coloring occurs on the tantalum electrodes of all types of tantalum capacitors.

Rating for rating, tantalum capacitors tend to have as much as three times better capacitance / volume efficiency than aluminum electrolytic capacitors. An approximation of the capacitance / volume efficiency of other types of capacitors may be inferred from the following table, which shows the dielectric constant ranges of the various materials used in each type. Note that tantalum pentoxide has a dielectric constant of 26, some three times greater than that of aluminum oxide. This, in addition to the fact that extremely thin films can be deposited during the electrolytic process mentioned earlier, makes the tantalum capacitor extremely efficient with respect to the number of microfarads available per unit volume. The capacitance of any capacitor is determined by the surface area of the two conducting plates, the distance between the plates, and the dielectric constant of the insulating material between the plates.

COMPARISON OF CAPACITOR DIELECTRIC CONSTANTS				
DIELECTRIC	e DIELECTRIC CONSTANT			
Air or vacuum	1.0			
Paper	2.0 to 6.0			
Plastic	2.1 to 6.0			
Mineral oil	2.2 to 2.3			
Silicone oil	2.7 to 2.8			
Quartz	3.8 to 4.4			
Glass	4.8 to 8.0			
Porcelain	5.1 to 5.9			
Mica	5.4 to 8.7			
Aluminum oxide	8.4			
Tantalum pentoxide	26			
Ceramic	12 to 400K			

In the tantalum electrolytic capacitor, the distance between the plates is very small since it is only the thickness of the tantalum pentoxide film. As the dielectric constant of the tantalum pentoxide is high, the capacitance of a tantalum capacitor is high if the area of the plates is large:

$$C = \frac{eA}{t}$$

where

C = capacitance

e = dielectric constant

A = surface area of the dielectric

t = thickness of the dielectric

Tantalum capacitors contain either liquid or solid electrolytes. In solid electrolyte capacitors, a dry material (manganese dioxide) forms the cathode plate. A tantalum lead is embedded in or welded to the pellet, which is in turn connected to a termination or lead wire. The drawings show the construction details of the surface mount types of tantalum capacitors shown in this catalog.



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SOLID ELECTROLYTE TANTALUM CAPACITORS

Solid electrolyte capacitors contain manganese dioxide, which is formed on the tantalum pentoxide dielectric layer by impregnating the pellet with a solution of manganous nitrate. The pellet is then heated in an oven, and the manganous nitrate is converted to manganese dioxide.

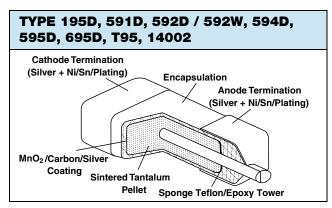
The pellet is next coated with graphite, followed by a layer of metallic silver, which provides a conductive surface between the pellet and the can in which it will be enclosed. After assembly, the capacitors are tested and inspected to assure long life and reliability. It offers excellent reliability and high stability for consumer and commercial electronics with the added feature of low cost.

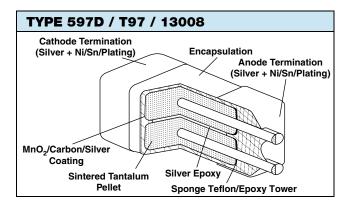
Surface mount designs of "Solid Tantalum" capacitors use lead frames or lead frameless designs as shown in the accompanying drawings.

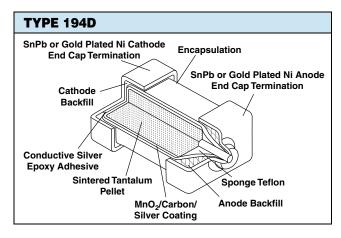
TANTALUM CAPACITORS FOR ALL DESIGN CONSIDERATIONS

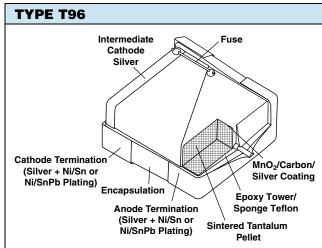
Solid electrolyte designs are the least expensive for a given rating and are used in many applications where their very small size for a given unit of capacitance is of importance. They will typically withstand up to about 10 % of the rated DC working voltage in a reverse direction. Also important are their good low temperature performance characteristics and freedom from corrosive electrolytes.

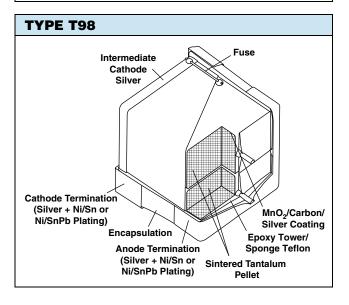
Vishay Sprague patented the original solid electrolyte capacitors and was the first to market them in 1956. Vishay Sprague has the broadest line of tantalum capacitors and has continued its position of leadership in this field. Data sheets covering the various types and styles of Vishay Sprague capacitors for consumer and entertainment electronics, industry, and military applications are available where detailed performance characteristics must be specified.













COMMERCIAL PRODUCTS

SOLID TANTALUM CAPACITORS - CONFORMAL COATED							
SERIES	592W	592D	591D	595D	594D		
PRODUCT IMAGE							
TYPE		Surface mount	TANTAMOUNT™ chip, co	nformal coated			
FEATURES	Low profile, robust design for use in pulsed applications	Low profile, maximum CV	Low profile, low ESR, maximum CV	Maximum CV	Low ESR, maximum CV		
TEMPERATURE RANGE	-55 °C to +125 °C (above 40 °C, voltage deratig is required)	-55 °C	to +125 °C (above 85 °C	C, voltage derating is re	equired)		
CAPACITANCE RANGE	330 μF to 2200 μF	1 μF to 2200 μF	1 μF to 1500 μF	0.1 μF to 1500 μF	1 μF to 1500 μF		
VOLTAGE RANGE	6 V to 10 V	4 V to 50 V	4 V to 50 V	4 V to 50 V	4 V to 50 V		
CAPACITANCE TOLERANCE	± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %		
LEAKAGE CURRENT		0.01 CV or 0.5 μA, whichever is greater					
DISSIPATION FACTOR	14 % to 45 %	4 % to 50 % 4 % to 50 % 4 % to 20 % 4 %			4 % to 20 %		
CASE CODES	C, M, X	S, A, B, C, D, R, M, X	A, B, C, D, R, M	T, S, A, B, C, D, G, M, R	B, C, D, R		
TERMINATION	100 % matte tin	100 %	matte tin standard, tin /	lead and gold plated a	vailable		

SOLID TANTAL	SOLID TANTALUM CAPACITORS - CONFORMAL COATED								
SERIES	597D	695D	195D	194D					
PRODUCT IMAGE									
TYPE		Tantamount™ chip	o, conformal coated						
FEATURES	Ultra low ESR, maximum CV, multi-anode	Pad compatible with 194D and CWR06	US and European case sizes	Industrial version of CWR06 / CWR16					
TEMPERATURE RANGE		55 °C to +125 °C (above 85 °C	C, voltage derating is required	d)					
CAPACITANCE RANGE	10 μF to 2200 μF	0.1 μF to 270 μF	0.1 μF to 330 μF	0.1 μF to 330 μF					
VOLTAGE RANGE	4 V to 75 V	4 V to 50 V	2 V to 50 V	4 V to 50 V					
CAPACITANCE TOLERANCE		± 10 %,	± 20 %						
LEAKAGE CURRENT		0.01 CV or 0.5 μA, ν	whichever is greater						
DISSIPATION FACTOR	6 % to 20 %	4 % to 8 %	4 % to 8 %	4 % to 10 %					
CASE CODES	V, D, E, R, F, Z, M, H	A, B, D, E, F, G, H	C, S, V, X, Y, Z, R, A, B, D, E, F, G, H	A, B, C, D, E, F, G, H					
TERMINATION	100 % matte tin standard, tin / lead solder plated available	100 % matte tin / lead and gol	Gold plated standard; tin / lead solder plated and hot solder dipped available						

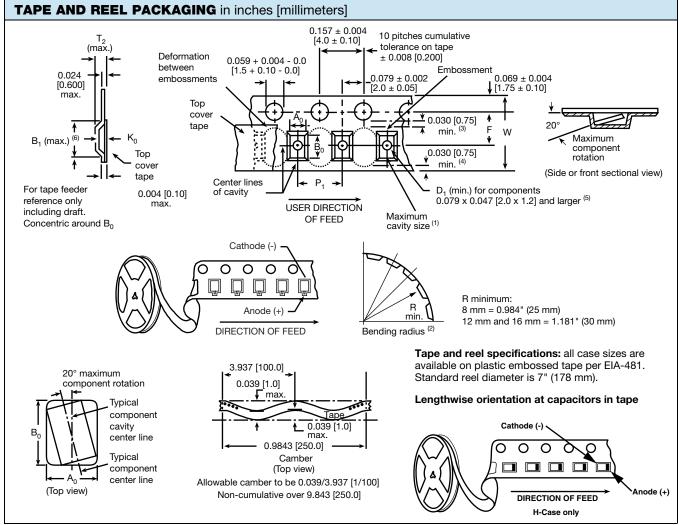


HIGH RELIABILITY PRODUCTS

SOLID TANTALUM CAPACITORS - CONFORMAL COATED							
SERIES	CWR06	CWR16	CWR26	13008	14002		
PRODUCT IMAGE							
TYPE		TANTAMO	DUNT™ chip, conforma	al coated			
FEATURES	MIL-PRF-55365/4 qualified	MIL-PRF-55365/13 qualified	MIL-PRF-55365/13 qualified	DLA approved			
TEMPERATURE RANGE		-55 °C to +125 °C (above 85 °C, voltage	derating is required)			
CAPACITANCE RANGE	0.10 μF to 100 μF	0.33 μF to 330 μF	10 μF to 100 μF	10 μF to 1500 μF	4.7 μF to 680 μF		
VOLTAGE RANGE	4 V to 50 V	4 V to 35 V	15 V to 35 V	4 V to 63 V	4 V to 50 V		
CAPACITANCE TOLERANCE	± 5 %, ± 10 %, ± 20 %	± 5 %, ± 10 %, ± 20 %	± 5 %, ± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %		
LEAKAGE CURRENT	0.01 CV	or 1.0 μA, whichever i	is greater	0.01 CV or 0.5 μA, v	vhichever is greater		
DISSIPATION FACTOR	6 % to 10 %	6 % to 10 %	6 % to 12 %	2 % 6 % to 20 % 6 % to 14 %			
CASE CODES	A, B, C, D, E, F, G, H	A, B, C, D, E, F, G, H	F, G, H	V, E, F, R, Z, D, M, H, N	B, C, D, R		
TERMINATION	Gold plated	l; tin / lead; tin / lead s	solder fused	Tin /	lead		

SOLID TANTALUM CA	PACITORS - CONFO	RMAL COATED			
SERIES	T95	T96	T97	T98	
PRODUCT IMAGE					
TYPE		TANTAMOUNT™ chip, Hi-Re	el COTS, conformal coated		
FEATURES	High reliability	High reliability, built in fuse	High reliability, ultra low ESR, multi-anode	High reliability, ultra low ESR, built in fuse, multi-anode	
TEMPERATURE RANGE	-55	°C to +125 °C (above 85 °	C, voltage derating is requir	red)	
CAPACITANCE RANGE	0.15 μF to 680 μF	10 μF to 680 μF	10 μF to 2200 μF	10 μF to 1500 μF	
VOLTAGE RANGE	4 V to 50 V	4 V to 50 V	4 V to 75 V	4 V to 75 V	
CAPACITANCE TOLERANCE	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	± 10 %, ± 20 %	
LEAKAGE CURRENT	0.01 CV or 0.5 μA, whichever is greater				
DISSIPATION FACTOR	4 % to 14 %	6 % to 14 %	6 % to 20 %	6 % to 10 %	
CASE CODES	A, B, C, D, R, S, V, X, Y, Z	R	V, E, F, R, Z, D, M, H, N	V, E, F, R, Z, M, H	
TERMINATION		100 % matte	tin, tin / lead		

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Notes

- · Metric dimensions will govern. Dimensions in inches are rounded and for reference only
- (1) A₀, B₀, K₀, are determined by the maximum dimensions to the ends of the terminals extending from the component body and / or the body dimensions of the component. The clearance between the ends of the terminals or body of the component to the sides and depth of the cavity (A₀, B₀, K₀) must be within 0.002" (0.05 mm) minimum and 0.020" (0.50 mm) maximum. The clearance allowed must also prevent rotation of the component within the cavity of not more than 20°
- (2) Tape with components shall pass around radius "R" without damage. The minimum trailer length may require additional length to provide "R" minimum for 12 mm embossed tape for reels with hub diameters approaching N minimum
- (3) This dimension is the flat area from the edge of the sprocket hole to either outward deformation of the carrier tape between the embossed cavities or to the edge of the cavity whichever is less
- (4) This dimension is the flat area from the edge of the carrier tape opposite the sprocket holes to either the outward deformation of the carrier tape between the embossed cavity or to the edge of the cavity whichever is less
- (5) The embossed hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location shall be applied independent of each other
- (6) B₁ dimension is a reference dimension tape feeder clearance only





CARRIER TAPE DIMENSIONS in inches [millimeters]						
TAPE WIDTH	W	D ₀	P ₂	F	E ₁	E _{2 min.}
8 mm	0.315 + 0.012 / - 0.004 [8.0 + 0.3 / - 0.1]		0.078 ± 0.0019	0.14 ± 0.0019 [3.5 ± 0.05]	0.324 ± 0.004 [1.75 ± 0.1]	0.246 [6.25]
12 mm	0.479 + 0.012 / - 0.004 [12.0 + 0.3 / - 0.1]	0.059 + 0.004 / - 0	[2.0 ± 0.05]	0.216 ± 0.0019 [5.5 ± 0.05]		0.403 [10.25]
16 mm	0.635 + 0.012 / - 0.004 [16.0 + 0.3 / - 0.1]	[1.5 + 0.1 / - 0]	0.078 ± 0.004	0.295 ± 0.004 [7.5 ± 0.1]		0.570 [14.25]
24 mm	0.945 ± 0.012 [24.0 ± 0.3]		$[2.0 \pm 0.1]$	0.453 ± 0.004 [11.5 ± 0.1]		0.876 [22.25]

CARRIER TAPE DIMENSIONS in inches [millimeters]					
TYPE	CASE CODE	TAPE WIDTH W IN mm	P ₁	K _{0 max.}	B _{1 max} .
	Α	8	0.157 ± 0.004 [4.0 ± 0.10]	0.058 [1.47]	0.149 [3.78]
	В	12		0.088 [2.23]	0.166 [4.21]
	С	12	0.315 ± 0.004	0.088 [2.23]	0.290 [7.36]
	D	12		0.088 [2.23]	0.300 [7.62]
592D 592W	М	16	[8.0 ± 0.10]	0.091 [2.30]	0.311 [7.90]
591D	R	12		0.088 [2.23]	0.296 [7.52]
	S	8	0.157 ± 0.004	0.058 [1.47]	0.139 [3.53]
	Т	12	[4.0 ± 0.10]	0.088 [2.23]	0.166 [4.21]
	Х	24	0.472 ± 0.004 [12.0 ± 0.10]	0.011 [2.72]	0.594 [15.1]
	А	8	0.157 ± 0.004 [4.0 ± 0.10]	0.063 [1.60]	0.152 [3.86]
	В	12		0.088 [2.23]	0.166 [4.21]
	С	12	0.315 ± 0.004 [8.0 ± 0.10]	0.118 [2.97]	0.290 [7.36]
	D	12		0.119 [3.02]	0.296 [7.52]
	G	12		0.111 [2.83]	0.234 [5.95]
595D 594D	Н	12		0.098 [2.50]	0.232 [5.90]
3940	М	12	0.157 ± 0.004 [4.0 ± 0.10]	0.085 [2.15]	0.152 [3.85]
	R	12	0.315 ± 0.004 [8.0 ± 0.10]	0.148 [3.78]	0.296 [7.52]
	S	8	0.157 ± 0.004	0.058 [1.47]	0.149 [3.78]
	T	8	$[4.0 \pm 0.10]$	0.054 [1.37]	0.093 [2.36]
	Α	8	0.157 ± 0.004 [4.0 ± 0.10]	0.058 [1.47]	0.139 [3.53]
	В	12		0.059 [1.50]	0.189 [4.80]
	D	12		0.063 [1.62]	0.191 [4.85]
	E	12		0.074 [1.88]	0.239 [6.07]
695D	F	12	0.315 ± 0.004 [8.0 ± 0.10]	0.075 [1.93]	0.259 [6.58]
	G	12	0.157 ± 0.004 [4.0 ± 0.10]	0.109 [2.77]	0.301 [7.65]
	Н	16	0.315 ± 0.004 [8.0 ± 0.10]	0.124 [3.15]	0.31 [7.87]



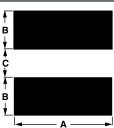
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		TAPE WIDTH			
TYPE	CASE CODE	W IN mm	P ₁	$K_{0 max.}$	B _{1 max.}
	A	8		0.058 [1.47]	0.139 [3.53]
	В	12	-	0.058 [1.47]	0.189 [4.80]
	С	8	0.157 ± 0.004	0.059 [1.37]	
			$[4.0 \pm 0.10]$		0.093 [2.36]
	D	12		0.067 [1.70]	0.179 [4.55]
	E	12	0.045 0.004	0.074 [1.88]	0.239 [6.07]
	F	12	0.315 ± 0.004 [8.0 ± 0.10]	0.076 [1.93]	0.259 [6.58]
195D	G	12	0.157 ± 0.004 [4.0 ± 0.10]	0.109 [2.77]	0.301 [7.65]
.002	H ⁽¹⁾	12	0.472 ± 0.004 [12.0 ± 0.1]	0.122 [3.11]	0.163 [4.14]
	R	12	0.315 ± 0.004 [8.0 ± 0.10]	0.149 [3.78]	0.296 [7.52]
	S	8		0.058 [1.47]	0.149 [3.78]
	V	8	1	0.060 [1.52]	0.150 [3.80]
	X	12	0.157 ± 0.004	0.069 [1.75]	0.296 [7.52]
	Y	12	$[4.0 \pm 0.10]$	0.089 [2.26]	0.296 [7.52]
	Z	12		0.114 [2.89]	0.288 [7.31]
	A	8		0.069 [1.75]	0.139 [3.53]
	В	12	1	0.073 [1.85]	0.189 [4.80]
	C	12	0.157 ± 0.004	0.069 [1.75]	0.244 [6.20]
194D CWR06	D	12	$[4.0 \pm 0.10]$	0.068 [1.72]	0.191 [4.85]
CWR16	E	12		0.074 [1.88]	0.239 [6.07]
CWR26	F	12		0.074 [1.88]	
	G	16	0.315 ± 0.004		0.262 [6.65]
	Н	16	$[8.0 \pm 0.10]$	0.134 [3.40]	0.289 [7.34]
			2217 2221	0.129 [3.28]	0.319 [8.10]
	D	16	0.317 ± 0.004	0.150 [3.80]	0.313 [7.95]
	E	16	[8.0 ± 0.10]	0.173 [4.40]	0.343 [8.70]
	F	16	-	0.205 [5.20]	0.309 [7.85]
	H	16	0.476 ± 0.004	0.224 [5.70]	0.313 [7.95]
597D T97	M	16	[12.0 ± 0.1]	0.193 [4.90]	0.339 [8.60]
13008	N	16		0.283 [7.20]	0.323 [8.20]
10000	R	16		0.159 [4.05]	0.313 [7.95]
	V	12	0.317 ± 0.004 [8.0 ± 0.10]	0.088 [2.23]	0.300 [7.62]
	Z	16	0.476 ± 0.004 [12.0 ± 0.1]	0.239 [6.06]	0.311 [7.90]
	A	8		0.063 [1.60]	0.152 [3.86]
	В	12	0.157 ± 0.004 [4.0 ± 0.10] 0.317 ± 0.004	0.088 [2.23]	0.166 [4.21]
	С	12		0.117 [2.97]	0.290 [7.36]
Т95	D	12		0.119 [3.02]	0.296 [7.52]
	R	12	$[8.0 \pm 0.10]$	0.149 [3.78]	0.296 [7.52]
	S	8		0.058 [1.47]	0.149 [3.78]
	V	8	1	0.060 [1.52]	0.150 [3.80]
	X	12	0.157 ± 0.004 [4.0 ± 0.10]	0.069 [1.75]	0.296 [7.52]
	Y	12		0.089 [2.26]	0.296 [7.52]
	Z	12		0.114 [2.89]	0.288 [7.31]
14002	B	12	0.157 ± 0.004	0.088 [2.23]	0.166 [4.21]
	С	12	$[4.0 \pm 0.10]$	0.000 [2.20]	0.290 [7.36]
	D	12	. ,	0.117 [2.97]	0.296 [7.52]
	R	12	0.317 ± 0.004 [8.0 ± 0.10]	•	0.296 [7.52]
		12		0.149 [3.78]	
T96	R	16	0.476 ± 0.004 [12.0 ± 0.1]	0.159 [4.05]	0.313 [7.95]
	F	16	0.476 ± 0.004	0.239 [6.06]	0.311 [7.90]
T98	M	16	$[12.0 \pm 0.1]$	0.193 [4.90]	0.339 [8.60]
	Z	16	1 .	0.272 [6.90]	0.307 [7.80]

Note

 $^{(1)}\,$ H case only, packaging code T: lengthwise orientation at capacitors in tape

PAD DIMENSIONS in inches [millimeters]

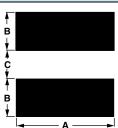


A				
CASE CODE	WIDTH (A)	PAD METALLIZATION (B)	SEPARATION (C)	
592D / W - 591D				
Α	0.075 [1.9]	0.050 [1.3]	0.050 [1.3]	
В	0.118 [3.0]	0.059 [1.5]	0.059 [1.5]	
С	0.136 [3.5]	0.090 [2.3]	0.122 [3.1]	
D	0.180 [4.6]	0.090 [2.3]	0.134 [3.4]	
	0.256 [6.5]	Anode pad: 0.095 [2.4]	0.400 [0.5]	
М		Cathode pad: 0.067 [1.7]	0.138 [3.5]	
D	0.040 [0.4]	Anode pad: 0.095 [2.4]	0.440.[0.0]	
R	0.240 [6.1]	Cathode pad: 0.067 [1.7]	0.118 [3.0]	
S	0.067 [1.7]	0.032 [0.8]	0.043 [1.1]	
Х	0.310 [7.9]	0.120 [3.0]	0.360 [9.2]	
595D - 594D		<u> </u>		
Т	0.059 [1.5]	0.028 [0.7]	0.024 [0.6]	
S	0.067 [1.7]	0.032 [0.8]	0.043 [1.1]	
A	0.083 [2.1]	0.050 [1.3]	0.050 [1.3]	
В	0.118 [3.0]	0.059 [1.5]	0.059 [1.5]	
С	0.136 [3.5]	0.090 [2.3]	0.122 [3.1]	
D	0.180 [4.6]	0.090 [2.3]	0.134 [3.4]	
G	0.156 [4.05]	0.090 [2.3]	0.082 [2.1]	
М	0.110 [2.8]	0.087 [2.2]	0.134 [3.4]	
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]	
195D				
A	0.067 [1.7]	0.043 [1.1]	0.028 [0.7]	
В	0.063 [1.6]	0.047 [1.2]	0.047 [1.2]	
С	0.059 [1.5]	0.031 [0.8]	0.024 [0.6]	
D	0.090 [2.3]	0.055 [1.4]	0.047 [1.2]	
E	0.090 [2.3]	0.055 [1.4]	0.079 [2.0]	
F	0.140 [3.6]	0.063 [1.6]	0.087 [2.2]	
G	0.110 [2.8]	0.059 [1.5]	0.126 [3.2]	
Н	0.154 [3.9]	0.063 [1.6]	0.140 [3.6]	
N	0.244 [6.2]	0.079 [2.0]	0.118 [3.0]	
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]	
S	0.079 [2.0]	0.039 [1.0]	0.039 [1.0]	
V	0.114 [2.9]	0.039 [1.0]	0.039 [1.0]	
Х	0.118 [3.0]	0.067 [1.7]	0.122 [3.1]	
Υ	0.118 [3.0]	0.067 [1.7]	0.122 [3.1]	
Z	0.118 [3.0]	0.067 [1.7]	0.122 [3.1]	



Vishay Sprague

PAD DIMENSIONS in inches [millimeters]



CASE CODE	WIDTH (A)	PAD METALLIZATION (B)	SEPARATION (C)		
CWR06 / CWR16 / CWR26 - 194D - 695D					
А	0.065 [1.6]	0.50 [1.3]	0.040 [1.0]		
В	0.065 [1.6]	0.70 [1.8]	0.055 [1.4]		
С	0.065 [1.6]	0.70 [1.8]	0.120 [3.0]		
D	0.115 [2.9]	0.70 [1.8]	0.070 [1.8]		
E	0.115 [2.9]	0.70 [1.8]	0.120 [3.0]		
F	0.150 [3.8]	0.70 [1.8]	0.140 [3.6]		
G	0.125 [3.2]	0.70 [1.8]	0.170 [4.3]		
Н	0.165 [4.2]	0.90 [2.3]	0.170 [4.3]		
T95					
В	0.120 [3.0]	0.059 [1.5]	0.059 [1.5]		
С	0.136 [3.5]	0.090 [2.3]	0.120 [3.1]		
D	0.180 [4.6]	0.090 [2.3]	0.136 [3.47]		
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]		
S	0.080 [2.03]	0.040 [1.02]	0.040 [1.02]		
V	0.114 [2.9]	0.040 [1.02]	0.040 [1.02]		
X, Y, Z	0.114 [2.9]	0.065 [1.65]	0.122 [3.1]		
14002					
В	0.120 [3.0]	0.059 [1.5]	0.059 [1.5]		
С	0.136 [3.5]	0.090 [2.3]	0.120 [3.1]		
D	0.180 [4.6]	0.090 [2.3]	0.136 [3.47]		
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]		
T96					
R	0.248 [6.3]	0.090 [2.3]	0.140 [3.6]		
597D - T97 - T98 - 13008					
D, E, V	0.196 [4.9]	0.090 [2.3]	0.140 [3.6]		
F, R, Z	0.260 [6.6]	0.090 [2.3]	0.140 [3.6]		
M, H, N	0.284 [7.2]	0.090 [2.3]	0.140 [3.6]		

6 °C/s max.

8 min max.

classification temperature (T_c)

Time 25 °C to peak temperature

Ramp-down rate $(T_p \text{ to } T_L)$

Ramp-down

Vishay Sprague

RECOMMENDED REFLOW PROFILES Capacitors should withstand reflow profile as per J-STD-020 standard, three cycles. T_C - 5 °C Max. ramp-up rate = 3 °C/s Max. ramp-down rate = $6 \, ^{\circ}\text{C/s}$ TEMPERATURE (°C) $T_{s max.}$ Preheat area T_{s min.} 25 Time 25 °C to peak TIME (s) **PROFILE FEATURE SnPb EUTECTIC ASSEMBLY LEAD (Pb)-FREE ASSEMBLY** Preheat / soak Temperature min. (T_{s min.}) 100 °C 150 °C Temperature max. (T_{s max.)} 150 °C 200 °C Time (t_s) from ($T_{s min.}$ to $T_{s max.}$) 60 s to 120 s 60 s to 120 s Ramp-up Ramp-up rate (T_L to T_p) 3 °C/s max. 3 °C/s max. Liquidus temperature (T_L) 183 °C 217 °C Time (t_L) maintained above T_L 60 s to 150 s 60 s to 150 s Depends on type and case - see table below Peak package body temperature (Tp) Time (t_p)* within 5 °C of the specified 20 s 30 s

TVD= / 0.40= 0.00=	PEAK PACKAGE BODY TEMPERATURE (Tp)			
TYPE / CASE CODE	SnPb EUTECTIC PROCESS	LEAD (Pb)-FREE PROCESS		
591D / 592D - all cases, except X25H, M and R cases	235 °C	260 °C		
591D / 592D - X25H, M and R cases	220 °C	250 °C		
594D / 595D - all cases except C, D, and R	235 °C	260 °C		
594D / 595D - C, D, and R case	220 °C	250 °C		
T95 A, B, S, V, X, Y cases	235 °C	260 °C		
T95 C, D, R, and Z cases	220 °C	250 °C		
14002 B case	235 °C	n/a		
14002 C, D, and R cases	220 °C	n/a		
T96 R case	220 °C	250 °C		
195D all cases, except G, H, R, and Z	235 °C	260 °C		
195D G, H, R, and Z cases	220 °C	250 °C		
695D all cases, except G and H cases	235 °C	260 °C		
695D G, H cases	220 °C	250 °C		
597D, T97, T98 all cases, except V case	220 °C	250 °C		
597D, T97, T98 V case	235 °C	260 °C		
194D all cases, except H and G cases	235 °C	260 °C		
194D H and G cases	220 °C	250 °C		

6 °C/s max.

6 min max.

GUIDE TO APPLICATION

 AC Ripple Current: the maximum allowable ripple current shall be determined from the formula:

$$I_{RMS} = \sqrt{\frac{P}{R_{ESR}}}$$

where,

P = power dissipation in W at +25 °C as given in the tables in the product datasheets (Power Dissipation).

R_{ESR} = the capacitor equivalent series resistance at the specified frequency

2. **AC Ripple Voltage:** the maximum allowable ripple voltage shall be determined from the formula:

$$V_{RMS} = I_{RMS} \times Z$$

or, from the formula:

$$V_{RMS} = Z \sqrt{\frac{P}{R_{ESR}}}$$

where,

P = power dissipation in W at +25 °C as given in the tables in the product datasheets (Power Dissipation).

R_{ESR} = the capacitor equivalent series resistance at the specified frequency

Z = the capacitor impedance at the specified frequency

- 2.1 The sum of the peak AC voltage plus the applied DC voltage shall not exceed the DC voltage rating of the capacitor.
- 2.2 The sum of the negative peak AC voltage plus the applied DC voltage shall not allow a voltage reversal exceeding 10 % of the DC working voltage at +25 °C.
- 3. **Reverse Voltage:** solid tantalum capacitors are not intended for use with reverse voltage applied. However, they have been shown to be capable of withstanding momentary reverse voltage peaks of up to 10 % of the DC rating at 25 °C and 5 % of the DC rating at +85 °C.
- 4. **Temperature Derating:** if these capacitors are to be operated at temperatures above +25 °C, the permissible RMS ripple current shall be calculated using the derating factors as shown:

TEMPERATURE	DERATING FACTOR	
+25 °C	1.0	
+85 °C	0.9	
+125 °C	0.4	

5. **Power Dissipation:** power dissipation will be affected by the heat sinking capability of the mounting surface. Non-sinusoidal ripple current may produce heating effects which differ from those shown. It is important that the equivalent I_{RMS} value be established when calculating permissible operating levels. (Power dissipation calculated using derating factor (see paragraph 4)).

6. Attachment:

- 6.1 **Soldering:** capacitors can be attached by conventional soldering techniques: vapor phase, convection reflow, infrared reflow, and hot plate methods. The soldering profile charts show recommended time / temperature conditions for soldering. Preheating is recommended. The recommended maximum ramp rate is 2 °C per second. Attachment with a soldering iron is not recommended due to the difficulty of controlling temperature and time at temperature. The soldering iron must never come in contact with the capacitor. For details see www.vishay.com/doc?40214.
- Recommended Mounting Pad Geometries: the nib
 must have sufficient clearance to avoid electrical
 contact with other components. The width
 dimension indicated is the same as the maximum
 width of the capacitor. This is to minimize lateral
 movement.
- 8. Cleaning (Flux Removal) After Soldering:

 TANTAMOUNTTM capacitors are compatible with all commonly used solvents such as TES, TMS, Prelete, Chlorethane, Terpene and aqueous cleaning media. However, CFC / ODS products are not used in the production of these devices and are not recommended. Solvents containing methylene chloride or other epoxy solvents should be avoided since these will attack the epoxy encapsulation material.



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