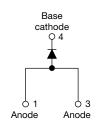


COMPLIANT HALOGEN

FREE

High Voltage Surface-Mount Input Rectifier Diode, 25 A





D²PAK 2L (TO-263AB 2L)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	25 A			
V _R	1200 V			
V _F at I _F	1.14 V			
I _{FSM}	300 A			
T _J max.	150 °C			
Package	2L D ² PAK (2L TO-263AB)			
Circuit configuration	Single			

FEATURES

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Glass passivated pellet chip junction
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Input rectification
- On-board and off-board EV / HEV battery chargers

DESCRIPTION

The VS-25ETS12SLHM3 rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage.

MECHANICAL DATA

Case: D²PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

J-STD-002

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	20	23	А		

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	25	A			
V _{RRM}		1200	V			
I _{FSM}		300	A			
V _F	10 A, T _J = 25 °C	1.0	V			
T _J		-40 to +150	°C			

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA			
VS-25ETS12S2LHM3	1200	1300	1			





ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 106 °C, 180° conduction half sine wave	25			
Maximum peak one cycle	I	10 ms sine pulse, rated V _{RRM} applied	250	A A ² s		
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300			
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	316			
Maximum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	442	A-5		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s		

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST (CONDITIONS	VALUES	UNITS	
Maximum forward voltage drop	V_{FM}	25 A, T _J = 25 °C		1.14	V	
Forward slope resistance	r _t	T 150 °C		10.4	mΩ	
Threshold voltage	V _{F(TO)}	T _J = 150 °C		0.85	V	
Maximum rayaraa laakaga aurrant		T _J = 25 °C	// _ rotod //	0.1	mΛ	
Maximum reverse leakage current	IRM	T _J = 150 °C	V_R = rated V_{RRM}	1.0	mA	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.9		
Maximum thermal resistance, junction to ambient	R _{thJA} ⁽¹⁾	For D ² PAK version	62	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.5		
Approximate weight			2	g	
Approximate weight			0.07	OZ.	
Marking device		Case style: D ² PAK 2L (TO-263AB 3L)	25ETS	12SH	

Note

 $^{^{(1)}}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W



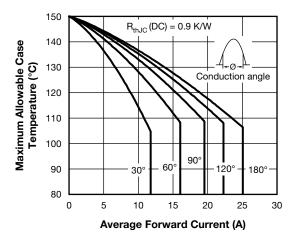


Fig. 1 - Current Rating Characteristics

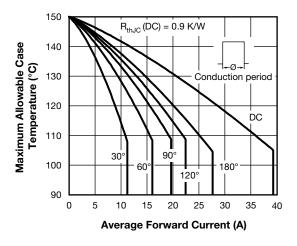


Fig. 2 - Current Rating Characteristics

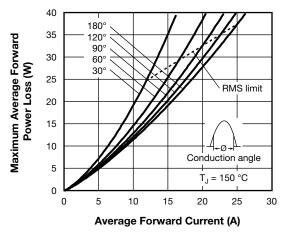


Fig. 3 - Forward Power Loss Characteristics

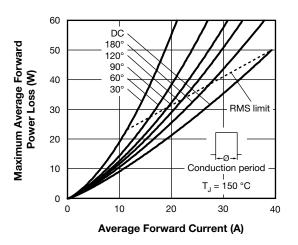


Fig. 4 - Forward Power Loss Characteristics

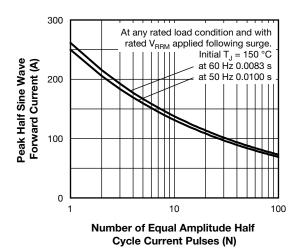


Fig. 5 - Maximum Non-Repetitive Surge Current

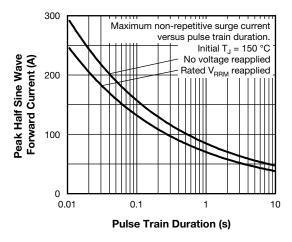


Fig. 6 - Maximum Non-Repetitive Surge Current

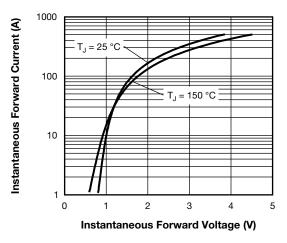


Fig. 7 - Forward Voltage Drop Characteristics

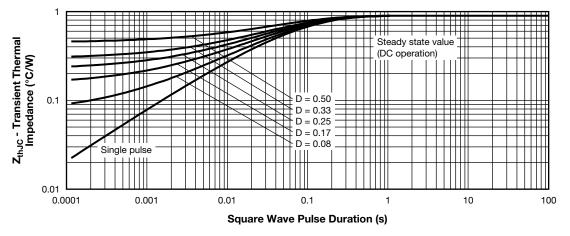
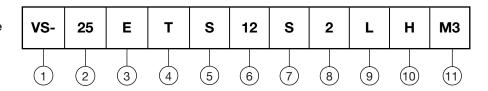


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Current rating (25 = 25 A)

- Circuit configuration

E = single diode

4 - Package:

 $T = D^2PAK$

5 - Type of silicon:

S = standard recovery rectifier

6 - Voltage code x 100 = V_{RRM} - 12 = 1200 V

7 - S = surface mountable

 L = tape and reel (left oriented), for different orientation, contact factory

- H = AEC-Q101 qualified

11 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

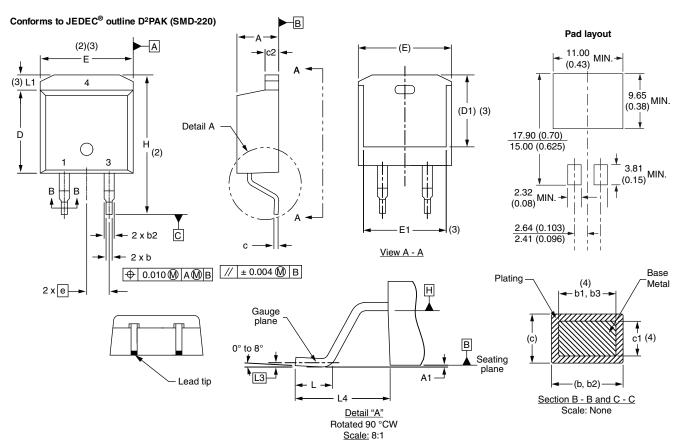
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-25ETS12S2LHM3	800	800	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96683			
Part marking information	www.vishay.com/doc?96693			
Packaging information	www.vishay.com/doc?95032			
SPICE model	www.vishay.com/doc?95409			



2L-D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INCHES		NOTES	
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L3	0.25 BSC		0.010) BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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Vishay

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