

# Ultrafast Rectifier, 20 A FRED Pt®



D<sup>2</sup>PAK 2L (TO-263AB 2L)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	20 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.2 V			
t <sub>rr</sub> (typ.)	ns			
T <sub>J</sub> max.	175 °C			
Package	D <sup>2</sup> PAK 2L (TO-263AB 2L)			
Circuit configuration	Single			

N/C

Anode

#### **FEATURES**

- Low forward voltage drop
- · Ultrafast recovery time
- 175 °C operating junction temperature

• Low leakage current



- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

State of the art, ultralow V<sub>F</sub>, soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC)

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other switching applications

#### **APPLICATIONS**

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adapters, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

#### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 129 °C	20	Δ
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	180	A
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-	.,,
Forward valtage	- W	I <sub>F</sub> = 20 A	-	1.35	1.7	V
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 20 A, T <sub>J</sub> = 150 °C	-	1.2	1.4		
Deviage leeks as suggest		$V_R = V_R$ rated	-	0.02	5	
Reverse leakage current I <sub>R</sub>	I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	20	200	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	12	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 8.0 -		nΗ		



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	$I_F = 1 \text{ A, } dI_F/dt = 10$		00 A/μs, V <sub>R</sub> = 30 V	-	26	-	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 20 A	-	42	-	ns
		T <sub>J</sub> = 125 °C		-	89	-	
Peak recovery current I <sub>RRM</sub>		T <sub>J</sub> = 25 °C		-	4.9	-	
	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 390 V	-	8.4	-	A	
Reverse recovery charge Q	0	T <sub>J</sub> = 25 °C		-	110	-	С
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	440	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	1.51	°C/W
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	
Thermal resistance, case to heat sink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D <sup>2</sup> PAK 2L (TO-263AB 2L)	ETU2006SH			

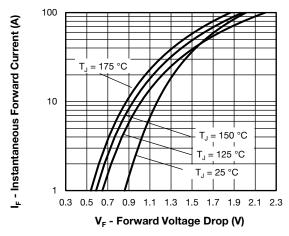


Fig. 1 - Typical Forward Voltage Drop Characteristics

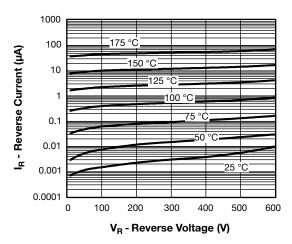


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

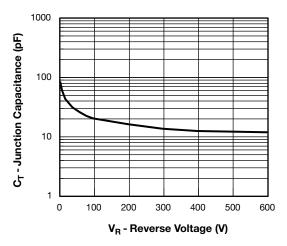


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

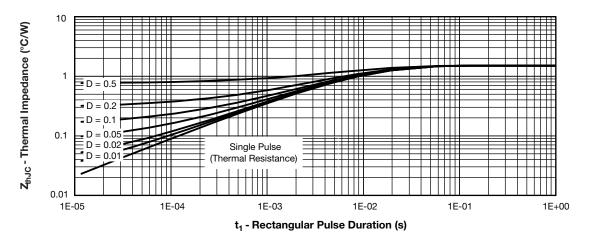


Fig. 4 - Max. Thermal Impedance Z<sub>thJC</sub> Characteristics

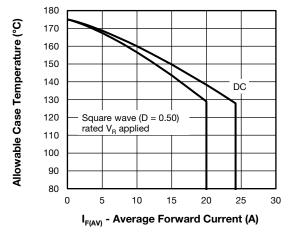


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

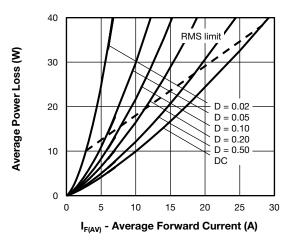


Fig. 6 - Forward Power Loss Characteristics

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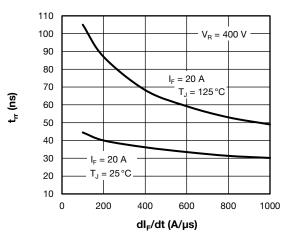


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

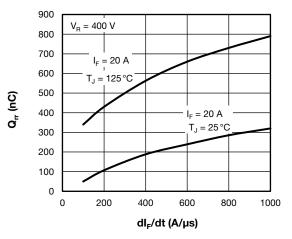
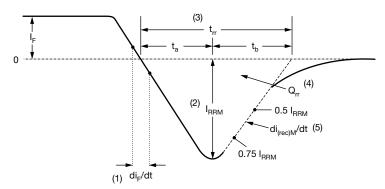


Fig. 8 - Typical Stored Charge vs. dl<sub>E</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm l_{F}$  to point where a line passing through 0.75  $\rm l_{RRM}$  and 0.50  $\rm l_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

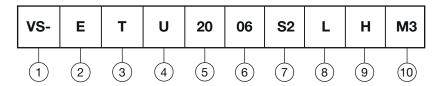
(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions



#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- Circuit configuration
  E = single diode
- T = D<sup>2</sup>PAK (TO-263) package
- 4 U = ultrafast recovery time
- 5 Current code (20 = 20 A)
- 6 Voltage code (06 = 600 V)
- 7 • S2 = true 2 pin D<sup>2</sup>PAK
- None = tube (50 pieces)
  - • L = tape and reel (left oriented, for D<sup>2</sup>PAK package)

If needed different orientation / packaging, please contact factory

- 9 H = AEC-Q101 qualified
- Environmental digit:

M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-ETU2006S2LHM3	800	800	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96683			
Part marking information	www.vishay.com/doc?96693			
Packaging information	www.vishay.com/doc?95032			



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