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PRODUCT SUMMARY

 $R_{DS(on)}$ max. (Ω) at V_{GS} = -4.5 V

 $R_{DS(on)}$ max. (Ω) at V_{GS} = -2.5 V

 $R_{DS(on)}$ max. (Ω) at V_{GS} = -1.8 V

 $R_{DS(on)}$ max. (Ω) at $V_{GS} = -1.5$ V

V_{DS} (V)

Qg typ. (nC)

I_D (A) ^a

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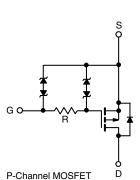


FEATURES

- TrenchFET[®] power MOSFET
- 100% Rg tested
- Typical ESD performance 2000 V
- Built in ESD protection with Zener Diode
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

· Load switch for portable devices



Configuration	Single		
ORDERING INFORMATIO	ON		
Package		SOT-23	
Lead (Pb)-free and halogen-free		Si2377EDS-T1-GE3	

-20

0.061

0.080

0.110

0.165

7.6

-4.4

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-20	V	
Gate-source voltage		V _{GS}	± 8	v	
	T _C = 25 °C		-4.4		
Continuous drain surrant (T 150 °C)	T _C = 70 °C		3.5		
Continuous drain current ($T_J = 150 \ ^\circ C$)	T _A = 25 °C	I _D	-3.7 ^{b, c}		
	T _A = 70 °C		-2.9 ^{b, c}	А	
Pulsed drain current		I _{DM}	-20	1	
Continuous source-drain diode current	T _C = 25 °C		-1.5		
	T _A = 25 °C	I _S	-1 ^{b, c}		
	T _C = 25 °C		1.8		
	T _C = 70 °C		1.1		
Maximum power dissipation	T _A = 25 °C	P _D	1.25 ^{b, c}	- W	
	T _A = 70 °C		0.8 ^{b, c}	7	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150		
Soldering recommendations (peak temperature) ^{d, e}		Ĭ	260		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b, d	t ≤ 5 s	R _{thJA}	80	100	°C/W
Maximum junction-to-foot (drain)	Steady state	R _{thJF}	55	70	0/00

Notes

a. $T_C = 25 \ ^{\circ}C$

b. Surface mounted on 1" x 1" FR4 board

t = 5 s c.

d. Maximum under steady state conditions is 130 °C/W

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RoHS

COMPLIANT HALOGEN FREE



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Si2377EDS

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				•			
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	-20	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	L 050 A	-	-13	-		
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	2.5	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	-0.4	-	-1	V	
		V_{DS} = 0 V, V_{GS} = ± 8 V	-	-	± 6		
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	-	± 0.5		
Zene ende velke en elveire enveret		$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			-1	μA	
Zero gate voltage drain current	I _{DSS}	V_{DS} = -20 V, V_{GS} = 0 V, T_{J} = 55 °C	-	-	-10	-	
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-15	-	-	А	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -3.2 \text{ A}$	-	0.050	0.061		
		V_{GS} = -2.5 V, I _D = -2.8 A	-	0.065	0.080	Ω	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -1.8 V, I _D = -1.5 A	-	0.090	0.110		
		V _{GS} = -1.5 V, I _D = -0.5 A	-	0.110	0.165		
Forward transconductance a	9 _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -3.2 \text{ A}$	-	12	-	S	
Dynamic ^b				•			
Total gate charge	Qg	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = -8 \text{ V}, \text{ I}_{D} = -5.3 \text{ A}$	-	14	21		
			-	7.6	12		
Gate-source charge	Q _{as}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -5.3 \text{ A}$	-	0.8	-	nC	
Gate-drain charge	Q _{ad}		-	3.1	-		
Gate resistance	R _g	f = 1 MHz	0.4	2	4	kΩ	
Turn-on delay time	t _{d(on)}		-	0.2	0.3		
Rise time	t _r	$V_{DD} = -10 \text{ V}, \text{ R}_1 = 2.3 \Omega$	-	1	1.5	-	
Turn-off delay time	t _{d(off)}	$I_D \cong -4.3$ A, $V_{GEN} = -4.5$ V, $R_g = 1$ Ω	-	4	6		
Fall time	t _f		-	2	3		
Turn-on delay time	t _{d(on)}		-	0.09	0.14	μs	
Rise time	t _r	$V_{DD} = -10 \text{ V}, \text{ R}_1 = 2.3 \Omega$	-	0.4	0.6		
Turn-off delay time	t _{d(off)}	$I_D \cong -4.3 \text{ A}, V_{GEN} = -8 \text{ V}, \text{ R}_g = 1 \Omega$	-	5.2	7.8		
Fall time	t _f		-	2.3	3.5		
Drain-Source Body Diode Characteris	1 1						
Continuous source-drain diode current	Is	T _C = 25 °C	-	-	-1.5		
Pulse diode forward current	I _{SM}		-	-	-20	A	
Body diode voltage	V _{SD}	I _S = -3 A, V _{GS} = 0 V	-	-0.8	-1.2	V	
Body diode reverse recovery time	t _{rr}		-	30	60	ns	
Body diode reverse recovery charge	Q _{rr}	I _F = -3 A, di/dt = 100 A/µs,	-	20	40	nC	
Reverse recovery fall time	ta	$T_J = 25 \text{ °C}$	-	13	-		
Reverse recovery rise time	t _b		_	17	_	ns	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2%

b. Guaranteed by design, not subject to production testing

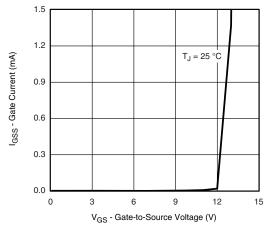
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2

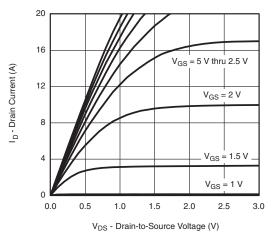


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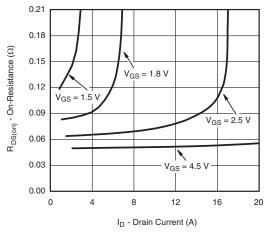
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



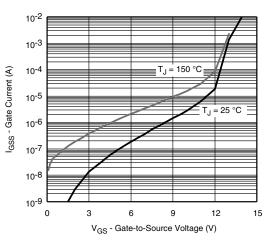
Gate Current vs. Gate-Source Voltage



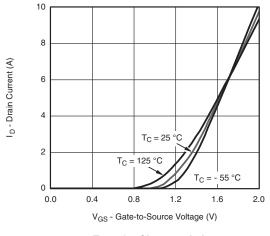




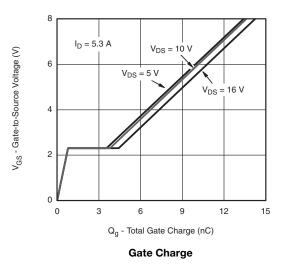
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



Transfer Characteristics



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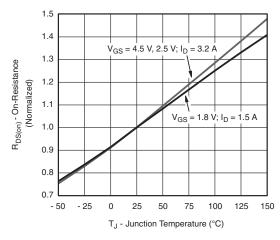
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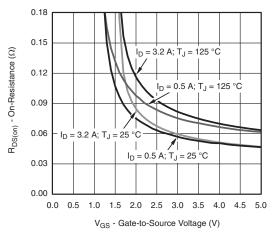


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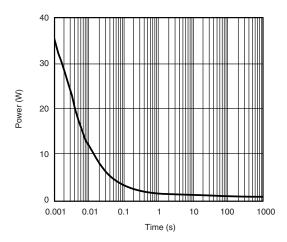
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



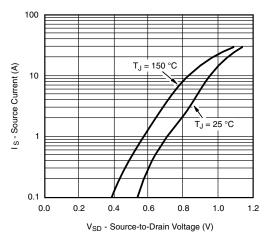
On-Resistance vs. Junction Temperature



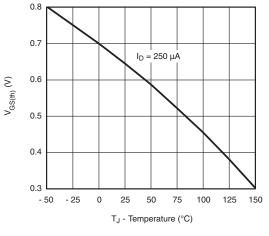
On-Resistance vs. Gate-to-Source Voltage



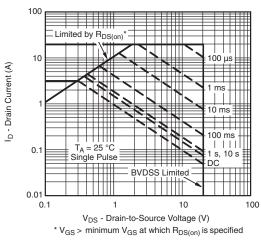
Single Pulse Power, Junction-to-Ambient



Source-Drain Diode Forward Voltage







Safe Operating Area, Junction-to-Ambient

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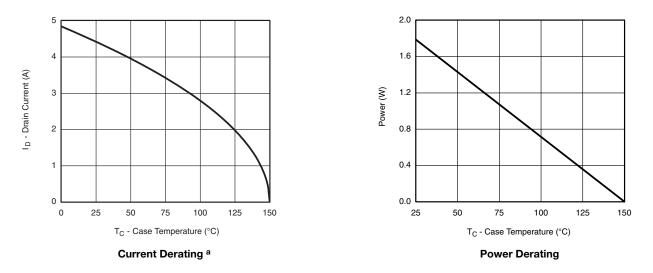
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



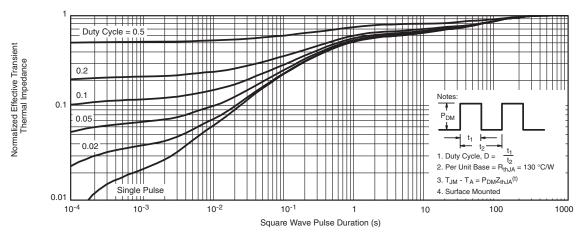
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

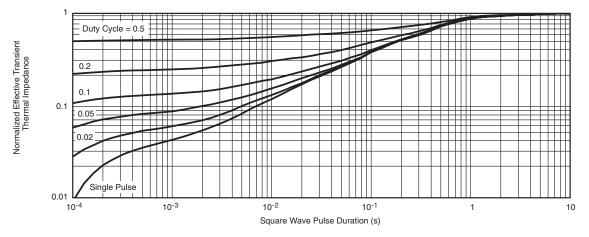


Vishay Siliconix

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for silicon technology and package reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65905.



Package Information

Vishay Siliconix

SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES			
	Min	Max	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e ₁	1.90 BSC		0.0748 Ref			
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025 Ref			
S	0.50 Ref		0.020	020 Ref		
q	3°	8°	3°	8°		



Application Note 826

Vishay Siliconix

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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