AUTOMOTIV

COMPLIANT

HALOGEN FREE

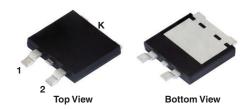


## Vishay General Semiconductor

# Dual TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.39 \text{ V}$  at  $I_F = 5 \text{ A}$ 

### eSMP® Series SMPD (TO-263AC)





#### **ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	2 x 15 A		
$V_{RRM}$	60 V		
I <sub>FSM</sub>	170 A		
$V_F$ at $I_F = 15 A$	0.57 V		
T <sub>J</sub> max.	150 °C		
Package	SMPD (TO-263AC)		
Circuit configuration	Common cathode		

#### **FEATURES**

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

#### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3\_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified

("\_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test. HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V30D60C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	60	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	30	Δ.	
	per diode		15	А	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	170	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.47	-	- V
	$I_F = 7.5 A$			0.51	-	
	I <sub>F</sub> = 15 A			0.60	0.70	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.38	-	
	I <sub>F</sub> = 7.5 A			0.44	-	
	I <sub>F</sub> = 15 A			0.57	0.65	
Reverse current per diode	V 60 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	-	1200	μΑ
	V <sub>R</sub> = 60 V	T <sub>A</sub> = 125 °C		17	60	mA

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V30D60C	UNIT	
	per diode	R <sub>θJC</sub>	2.5		
Typical thermal resistance	per device		1.7	°C/W	
	per device	R <sub>0</sub> JA (1)(2)	48	]	

#### **Notes**

(1) The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$ 

(2) Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTITY		BASE QUANTITY	DELIVERY MODE			
V30D60C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V30D60CHM3_A/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

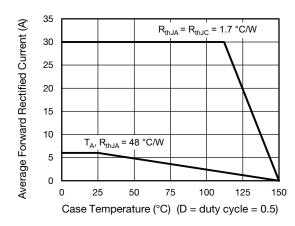


Fig. 1 - Forward Current Derating Curve

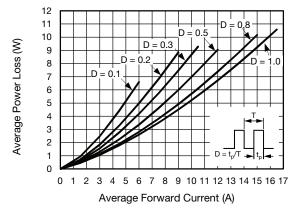


Fig. 2 - Forward Power Loss Characteristics Per Diode



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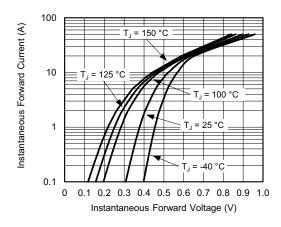


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

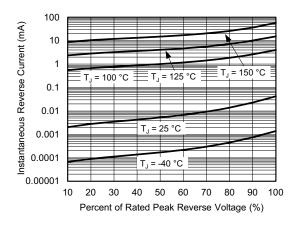


Fig. 4 - Typical Reverse Characteristics Per Diode

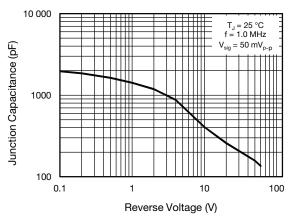


Fig. 5 - Typical Junction Capacitance Per Diode

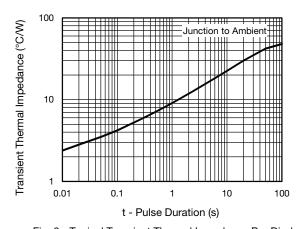


Fig. 6 - Typical Transient Thermal Impedance Per Diode

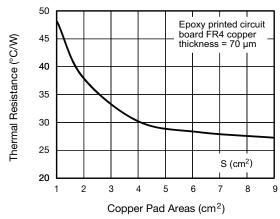
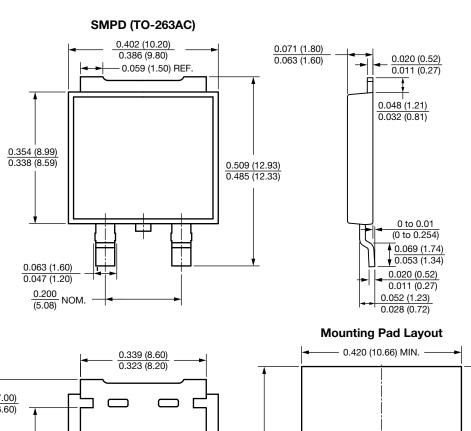


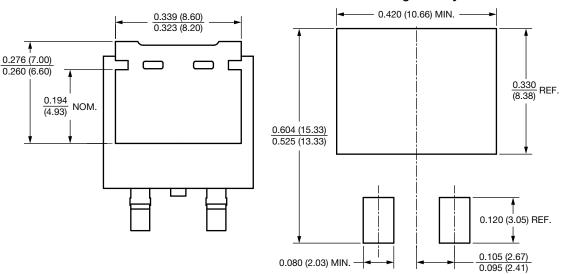
Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas



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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)







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