



# N-Channel 200 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
200	0.090 at V <sub>GS</sub> = 10 V	19	
200	0.105 at V <sub>GS</sub> = 6 V	17.5	

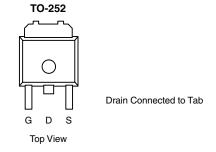
#### **FEATURES**

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R<sub>a</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



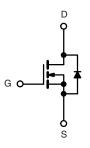
#### **APPLICATIONS**

· Primary Side Switch



#### Ordering Information:

SUD19N20-90-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $(T_A =$	25 °C, unless othe	rwise noted)		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	200	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	7 v
Continuous Drain Current (T,I = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	I-	19	
Continuous Drain Current (1 <sub>J</sub> = 175 °C)°	T <sub>C</sub> = 125 °C	I <sub>D</sub>	11	
Pulsed Drain Current		I <sub>DM</sub>	40	A
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	19	
Avalanche Current		I <sub>AS</sub>	19	
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	18	mJ
Maximum Power Discination	T <sub>C</sub> = 25 °C	P <sub>D</sub>	136 <sup>b</sup>	w
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		3 <sup>a</sup>	\\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
lunation to Ambient	t ≤ 10 s	- R <sub>thJA</sub>	15	18	°C/W
Junction-to-Ambient <sup>a</sup>	Steady State		40	50	
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.85	1.1	

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See SOA curve for voltage derating.

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Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V			1	
Zero Gate Voltage Drain Current	$I_{DSS}$	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ
		V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	40			Α
		$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		0.075	0.090	
Durin Course Co Otata Davidana h	<b>B</b>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C			0.190	Ω
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A, T <sub>J</sub> = 175 °C			0.260	
		$V_{GS} = 6 \text{ V}, I_D = 5 \text{ A}$		0.082	0.105	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 19 A		35		S
Dynamic <sup>a</sup>						
Input Capacitance	C <sub>iss</sub>			1800		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		180		
Reverse Transfer Capacitance	C <sub>rss</sub>			80		
Total Gate Charge <sup>c</sup>	$Q_g$			34	51	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 19 \text{ A}$		8		nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			12		
Gate Resistance	$R_g$		0.5		2.9	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	25	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 100 \text{ V}, R_L = 5.2 \Omega$		50	75	nc
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 19 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	ns
Fall Time <sup>c</sup>	t <sub>f</sub>			60	90	
Source-Drain Diode Ratings and Char	acteristics (7	Γ <sub>C</sub> = 25 °C)				
Pulsed Current	I <sub>SM</sub>				50	Α
Diode Forward Voltage <sup>b</sup>	$V_{SD}$	I <sub>F</sub> = 19 A, V <sub>GS</sub> = 0 V	_	0.9	1.5	V
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 19 A, dl/dt = 100 A/μs		180	250	ns

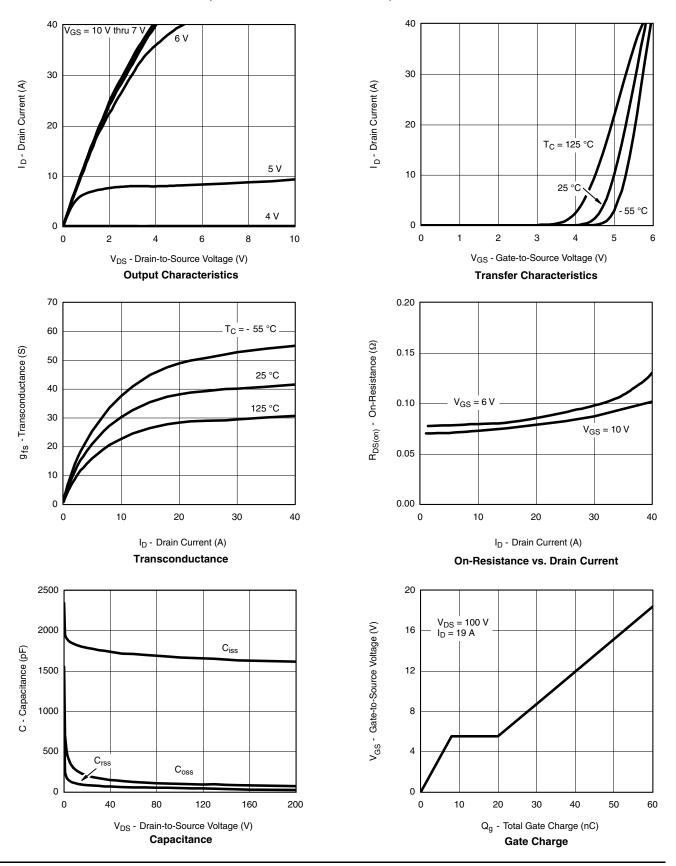
#### Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



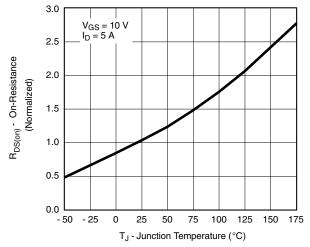
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



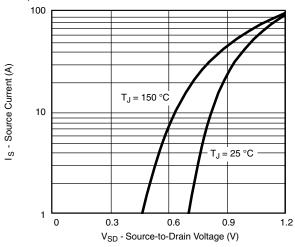
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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

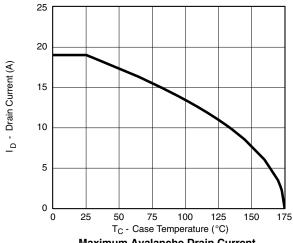


On-Resistance vs. Junction Temperature

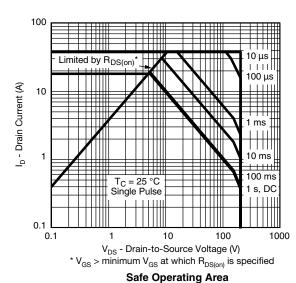


Source-Drain Diode Forward Voltage

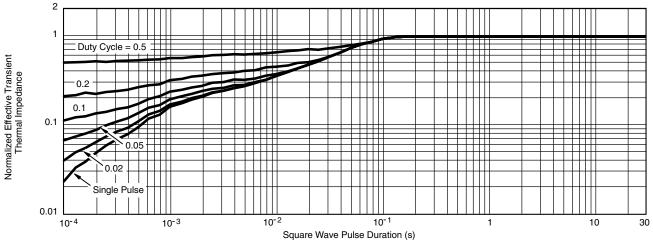
#### THERMAL RATINGS



Maximum Avalanche Drain Current vs. Case Temperature



Sale Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

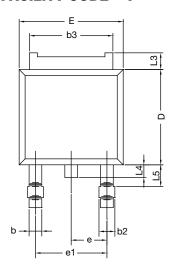
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71767">www.vishay.com/ppg?71767</a>.

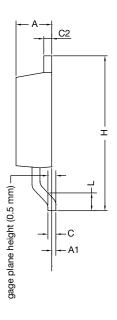
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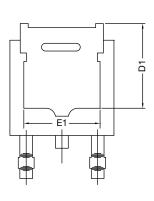


## **TO-252AA Case Outline**

#### **VERSION 1: FACILITY CODE = Y**







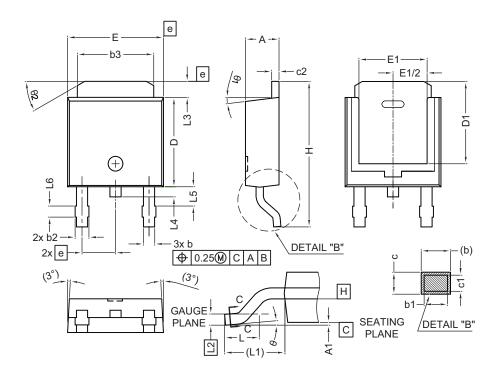
	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
Е	6.35	6.73	
E1	4.32	=	
Н	9.40	10.41	
е	2.28 BSC		
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

#### Note

• Dimension L3 is for reference only



#### **VERSION 2: FACILITY CODE = N**



	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
Е	6.35	6.73	
E1	4.32	-	
е	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74	ref.	
L2	0.51 BSC		
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

#### Notes

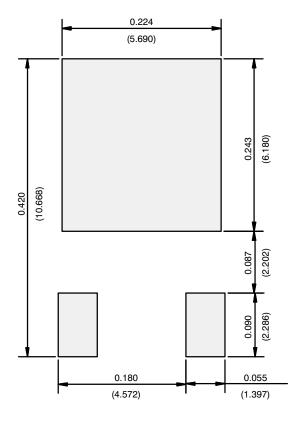
- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- · Radius on terminal is optional

ECN: E19-0649-Rev. Q, 16-Dec-2019

DWG: 5347



#### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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