

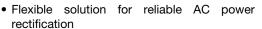
Thyristor High Voltage, Phase Control SCR, 40 A



PRIMARY CHARACTERISTICS					
I _{T(AV)}	35 A				
V _{DRM} /V _{RRM}	1600 V				
V_{TM}	1.45 V				
I _{GT}	150 mA				
T_J	-40 °C to +125 °C				
Package	TO-247AD 3L				
Circuit configuration	Single SCR				

FEATURES

 Designed and qualified according to JEDEC® - JESD 47





- Easy control peak current at charger power up to reduce passive / electromechanical components
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

APPLICATIONS

 Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding and battery charge

DESCRIPTION

The VS-40TPS16L-M3 high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications.

AEC-Q101 qualified P/N available (40TPS16LHM3).

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	35	Δ.		
I _{RMS}		55	A		
V _{RRM} /V _{DRM}		1600	V		
I _{TSM}		500	A		
V_{T}	40 A, T _J = 25 °C	1.45	V		
dv/dt		1000	V/µs		
di/dt		100	A/µs		
TJ		-40 to +125	°C		

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA				
VS-40TPS16L-M3	1600	1700	10				



ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average on-state current	I _{T(AV)}	T _C = 79 °C, 180° conduction half sine wave	Э	35		
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}			55	А	
Maximum peak, one-cycle	I	10 ms sine pulse, rated V _{RRM} applied		420		
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	1.292.1	500		
Maximum I ² t for fusing	I ² t	10 ms sine pulse, rated V _{RRM} applied	Initial $T_{.1} = T_{.1} \text{ max.}$	880	A ² s	
waxiinum i-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	ij – ijiliax.	1250		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied		12 500	A²√s	
Low level value of threshold voltage	V _{T(TO)1}		1.02	V		
High level value of threshold voltage	V _{T(TO)2}	T, _I = 125 °C	1.23	V		
Low level value of on-state slope resistance	r _{t1}	1J = 125 C		9.74	0	
High level value of on-state slope resistance	r _{t2}			7.50	mΩ	
Maximum pook on state voltage	V	110 A, T _J = 25 °C		1.92	V	
Maximum peak on-state voltage	V _{TM}	90 A, T _J = 25 °C	1.82	V		
Maximum rate of rise of turned-on current	di/dt	T _J = 25 °C		100	A/μs	
Maximum holding current	I _H	Anode supply = 6 V, resistive load, initial $T_J = 1 \text{ A}$, $I_T = 25 ^{\circ}\text{C}$		300		
Maximum latching current	ΙL	Anode supply = 6 V, resistive load, T _J = 25 °C		350	A	
Manipular variation and alive at leading a comment	I _{RRM/} I _{DRM}	T _J = 25 °C		0.5	mA	
Maximum reverse and direct leakage current		$T_J = 125 ^{\circ}\text{C}$ $V_R = \text{rated } V_{RRM}/V_{DR}$	$V_R = \text{rated } V_{RRM}/V_{DRM}$			
Maximum rate of rise of off-state voltage	dv/dt	$T_J = T_J$ maximum, linear to 80 % V_{DRM} , R_g - k = open		1000	V/µs	

TRIGGERING						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
Maximum peak gate power	P _{GM}			10	W	
Maximum average gate power	P _{G(AV)}			2.5		
Maximum peak gate current	I _{GM}			2.5	Α	
Maximum peak negative gate voltage	-V _{GM}			10	V	
	V _{GT}	T _J = -40 °C	Anode supply = 6 V	4.0		
Maximum required DC gate voltage to trigger		T _J = 25 °C		2.5	V	
		T _J = 125 °C	resistive load	1.7		
		T _J = -40 °C	Acada a sala QV	270		
Maximum required DC gate current to trigger	I _{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	150	mA	
		T _J = 125 °C	resistive load	80		
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 125 °C, V _{DRM} = rated value		0.25	V	
Maximum DC gate current not to trigger	I_{GD}			6	mA	



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +125	°C			
Maximum thermal resistance, junction to case	R_{thJC}	DC operation	0.6				
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	40	°C/W			
Maximum thermal resistance, case to heat sink	R _{thCS}	Mounting surface, smooth, and greased	0.25				
Approximate weight			6	g			
Approximate weight			0.21	OZ.			
Mounting torque minimum			6 (5)	kgf · cm			
maximum			12 (10)	(lbf · in)			
Marking device		Case style TO-247AD 3L	40TPS	16L			

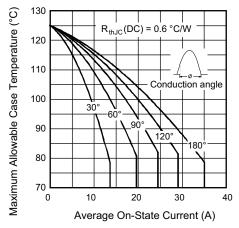


Fig. 1 - Current Rating Characteristics

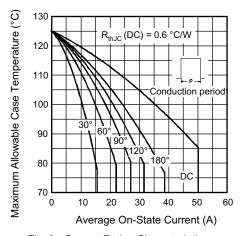


Fig. 2 - Current Rating Characteristics

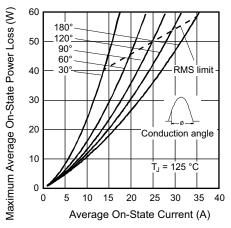


Fig. 3 - On-State Power Loss Characteristics

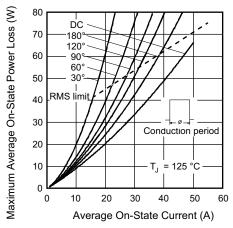


Fig. 4 - On-State Power Loss Characteristics



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Vishay Semiconductors

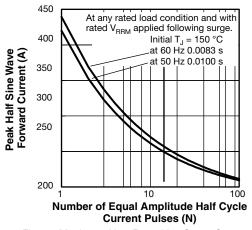


Fig. 5 - Maximum Non-Repetitive Surge Current

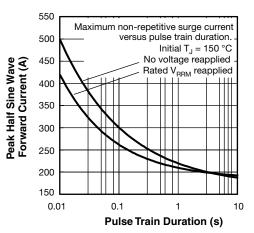


Fig. 6 - Maximum Non-Repetitive Surge Current

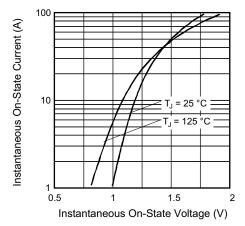


Fig. 7 - On-State Voltage Drop Characteristics

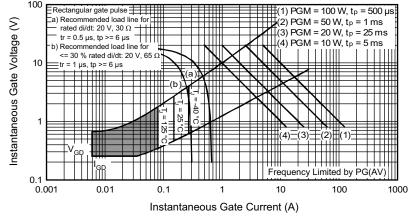


Fig. 8 - Gate Characteristics

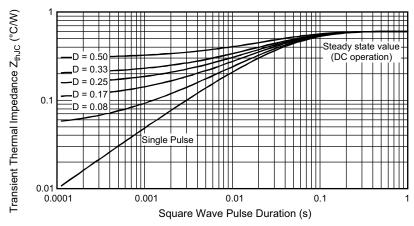
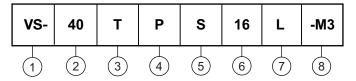


Fig. 9 - Gate Characteristics

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- 2 Current rating (40 = 40 A)
- Circuit configuration:

T = thyristor

- 4 Package:
 - P = TO-247
- 5 Type of silicon:
 - S = standard recovery rectifier
- 6 Voltage ratings 16 = 1600 V
- 7 L = long leads
- 8 Environmental digit:

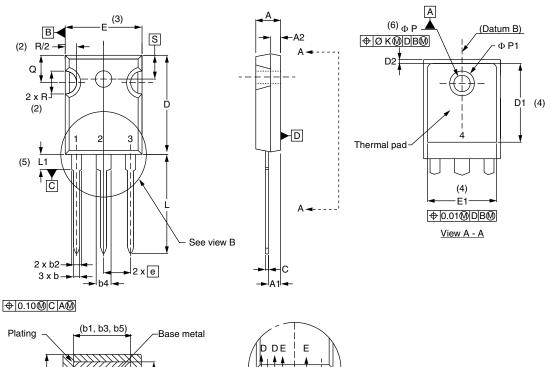
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-40TPS16L-M3	25	500	Antistatic plastic tubes		

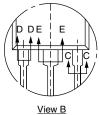
LINKS TO RELATED DOCUMENTS				
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626		
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007		

TO-247AD 3L

DIMENSIONS in millimeters and inches



Plating _	(b1, b3, b5)	-Base meta
(c)		c1
	(b, b2, b4) —	
	Section C - C, D - D,	<u>, E - E</u>



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	IVIILLIIV	IEIENO	INCHES		NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
Е	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	=.	
е	5.46	5.46 BSC		BSC	
ØK	2.	54	0.0)10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØР	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	' BSC	
		•	•		

INCHES

MILLIMETERS

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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