Vishay Semiconductors

High Voltage, Input Rectifier Diode, 20 A



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PRIMARY CHARACTERISTICS							
I _{F(AV)}	20 A						
V _R	1600 V						
V _F at I _F	1.1 V						
I _{FSM}	300 A						
T _J max.	150 °C						
Package	2L TO-220AC						
Circuit configuration	Single						

FEATURES

- Glass passivated pellet chip junction
- AEC-Q101 qualified
- Meets JESD 201 class 1A whisker test
- Flexible solution for reliable AC power FREE
 rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- On-board and off-board EV/HEV battery chargers
- Input rectification

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS								
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS					
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	16.3	21	А					

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I _{F(AV)}	Sinusoidal waveform	20	A							
V _{RRM}		1600	V							
I _{FSM}		300	A							
V _F	10 A, T _J = 25 °C	1.0	V							
TJ		-40 to +150	°C							

VOLTAGE RATINGS									
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA						
VS-20ETS16THM3	1600	1700	1						

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COMPLIANT

VS-20ETS16THM3

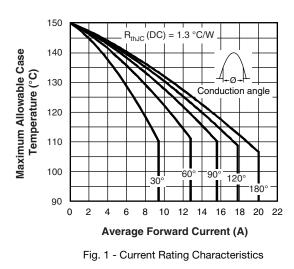


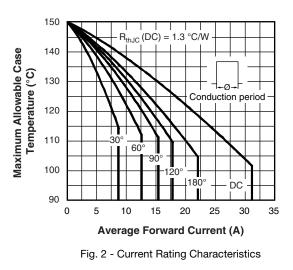
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ABSOLUTE MAXIMUM RATINGS								
PARAMETER SYMBOL TEST CONDITIONS			VALUES	UNITS				
Maximum average forward current	I _{F(AV)}	T_C = 105 °C, 180° conduction half sine wave	20					
Maximum peak one cycle		10 ms sine pulse, rated V_{RRM} applied	250	А				
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300					
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied 316		A ² s				
Maximum int for fusing	1-1	10 ms sine pulse, no voltage reapplied	442	A-S				
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s				

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
Maximum forward voltage drop	V _{FM}	20 A, T _J = 25 °C	1.1	V				
Forward slope resistance	r _t	T.I = 150 °C	10.4	mΩ				
Threshold voltage	V _{F(TO)}	$I_{\rm J} = 150$ C	0.85	V				
Maximum reverse leakage current		T _J = 25 °C		0.1	~^^			
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	V _R = Rated V _{RRM}	1.0	mA			

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T _J , T _{Stg}		-40 to +150	°C		
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.3			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5 °C			
Approximate weight				2	g		
Approximate weight				0.07	oz.		
mini				6 (5)	kgf ⋅ cm		
Mounting torque	maximum			12 (10)	(lbf \cdot in)		
Marking device			Case style 2L TO-220AC	20ETS	516TH		





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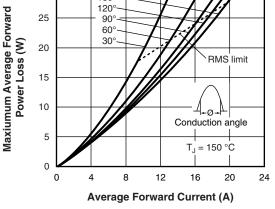


Fig. 3 - Forward Power Loss Characteristics

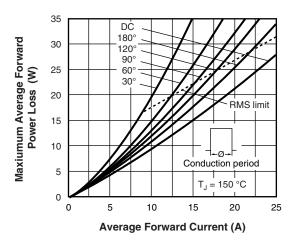


Fig. 4 - Forward Power Loss Characteristics

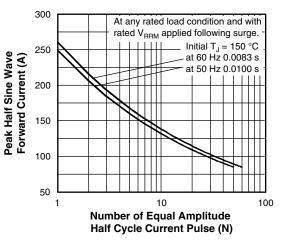


Fig. 5 - Maximum Non-Repetitive Surge Current

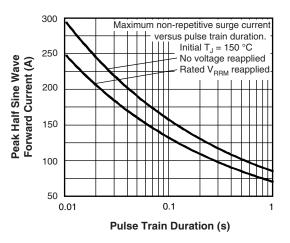
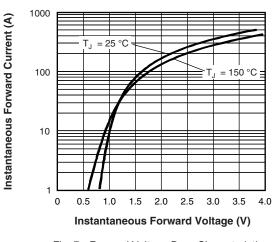
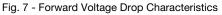


Fig. 6 - Maximum Non-Repetitive Surge Current





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VS-20ETS16THM3

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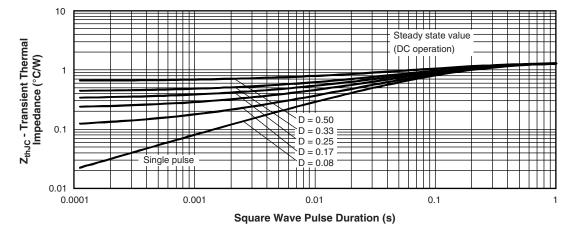


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

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Device code	vs-	20	E	т	S	16	т	н	МЗ
	1	2	3	4	5	6	7	8	9
	1 ·	- Visł	nay Sen	nicondu	ctors pro	oduct			
	2 -	- Cur	rent rat	ing (20 =	= 20 A)				
	3 -	- Circ	cuit con	figuratio	n:				
		E = 2L TO-220AC							
	4 -		kage:						
	_		TO-220						
	5 -		e of sili						
	_			rd recov	-				
	6 -	· Volt	tage co	de x 100	$= V_{RRM}$	1	—16 =	1600 V	
	7 -	• N	one = T	O-220A	В				
		• T	= True	pin TO-2	220				
	8 -	H =	AEC-Q	101 qua	lified				
	9 -	Env	ironmer	ntal digit:					
		М3	= halog	jen-free,	RoHS-0	complia	nt, and t	erminat	ions lea

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-20ETS16THM3	50	1000	Antistatic plastic tubes					

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96069				
Part marking information	www.vishay.com/doc?95391				

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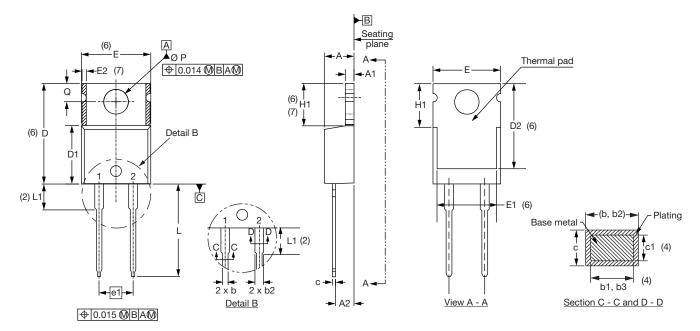
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2L TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	STWDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183		E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055		E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115		е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040		e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4	H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068		L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4	L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024		ØΡ	3.54	3.73	0.139	0.147	
c1	0.36	0.56	0.014	0.022	4	Q	2.60	3.00	0.102	0.118	
D	14.85	15.25	0.585	0.600	3						
D1	8.38	9.02	0.330	0.355							
D2	11.68	12.88	0.460	0.507	6						
E	10.11	10.51	0.398	0.414	3, 6						

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Dimension b1, b3 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1

 $^{\left(7\right)}$ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed

⁽⁸⁾ Outline conforms to JEDEC[®] TO-220, except D2, where JEDEC[®] minimum is 0.480".

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