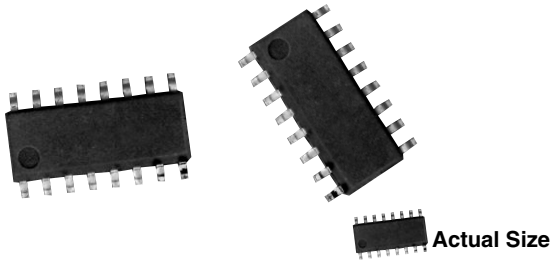
Molded, 50 mil Pitch, Dual-In-Line Thin Film Resistor, Narrow Body, Surface Mount Network



Actual Size

The NOMC series features a standard 14 pins and 16 pins narrow body (0.150") small outline surface mount style. It can accommodate resistor networks to your particular application requirements. The networks can be constructed with passivated nichrome (standard), or tantalum nitride [1] resistor films to optimize performance.

Note

[1] Available upon request. Resistance value range and performance differs from passivated nichrome standard electrical specifications on datasheet, consult factory

FEATURES

- Standard 14 pins and 16 pins counts (0.150" narrow body) JEDEC MS-012 variation AB and AC
- Rugged molded case construction
- Excellent long term ratio stability (ΔR ± 0.015 %)
- Low TCR tracking ± 5 ppm/°C
- Isolated and bussed schematics
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free Available

RoHS* Available

HALOGEN FREE

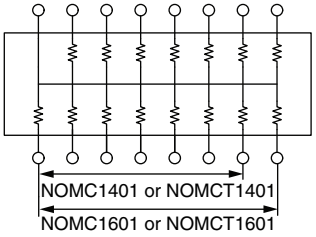
Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

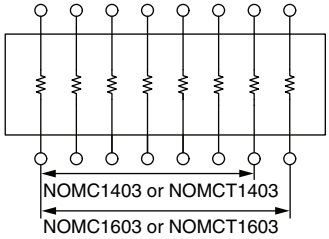
TYPICAL PERFORMANCE

	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.10	0.05

SCHEMATICS



The 01 circuit provides a choice of 13 or 15 equal value resistors each connected between a common lead (14 or 16). Custom schematics available.



The 03 circuit provides a choice of 7 or 8 equal value resistors (14 or 16). Custom schematics available.

STANDARD RESISTANCE OFFERING (Equal Value Resistors)	
ISOLATED (03) SCHEMATIC	**BUSSED (01) SCHEMATIC**
1 kΩ	1 kΩ
2 kΩ	5 kΩ
5 kΩ	10 kΩ
10 kΩ	20 kΩ
20 kΩ	
25 kΩ	
50 kΩ	
100 kΩ	

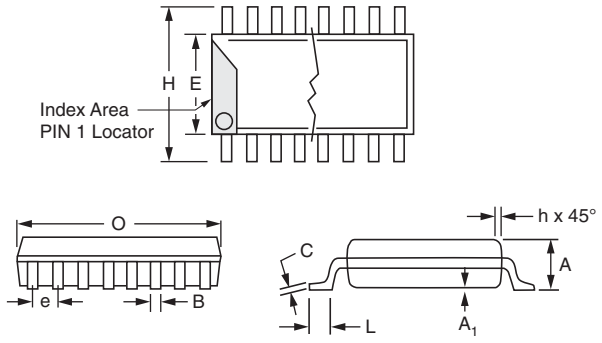
Note

- Consult factory for additional values

STANDARD ELECTRICAL SPECIFICATIONS

TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome (standard) Tantalum nitride (available upon request)	-
Pin/Lead Number	14, 16	-
Resistance Range	100 Ω to 50 kΩ each resistor (bussed (01) schematic) 100 Ω to 100 kΩ each resistor (isolated (03) schematic)	- -
TCR: Absolute	± 25 ppm/°C (standard)	- 55 °C to + 125 °C
TCR: Tracking	± 5 ppm/°C (typical)	- 55 °C to + 125 °C
Tolerance: Absolute	± 0.10 % to ± 1 %	+ 25 °C
Tolerance: Ratio	± 0.025 % to ± 0.1 %	+ 25 °C
Power Rating: Resistor	100 mW ((typical) (03) schematic) 50 mW ((01) schematic)	Maximum at + 70 °C
Power Rating: Package	400 mW/500 mW	Maximum at + 70 °C
Stability: Absolute	ΔR ± 0.05 %	2000 h at + 70 °C
Stability: Ratio	ΔR ± 0.015 %	2000 h at + 70 °C
Voltage Coefficient	< 0.1 ppm/V	-
Working Voltage	100 V max. not to exceed $\sqrt{P \times R}$	-
Operating Temperature Range	- 55 °C to + 125 °C	-
Storage Temperature Range	- 55 °C to + 150 °C	-
Noise	≤ - 30 dB	-
Thermal EMF	0.08 µV/°C	-
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C
Shelf Life Stability: Ratio	ΔR ± 0.002 %	1 year at + 25 °C

DIMENSIONS AND IMPRINTING in inches and millimeters



DIMENSION	14		16	
	INCHES	MILLIMETERS	INCHES	MILLIMETERS
H	0.235	5.969	0.235	5.969
E	0.154	3.911	0.154	3.91
O	0.340	8.363	0.390	9.906
A	0.063	1.60	0.063	1.60
e	0.050	1.270	0.050	1.270
B	0.015	0.381	0.015	0.381
C	0.008	0.203	0.008	0.203
L	0.025	0.635	0.025	0.635
A1	0.006	0.152	0.006	0.152
h	0.015	0.381	0.015	0.381

MECHANICAL SPECIFICATIONS

Resistive Element	Passivated nichrome
Substrate Material	Silicon
Body	Molded epoxy
Terminals	Copper alloy
Lead (Pb)-free Option	100 % matte tin
Tin Lead Option	Sn90
Tin Lead and Lead (Pb)-free Finish	Plated

Note

- Available upon request. Resistance value range and performance differs from passivated nichrome standard electrical specifications on datasheet, consult factory

ORDERING INFORMATION CHECK LIST (Customs)

Special requirements should be identified in advance, but as a minimum, you should have the following information ready.

ELECTRICAL	MECHANICAL
1. Resistors, by value and tolerance 2. Reference resistor(s) and matching of which resistors to which reference resistors 3. Reference by ratio 4. Absolute temperature coefficient of resistivity 5. Temperature tracking of subordinate resistors to reference resistor(s) 6. Maximum operating voltage 7. Resistor power ratings 8. Operating temperature range	1. Maximum allowable seated height (from PC board to top of network) 2. Special marking concerns 3. Schematic pin out of package

GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: NOMC16031002BUF

N	O	M	C	1	6	0	3	1	0	0	2	B	U	F
N	O	M	C	T	1	4	0	3	1	0	0	3	Z	T	1

GLOBAL MODEL (4 or 5 digits)	PINS	SCHEMATIC	RESISTANCE	TOLERANCE AND RATIO TOLERANCE	PACKAGING
NOMC (Tin Lead) **NOMCT** (Lead (Pb)-free) (e3)	**14** **16**	**01** = 13 or 15 bussed equal value resistors **03** = 7 or 8 isolated equal value resistors	First 3 digits are significant figures and the last digit specifies the number of zeros to follow. Example: 1002 = 10K 1003 = 100K	Abs. Tol. / Ratio **A** = 0.1 % [1] / 0.05 % **B** = 0.1 % / 0.1 % **C** = 0.25 % / 0.1 % **D** = 0.5 % / 0.1 % **F** = 1 % / 0.5 % **Z** = 0.1 % [1] / 0.025 %	TAPE AND REEL **T0** = 100 min., 100 mult **T1** = 1000 min., 1000 mult [2] **T3** = 300 min., 300 mult **T5** = 500 min., 500 mult **TF** = Full reel 2500 **TS** = 100 min., 1 mult **UF** = TUBED

Historical Part Number example: NOMC16031002Z (for reference purposes only)

NOMC	16	03	1002	Z
SERIES	PINS	SCHEMATIC	RESISTANCE	TOLERANCE AND RATIO TOLERANCE

Notes

(1) Tolerance available 1K and up

(2) Preferred packaging code

Vishay Dale Thin Film Land Patterns

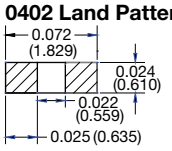
1. Scope

This technical note provides sample land patterns for Vishay Dale Thin Film SMT resistive products. The following drawings are based on IPC-SM-782 Surface Mount Design and Land Pattern Standard. These drawings are for reference only Vishay Thin Film recommends that the user contacts their PC board supplier for actual land patterns required. The pads are intended for lead (Pb)-free and tin / lead solder types.

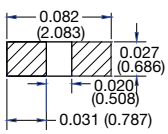
2. Product Series

Thin Film Surface Mount Chip Resistors (FC, L, P, PTN, PLT, PLTT. PLTU, PAT, PATT, PNM, M/D55342 QPL Series)

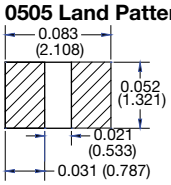
0402 Land Pattern



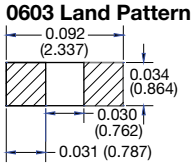
0502 Land Pattern



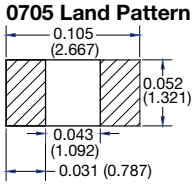
0505 Land Pattern



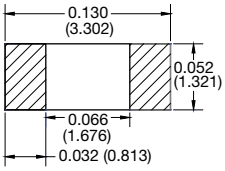
0603 Land Pattern



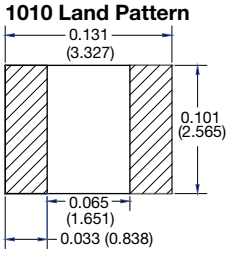
0705 Land Pattern



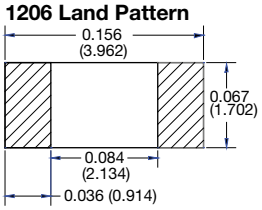
1005 Land Pattern



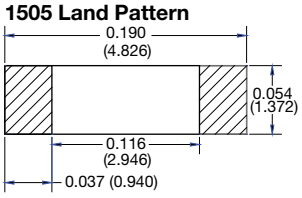
1010 Land Pattern



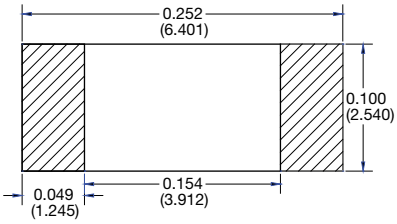
1206 Land Pattern



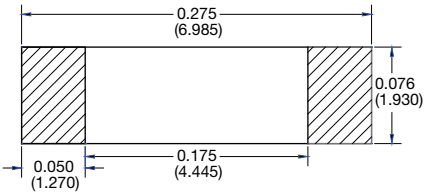
1505 Land Pattern



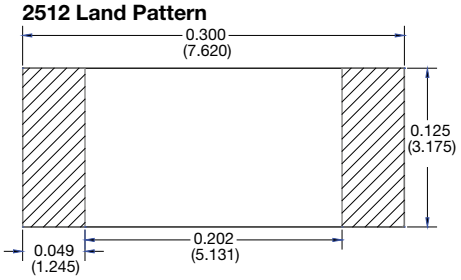
2010 Land Pattern



2208 Land Pattern

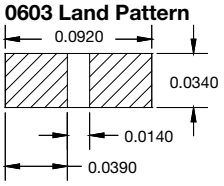


2512 Land Pattern

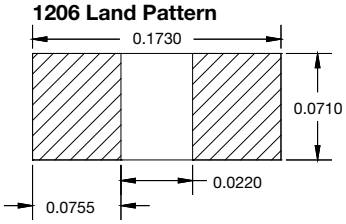


Thin Film Surface Mount Chip Resistors (PHP, PCAN Series)

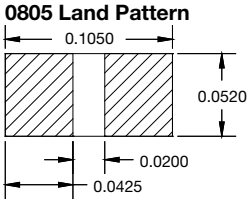
0603 Land Pattern



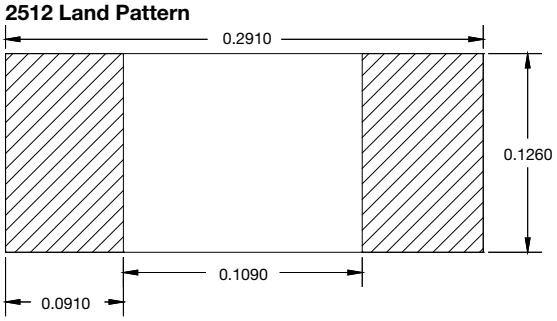
1206 Land Pattern



0805 Land Pattern

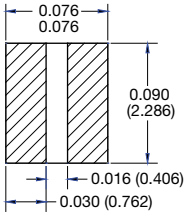


2512 Land Pattern

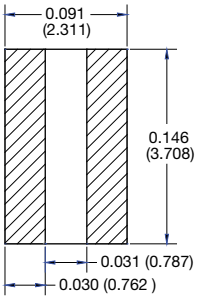


Thin Film Surface Mount Chip Resistors Long Axis Termination (L Series)

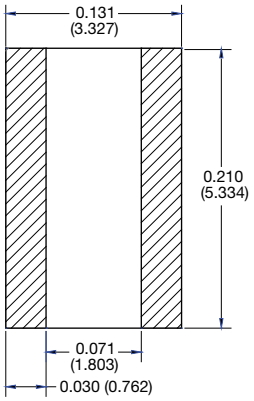
0508 Land Pattern



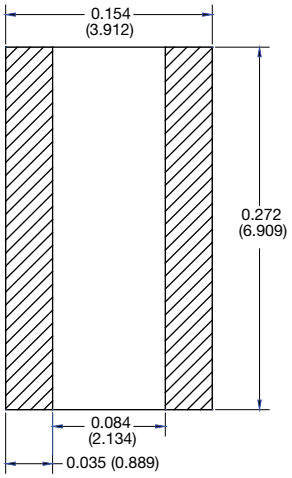
0612 Land Pattern



1020 Land Pattern

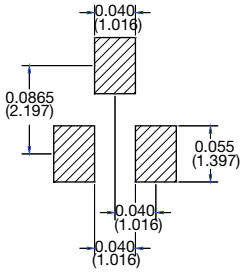


1225 Land Pattern

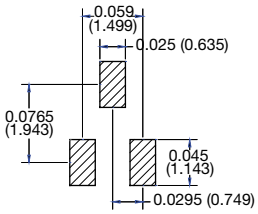


Surface Mount Networks (MPM, MP3, MP4 Series)

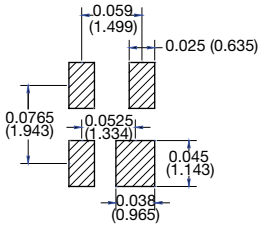
SOT-23 (MPM, MPMA)



SC70-3 (MP3)

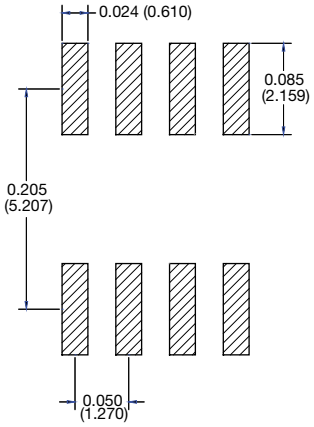


SC70-4 (MP4)

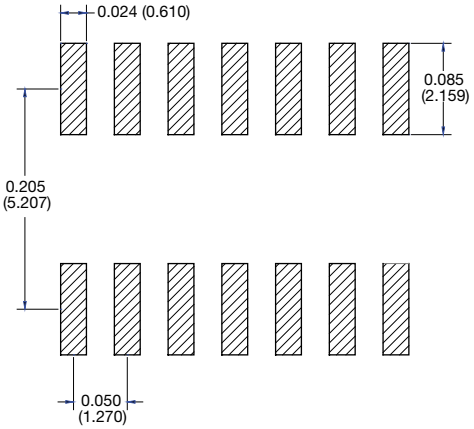


Surface Mount Networks SOIC Narrow Body 150 mils (ORN, CSO, MOMC, HTRN, AORN, MORN Series)

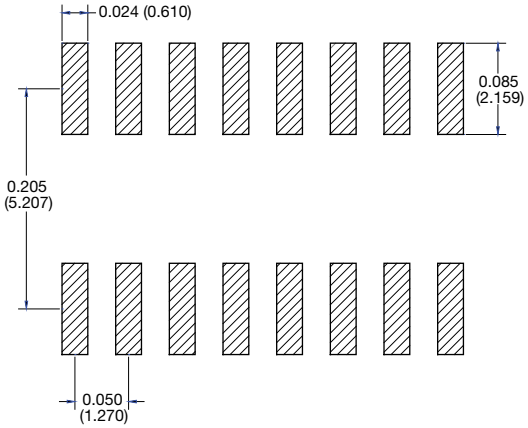
SOIC-8
(ORN, HTRN, AORN, CSO-8)



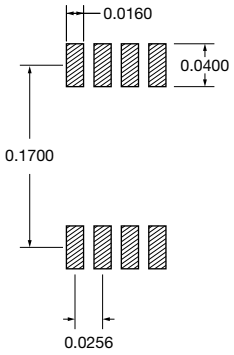
SOIC-14
(NOMC-14, NOMCA-14, CSO-14)



SOIC-16
(NOMC-16, NOMCA-16, CSO-16, VSOR-16)

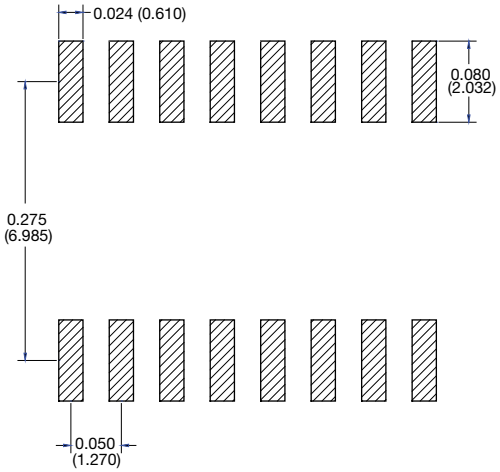


MORN MSOP MO-187AA
(MORN-8)



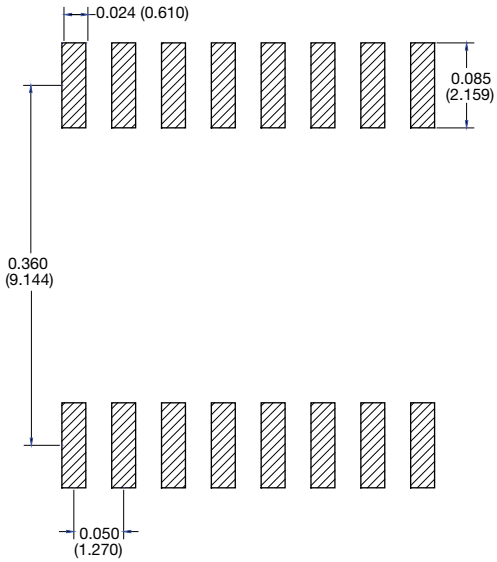
Surface Mount Networks SOIC Medium Body 220 mils (TOMC Series)

SOIC-16 (TOMC)

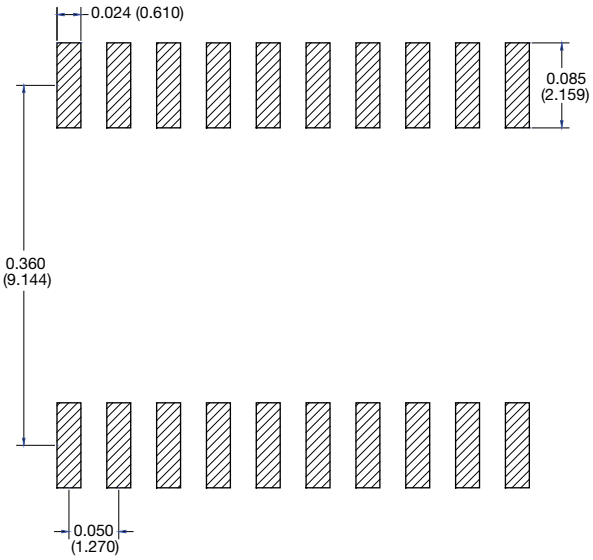


Surface Mount Networks SOIC Wide Body 300 mils (WOMC Series)

SOIC-16 (WOMC-16)



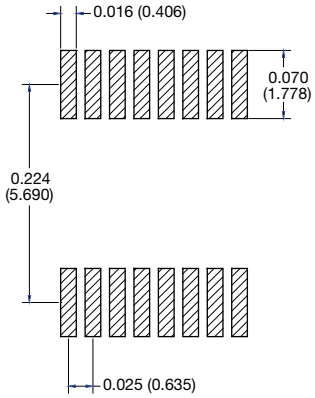
SOIC-20 (WOMC-20)



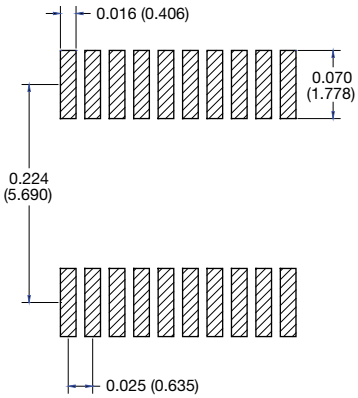
Surface Mount Networks High Density SSOP, TSOP (VSSR, VTSR Series)

SSOP MO-137

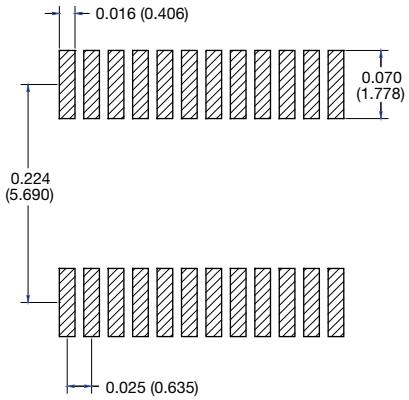
OSOP-16, VSSR-16



OSOP-20, VSSR-20

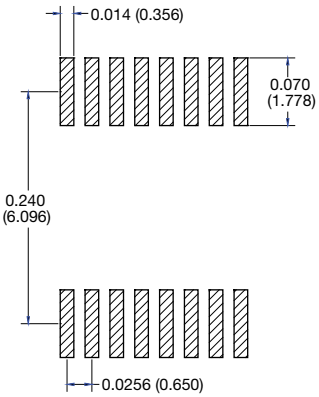


OSOP-24, VSSR-24, HD-CSO-24

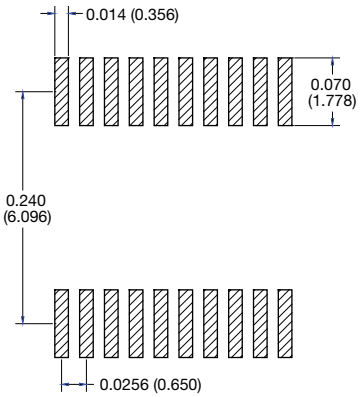


TSSOP MO-153

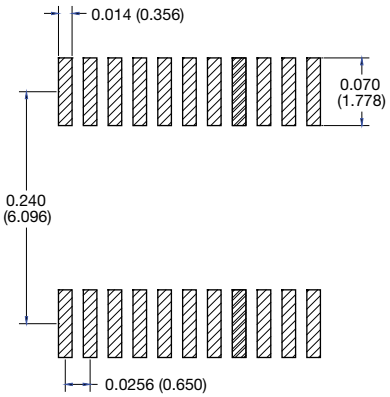
VTSR-16



VTSR-20

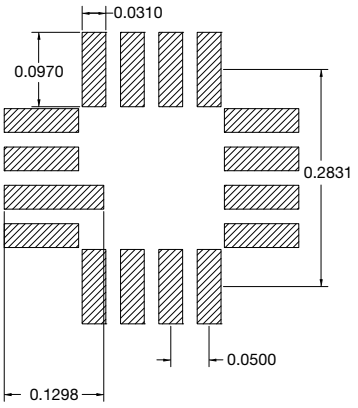


VTSR-24

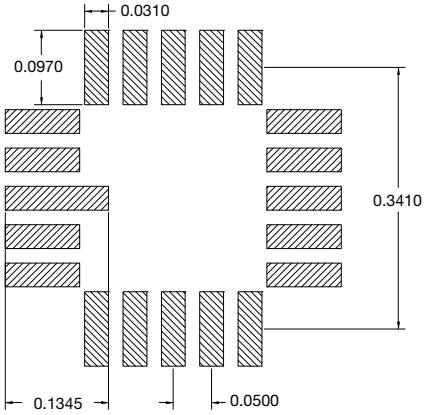


Surface Mount Leadless Networks (LCC Series)

16 Pin LCC

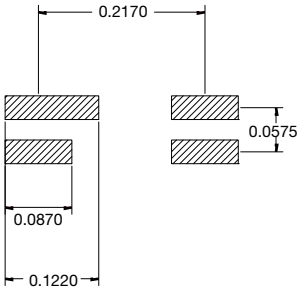


20 Pin LCC



Surface Mount Leadless Networks (MPH Series)

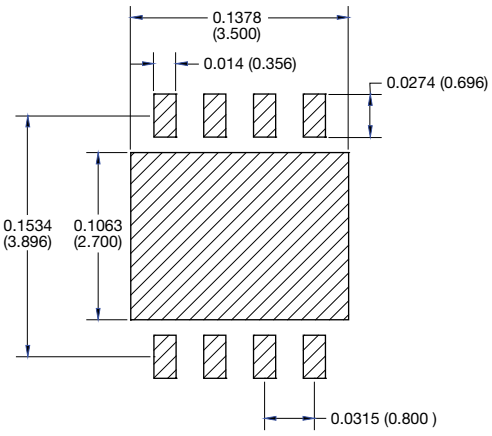
4 Pin LCC



Surface Mount Leadless Packages DUAL/ QUAD Flat No Lead (DFN, QFN Series)

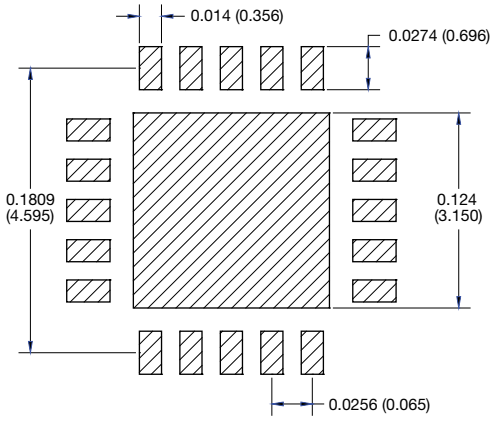
DFN MLP

DFN-8 4 x 5 mm Sq



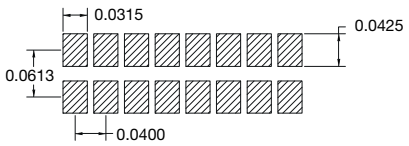
QFN MLP

QFN-20 5 x 5 mm Sq

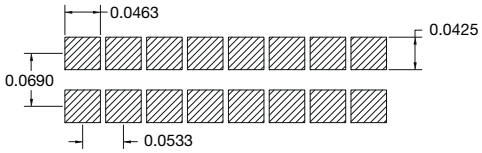


Surface Mount Leadless Resistor Arrays (PR Series)

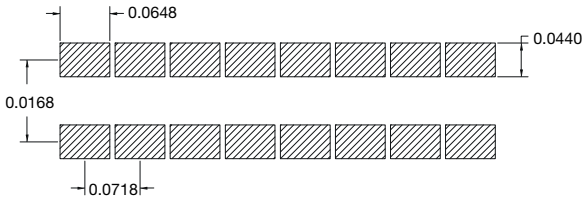
PR 100 (2 to 8 resistors)



PR 135 (2 to 8 resistors)



PR 182 (2 to 8 resistors)

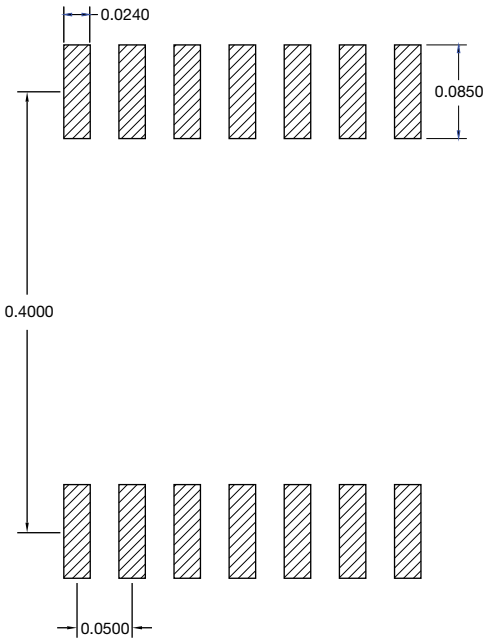


Note

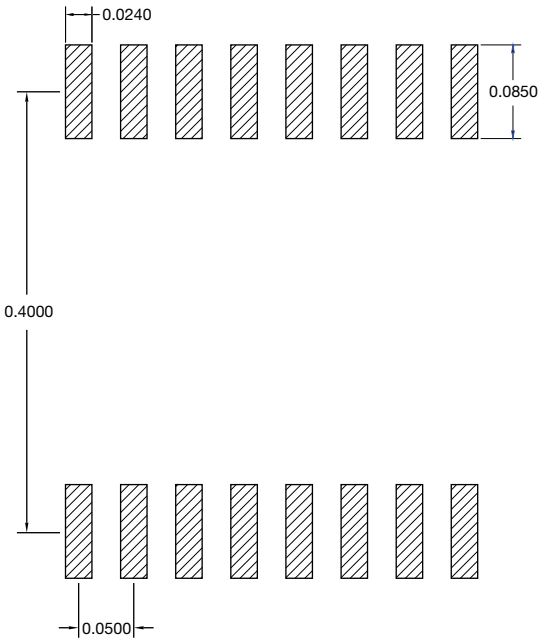
- All dimensions in inches (mm)

Flatpack

14 Pin Bottom Brazed Flatpack



16 Pin Bottom Brazed Flatpack



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