AUTOMOTIVE GRADE

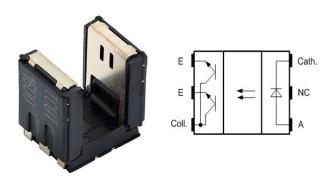
> HALOGEN FREE

> **GREEN**



### Vishay Semiconductors

# Tall Dome Dual Channel Transmissive Optical Sensor with Phototransistor Outputs



#### **DESCRIPTION**

The TCUT1600X01 is a compact transmissive sensor that includes an infrared emitter and two phototransistor detectors, located face-to-face in a surface mount package. The tall dome design supports additional mechanical room for vertical signal encoding.

### **FEATURES**

- Package type: surface mount
- · Detector type: phototransistor
- Dimensions (L x W x H in mm): 5.5 x 4 x 5.7
- AEC-Q101 qualified
- Gap (in mm): 3
- Aperture (in mm): 0.3
- Channel distance (center to center): 0.8 mm
- Typical output current under test: I<sub>C</sub> = 1.6 mA
- Emitter wavelength: 950 nm
- · Lead (Pb)-free soldering released
- Moisture sensitivity level (MSL): 1
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



- · Automotive optical sensors
- Accurate position sensor for encoder
- Sensor for motion, speed, and direction
- · Sensor for "turn and push" encoding

PRODUCT SUMMARY					
PART NUMBER	GAP WIDTH (mm)			DAYLIGHT BLOCKING FILTER INTEGRATED	
TCUT1600X01	3	0.3	1.6	No	

#### Note

<sup>(1)</sup> Conditions like in table basic characteristics/coupler

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	VOLUME (1)	REMARKS		
TCUT1600X01	Tape and reel	MOQ: 1300 pcs, 1300 pcs/reel	Drypack, MSL 1		

#### Note

(1) MOQ: minimum order quantity



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
COUPLER					
Total power dissipation	T <sub>amb</sub> ≤ 95 °C	P <sub>tot</sub>	37.5	mW	
Junction temperature		T <sub>j</sub>	110	°C	
Ambient temperature range		T <sub>amb</sub>	-40 to +105	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +125	°C	
Soldering temperature	In accordance with fig. 16	T <sub>sd</sub>	260	°C	
INPUT (EMITTER)					
Reverse voltage		$V_R$	5	V	
Forward current	T <sub>amb</sub> ≤ 95 °C	I <sub>F</sub>	25	mA	
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	200	mA	
Power dissipation	T <sub>amb</sub> ≤ 95 °C	P <sub>V</sub>	37.5	mW	
OUTPUT (DETECTOR)					
Collector emitter voltage		V <sub>CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		I <sub>C</sub>	20	mA	
Collector dark current	$T_{amb} = 85  ^{\circ}C,  V_{CE} = 5  V$	I <sub>CEO</sub>	3.3	μΑ	

### **ABSOLUTE MAXIMUM RATINGS**

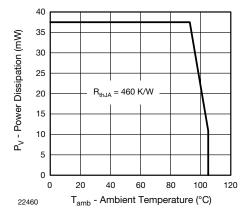


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

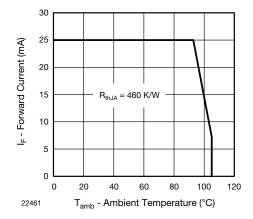


Fig. 2 - Forward Current Limit vs. Ambient Temperature



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
COUPLER						
Collector current per channel	$V_{CE} = 5 \text{ V}, I_F = 15 \text{ mA}$	I <sub>C</sub>	0.7	1.6	-	mA
Collector emitter saturation voltage	I <sub>F</sub> = 15 mA, I <sub>C</sub> = 0.2 mA	V <sub>CEsat</sub>	-	-	0.4	V
INPUT (EMITTER)						
Forward voltage	I <sub>F</sub> = 15 mA	V <sub>F</sub>	1	1.2	1.4	V
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μΑ
Junction capacitance	$V_R = 0 V, f = 1 MHz$	C <sub>j</sub>	-	25	-	pF
OUTPUT (DETECTOR)						
Collector emitter voltage I <sub>C</sub>	I <sub>C</sub> = 1 mA	$V_{CEO}$	20	-	-	V
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V
Collector dark current	$V_{CE} = 25 \text{ V}, I_F = 0 \text{ A}, E = 0 \text{ lx}$	I <sub>CEO</sub>	-	1	100	nA
SWITCHING CHARACTERISTIC	es					
Rise time	$I_C$ = 0.7 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see fig. 3)	t <sub>r</sub>	-	9	150	μs
Fall time	$I_C$ = 0.7 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see fig. 3)	t <sub>f</sub>	-	16	150	μs

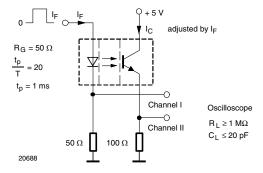


Fig. 3 - Test Circuit for  $t_{\text{r}}$  and  $t_{\text{f}}$ 

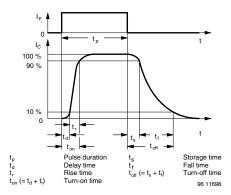


Fig. 4 - Switching Times

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

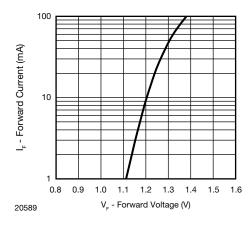


Fig. 5 - Forward Current vs. Forward Voltage

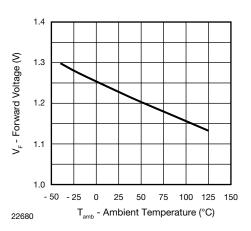


Fig. 6 - Forward Voltage vs. Ambient Temperature



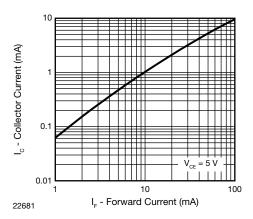


Fig. 7 - Collector Current vs. Forward Current

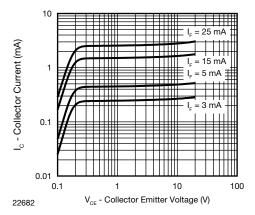


Fig. 8 - Collector Current vs. Collector Emitter Voltage

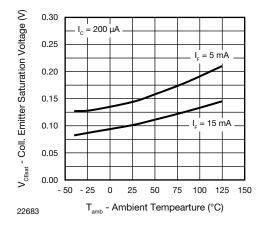


Fig. 9 - Collector Emitter Saturation Voltage vs.
Ambient Temperature

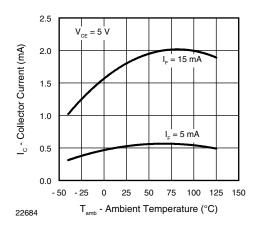


Fig. 10 - Collector Current vs. Ambient Temperature

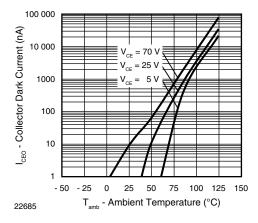


Fig. 11 - Collector Dark Current vs. Ambient Temperature

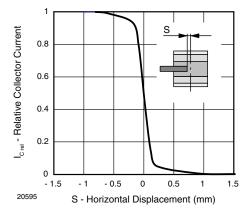


Fig. 12 - Relative Collector Current vs. Horizontal Displacement



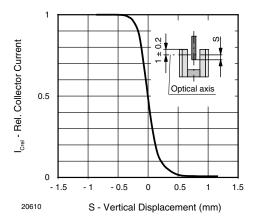


Fig. 13 - Relative Collector Current vs. Vertical Displacement

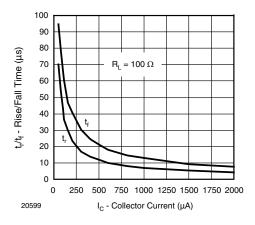


Fig. 14 - Rise/Fall Time vs. Collector Current

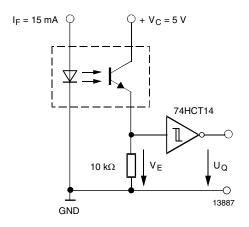


Fig. 15 - Application example

### **REFLOW SOLDER PROFILE**

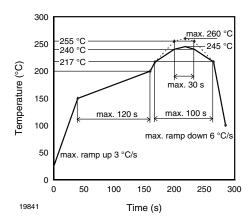
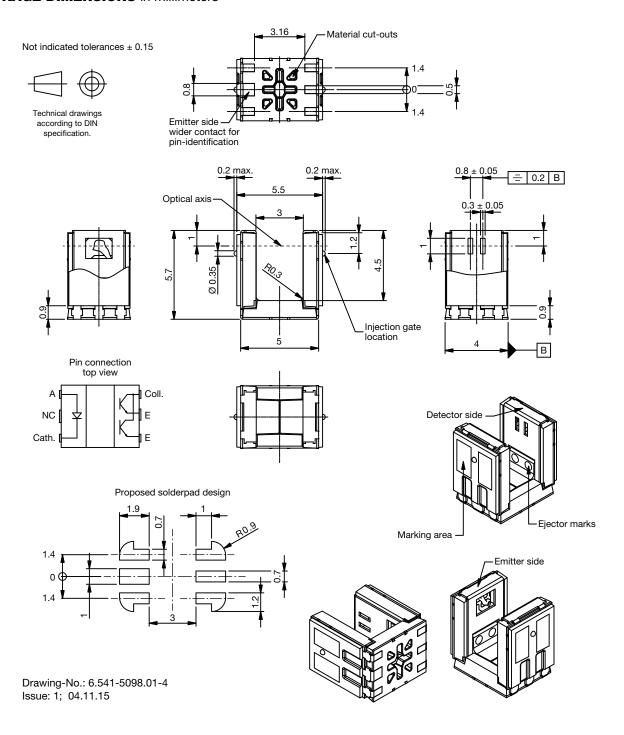


Fig. 16 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

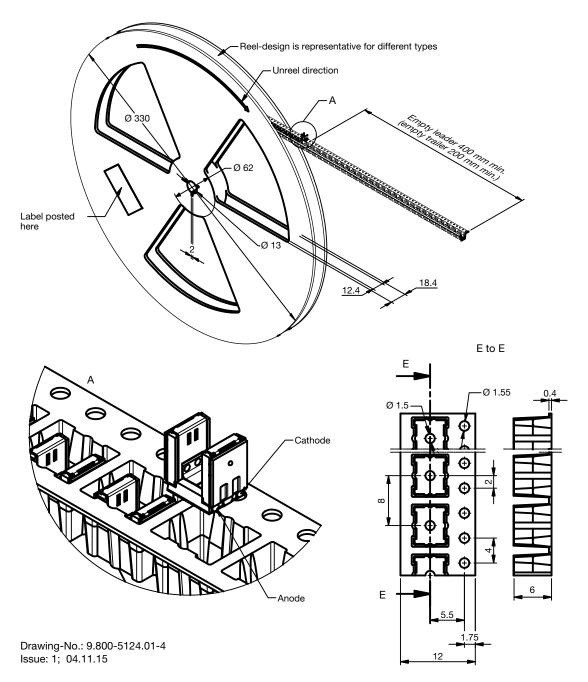
### **FLOOR LIFE**

Level 1, acc. JEDEC®, J-STD-020. No time limit.

### **PACKAGE DIMENSIONS** in millimeters



### **PACKAGE DIMENSIONS** in millimeters





### **Legal Disclaimer Notice**

Vishay

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Revision: 02-Oct-12 Document Number: 91000