

### **Vishay Semiconductors**

## **Silicon NPN Phototransistor**



### DESCRIPTION

VEMT2003X01 series are silicon NPN epitaxial planar phototransistors with daylight blocking filter in a miniature, black dome lens package for surface mounting. Filter bandwidth is matched with 830 nm to 950 nm IR emitters.

#### FEATURES

- · Package type: surface mount
- Package form: GW, RGW
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.55
- AEC-Q101 qualified
- High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- Fast response times
- Angle of half sensitivity:  $\phi = \pm 35^{\circ}$
- Package matched with IR emitter series VSMB2943RGX01 and VSMB2943GX01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- Detector in automotive applications
- Photo interrupters
- Miniature switches
- Counters
- Encoders
- Position sensors

PRODUCT SUMMARY			
COMPONENT	I <sub>ca</sub> (mA)	φ (deg)	λ <sub>0.5</sub> (nm)
VEMT2003X01	2.7	± 35	790 to 970
VEMT2023X01	2.7	± 35	790 to 970

#### Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMT2003X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing	
VEMT2023X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing	

Note

• MOQ: minimum order quantity





COMPLIANT HALOGEN FREE <u>GREEN</u> (5-2008)



## Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V <sub>CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		Ι <sub>C</sub>	50	mA	
Power power dissipation	T <sub>amb</sub> ≤ 75 °C	Pv	100	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C	
Soldering temperature	Acc. reflow profile fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R <sub>thJA</sub>	250	K/W	

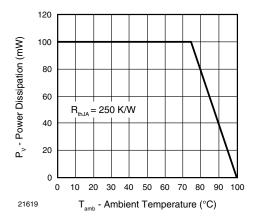


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I <sub>C</sub> = 0.1 mA	V <sub>CEO</sub>	20			V
Collector dark current	$V_{CE} = 5 V, E = 0$	I <sub>CEO</sub>		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 V, f = 1 MHz, E = 0$	C <sub>CEO</sub>		25		pF
Collector light current	$\begin{array}{l} E_{e} = 1 \text{ mW/cm}^2,  \lambda = 950 \text{ nm}, \\ V_{CE} = 5 \text{ V} \end{array}$	I <sub>ca</sub>	1.3	2.7	4.1	mA
Angle of half sensitivity		φ		± 35		deg
Wavelength of peak sensitivity		λρ		860		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		790 to 970		nm
Collector emitter saturation voltage	$I_{\rm C} = 0.05 \ {\rm mA}$	V <sub>CEsat</sub>			0.4	V
Temperature coefficient of Ica	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_{CE} = 5 \text{ V}$	Tk <sub>lca</sub>		1.1		%/K

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### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

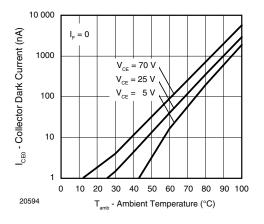


Fig. 2 - Collector Dark Current vs. Ambient Temperature

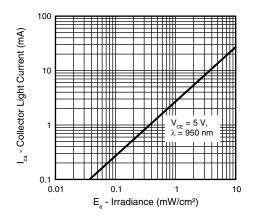


Fig. 3 - Collector Light Current vs. Irradiance

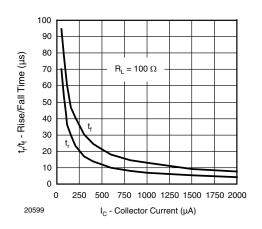


Fig. 4 - Rise/Fall Time vs. Collector Current

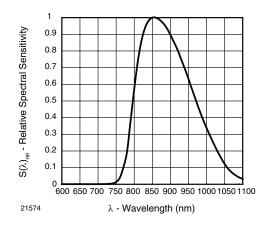


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

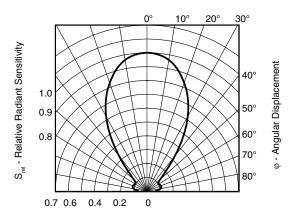


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

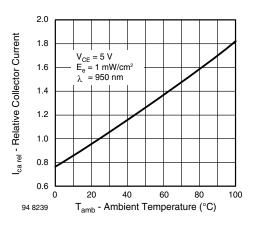


Fig. 7 - Relative Collector Current vs. Ambient Temperature

Rev. 1.0, 08-Apr-13

3 For technical questions, contact: <u>detectortechsupport@vishay.com</u> Document Number: 84162

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### **REFLOW SOLDER PROFILE**

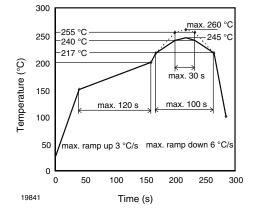


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

50

0.75

Collector

17

2.3

## VEMT2003X01, VEMT2023X01

### **Vishay Semiconductors**

### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

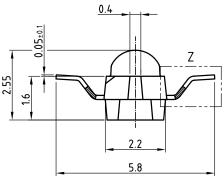
### **FLOOR LIFE**

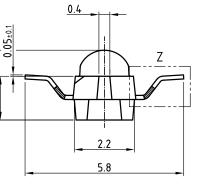
Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions:  $T_{amb} < 30\ ^\circ C,\ RH < 60\ \%$ Moisture sensitivity level 2a, acc. to J-STD-020.

### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



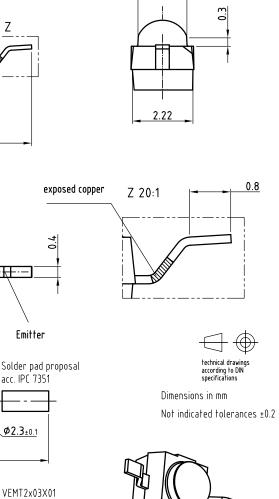




2.3

Pin ID

6.7



Ø1.8

Drawing refers to following types:

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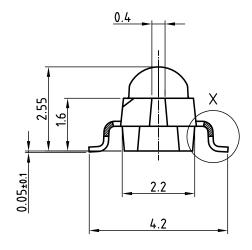
Emitter

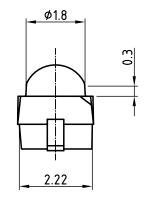
Ø2.3±0.1

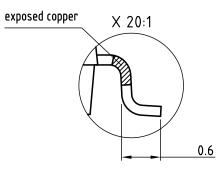


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### PACKAGE DIMENSIONS VEMT2023X01 in millimeters



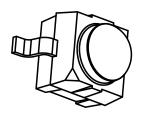


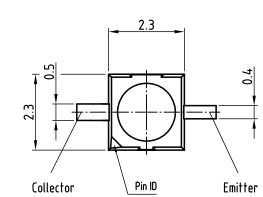


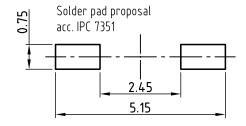


according to DIN specifications Dimensions in mm

Not indicated tolerances ±0.2



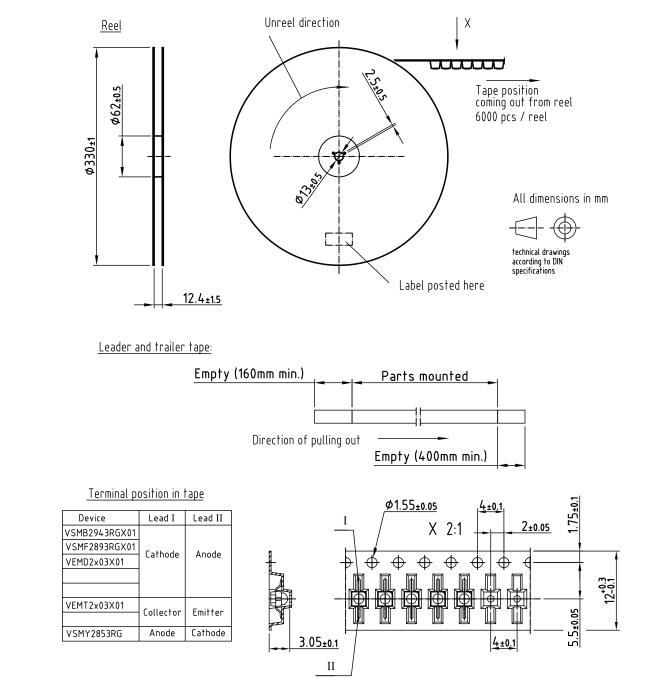




Drawing refers to following types:

VEMT2x23X01

Drawing-No.: 6.544-5408.02-4 Issue: prel; 03.08.12



Drawing refers to following types: Reel dimensions and tape

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TAPE AND REEL DIMENSIONS VEMT2003X01 in millimeters

Drawing-No.: 9.800-5100.02-4 Issue: prel; 03.08.12

VEMT2003X01, VEMT2023X01

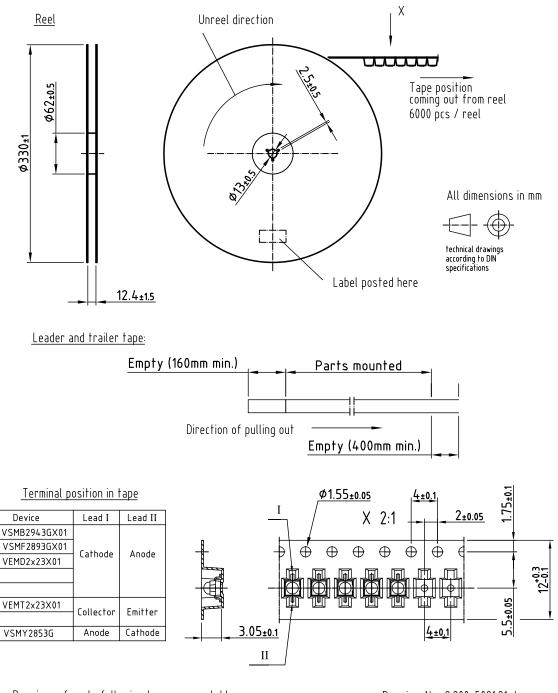
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see table



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### TAPE AND REEL DIMENSIONS VEMT2023X01 in millimeters



Drawing refers to following types: see table Reel dimensions and tape Drawing-No.: 9.800-5091.21-4 Issue: prel; 03.08.12

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