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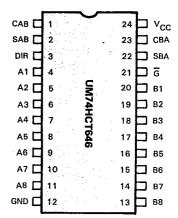
T-52-31

UM74HCT646 Octal Bus Transceivers and Register

FEATURES

- 8 Bi-directional Data Bus
- Transmits Directly Read-time Data or Stored Data in the Internal Register in Either Direction
- 3-state Outputs with Driving Capability for Direct Bus Interface
- Low Power Consumption with Function, Pin-out, Speed and Drive Compatibility with 74LS Logic Family and Interfaces Directly with TTL, NMOS and CMOS Dovices

PIN CONFIGURATION



UM2332 4Kx8 Read Only Memory

FEATURES

- Access Time 200/300/450 ns (Max.)
- Single +5V ± 5% Power Supply
- TTL Compatible Inputs and Outputs
- Three-state Outputs
- Pin Compatible with 2532 EPROM
- Two Programmable Chip Selects for Output Control
- N-channel Silicon Gate Technology

PIN CONFIGURATION

