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High Speed ADC to HSMC (Altera) Header Adaptor Card

(ACTIVE) HSMC-ADC-BRIDGE

Description & Features

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Description

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The HSMC-ADC-Bridge passive interconnect board enables the output of TI's LVDS output high speed ADCs to be directly connected to a standard HSMC interconnect header, a typical input on the latest Altera FPGA EVMs. This enables users of TI's high speed data converter EVMs to directly interface to Altera FPGA's for prototyping purposes, saving the time and cost of producing a custom prototyping board.

Features

- Enables direct connection from TI high speed ADC EVMs with LVDS outputs to the HSMC standard header

What's Included

- SLOR100, HSMC-ADC-Bridge Physical Design Database. (Rev. A)

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Part Number	Buy from Texas Instruments or Third Party	Buy from Authorized Distributor	Status	Lead-Free	RoHS	REACH	WEEE	HI-V (>50VRMS/75 VDC)	CE	CE-EMC	CE-RTTE/LVD	FCC	Batteries	Enclosure	Ext Power Supply
HSMC-ADC-BRIDGE: High Speed ADC to HSMC (Altera) Header Adaptor Card	\$49.00(USD) In Stock Typically Ships in 1 to 3 Business Days Buy from TI	Pricing may vary. Buy from distributor	ACTIVE	Yes	Yes	Yes	NA	NA	NA	NA	NA	NA	No	No	No

Contact a Distributor

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Technical Documents

Design Files (1)

Title	Abstract	Type	Size (KB)	Date	Views
HSMC-ADC-Bridge Physical Design Database. (Rev. A)		ZIP	1755	13 Oct 2011	401

More Literature (1)

Title	Abstract	Type	Size (KB)	Date	Views
TI and Altera Ease Design Process with Compatible Evaluation Tools		PDF	1046	25 Apr 2011	380

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