



# **Surface Mount Ultrafast Power Rectifiers**

#### **FEATURES**

- Glass passivated chip junction
- Ideal for automated placement
- Ultrafast recovery time for high efficiency
- Low forward voltage, low power loss
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition







### DO-214AA (SMB)

### MECHANICAL DATA

Case: DO-214AA (SMB)

Molding compound, UL flammability classification rating 94V-0

Base P/N with suffix "G" on packing code - green compound (halogen-free)

Base P/N with prefix "H" on packing code - AEC-Q101 qualified **Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 1A whisker test

with prefix "H" on packing code meet JESD 201 class 2 whisker test

**Polarity:** Indicated by cathode band **Weight:** 0.09 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T <sub>A</sub> =25 <sup>°</sup> ℂ unless otherwise noted)								
PARAMETER	SYMBOL	MUR	MUR	MUR	MUR	MUR	MUR	Unit
FARAIVIETER		105S	110S	115S	120S	140S	160S	
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	150	200	400	600	V
Maximum RMS voltage	$V_{RMS}$	35	70	105	140	280	420	V
Maximum DC blocking voltage	$V_{DC}$	50	100	150	200	400	600	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	1		Α				
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	40		35		Α		
Maximum instantaneous forward voltage (Note 1) @ 1 A, $T_J$ =25 $^{\circ}$ C @ 1 A, $T_J$ =150 $^{\circ}$ C	V <sub>F</sub>				25 05	V		
Maximum reverse current @ rated VR $T_J$ =25 $^{\circ}$ C $T_J$ =150 $^{\circ}$ C	I <sub>R</sub>	2 5 50 150			μΑ			
Maximum reverse recovery time (Note 2)	Trr	25		5	0	ns		
Typical thermal resistance	$R_{ heta jL}$	17		°C/W				
Operating junction temperature range	T <sub>J</sub>	- 55 to +175		οС				
Storage temperature range	T <sub>STG</sub>	- 55 to +175			оС			

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Reverse Recovery Test Conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A

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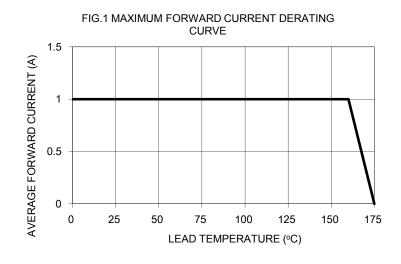
ORDERING INFORMATION						
PART NO.	AEC-Q101	PACKING CODE	GREEN COMPOUND	PACKAGE	PACKING	
	QUALIFIED		CODE			
MUDA		R5		SMB	850 / 7" Plastic reel	
MUR1xxS (Note 1)	Prefix "H"	R4	Suffix "G"	SMB	3,000 / 13" Paper reel	
(11010 1)		M4		SMB	3,000 / 13" Plastic reel	

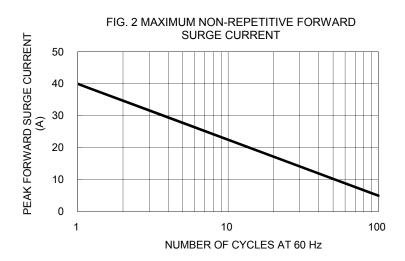
Note 1: "xx" defines voltage from 50V (MUR105S) to 600V (MUR160S)

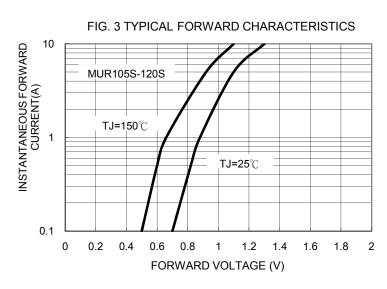
EXAMPLE					
PREFERRED P/N	PART NO.	AEC-Q101 QUALIFIED	PACKING CODE	GREEN COMPOUND CODE	DESCRIPTION
MUR160S R5	MUR160S		R5		
MUR160S R5G	MUR160S		R5	G	Green compound
MUR160SHR5	MUR160S	Н	R5		AEC-Q101 qualified

#### RATINGS AND CHARACTERISTICS CURVES

(TA=25°C unless otherwise noted)







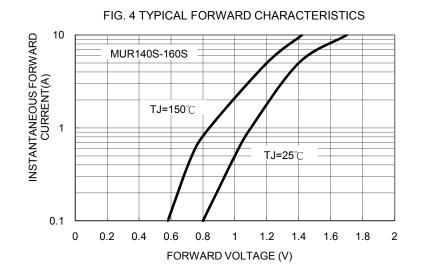




FIG. 5 TYPICAL REVERSE CHARACTERISTICS

1000

MUR105S-120S

10

TJ=150°C

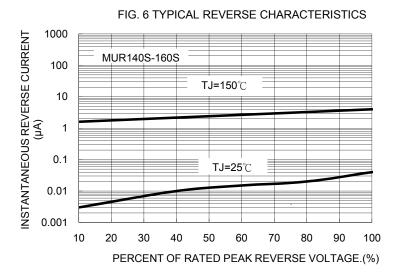
TJ=25°C

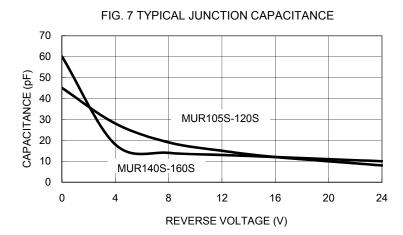
0.01

0.001

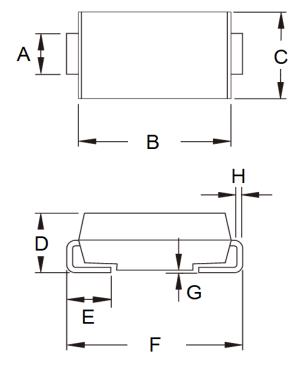
10 20 30 40 50 60 70 80 90 100

PERCENT OF RATED PEAK REVERSE VOLTAGE.(%)

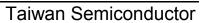




#### PACKAGE OUTLINE DIMENSIONS

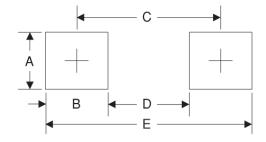


DIM.	Unit	(mm)	Unit (inch)		
DIIVI.	Min	Max	Min	Max	
Α	1.95	2.10	0.077	0.083	
В	4.25	4.75	0.167	0.187	
С	3.48	3.73	0.137	0.147	
D	1.99	2.61	0.078	0.103	
Е	0.90	1.41	0.035	0.056	
F	5.10	5.30	0.201	0.209	
G	0.10	0.20	0.004	0.008	
Н	0.15	0.31	0.006	0.012	





# SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	2.3	0.091
В	2.5	0.098
С	4.3	0.169
D	1.8	0.071
E	6.8	0.268

# MARKING DIAGRAM



P/N = Specific Device Code

G = Green Compound

YW = Date Code

F = Factory Code





Taiwan Semiconductor

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