

Taiwan Semiconductor

1.5A, 400V - 1000V Glass Passivated Bridge Rectifier

FEATURES

- Ideal for automated placement
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- UL Recognized File # E-326854
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application

MECHANICAL DATA

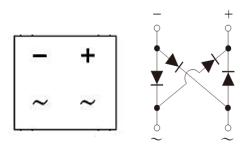
- Case: DBLS
- Molding compound :meets UL 94V-0 flammability rating
- Packing code with suffix "G" means green compound (halogen-free)
- Moisture sensitivity level: level 1, per J-STD-020
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: As marked
- Weight: 0.36 g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _{F(AV)}	1.5	А		
V _{RRM}	400 - 1000	V		
I _{FSM}	50	А		
T _{J MAX}	150 °C			
Package	DBLS			
Configuration	Quad			









ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)						
PARAMETER	SYMBOL	DBLS 154G-T	DBLS 155G-T	DBLS 156G-T	DBLS 157G-T	UNIT
Marking code on the device		DBLS154G	DBLS155G	DBLS156G	DBLS157G	
Repetitive peak reverse voltage	V _{RRM}	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	400	600	800	1000	V
Forward current	I _{F(AV)}	1.5			А	
Surge peak forward current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	50		A		
I ² t value (of a surge on-state current)	l ² t		1().3		A ² s
Junction temperature	TJ		-55 tc	+150		°C
Storage temperature	T _{STG}	-55 to +150			°C	



THERMAL PERFORMANCE				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-lead thermal resistance	$R_{\Theta JL}$	15	°C/W	
Junction-to-ambient thermal resistance	R _{eja}	40	°C/W	

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Forward voltage ⁽¹⁾	I _F = 1.5A, T _J = 25°C	V _F	-	1.1	V
	T _J = 25°C		-	2	μA
Reverse current @ rated $V_R^{(2)}$	T _J = 125°C	- I _R	-	500	μA
Junction capacitance	1 MHz, V _R =4.0V	CJ	25	-	pF

Notes:

1. Pulse test with PW=0.3 ms

2. Pulse test with PW=30 ms.

ORDERING INFORMATION				
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING
DBLS15xG-T	C1	0	DBLS	50 / TUBE
(Note 1, 2)	RD	G	DBLS	1,500 / 13" Paper reel

Notes:

1. "x" defines voltage from 400V (DBLS154G-T) to 1000V (DBLS157G-T)

2. Whole series with green compound (halogen-free)

EXAMPLE P/N				
EXAMPLE P/N	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
DBLS154G-T C1G	DBLS154G-T	C1	G	Green compound



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CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

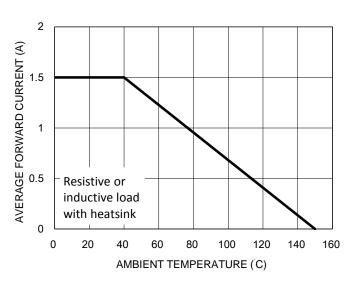
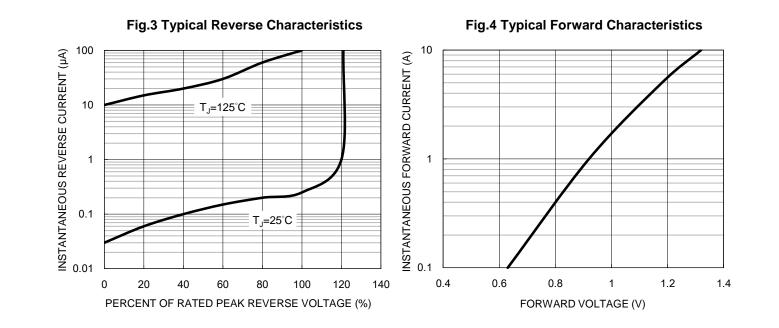


Fig.1 Forward Current Derating Curve

Fig.2 Typical Junction Capacitance

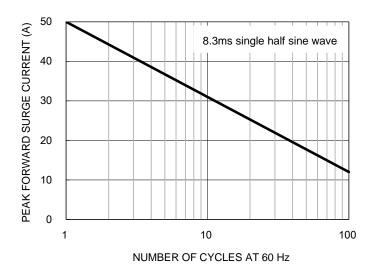




CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.5 Maximum Non-repetitive Forward Surge Current

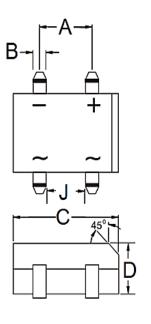


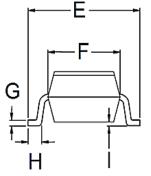


DBLS154G-T - DBLS157G-T Taiwan Semiconductor

PACKAGE OUTLINE DIMENSIONS

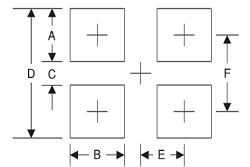
DBLS





DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
А	5.00	5.20	0.197	0.205
В	1.02	1.20	0.040	0.047
С	8.13	8.51	0.320	0.335
D	2.35	2.60	0.093	0.102
Е	9.80	10.30	0.386	0.406
F	6.20	6.50	0.244	0.256
G	0.22	0.33	0.009	0.013
Н	1.02	1.53	0.040	0.060
I	0.076	0.33	0.003	0.013
J	3.90	4.10	0.154	0.161

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
А	2.3	0.091
В	1.3	0.051
С	6.9	0.272
D	11.5	0.453
Е	2.6	0.102
F	9.2	0.362

MARKING DIAGRAM



- P/N = Specific Device Code
- G = Green Compound
- = Date Code YW
- F = Factory Code



DBLS154G-T - DBLS157G-T

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