CMOS Digital Integrated Circuits Silicon Monolithic

# TC7SET14FU

### 1. Functional Description

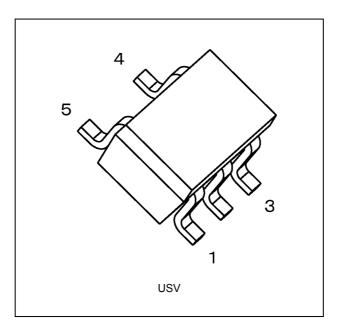
Schmitt Inverter

#### 2. Features

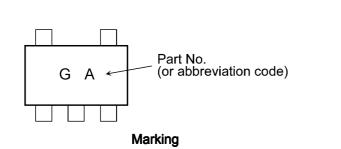
- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C (Note 2)
- (3) High speed operation:  $t_{pd} = 5.0$  ns (typ.) ( $V_{CC} = 5.0$  V,  $C_L = 15$  pF)
- (4) Low power dissipation:  $I_{CC} = 2.0 \ \mu A \ (max) \ (T_a = 25 \ ^\circ C)$
- (5) Compatible with TTL outputs
- (6) 5.5 V tolerant inputs
- Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

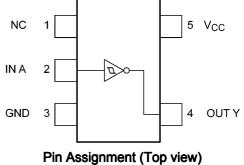
Note 2: For devices with the ordering part number TC7SET14FU,LJ(CT.  $T_{opr}$  = -40 to 85 °C for the other devices.

#### 3. Packaging



4. Marking and Pin Assignment





Start of commercial production 2004-02

## TOSHIBA

#### 5. IEC Logic Symbol



#### 6. Truth Table

А	Y
L	Н
Н	L

#### 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 7.0	V
Input voltage	V <sub>IN</sub>		-0.5 to 7.0	
DC output voltage	V <sub>OUT</sub>		-0.5 to V <sub>CC</sub> + 0.5	
Input diode current	I <sub>IK</sub>		-20	mA
Output diode current	Ι <sub>ΟΚ</sub>	(Note 1)	±20	
DC output current	I <sub>OUT</sub>		±25	
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	
Power dissipation	PD		200	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{OUT}$  < GND,  $V_{OUT}$  >  $V_{CC}$ 

#### 8. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		4.5 to 5.5	V
Input voltage	V <sub>IN</sub>		0 to 5.5	
Output voltage	V <sub>OUT</sub>		0 to V <sub>CC</sub>	
Operating temperature	T <sub>opr</sub>	(Note 1)	-40 to 125	°C
		(Note 2)	-40 to 85	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1: For devices with the ordering part number TC7SET14FU,LJ(CT.

Note 2: For devices except those with the ordering part number TC7SET14FU,LJ(CT.

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#### 9. Electrical Characteristics

## 9.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Unit	
Positive threshold voltage	V <sub>P</sub>	—		4.5	_	_	1.9	V
				5.5	_	_	2.1	1
Negative threshold voltage	V <sub>N</sub>	—	_		0.5	_	_	V
				5.5	0.6	_	_	]
Hysteresis voltage	V <sub>H</sub>			4.5	0.4	_	1.4	V
				5.5	0.4	_	1.5	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -50 μA	4.5	4.4	4.5	_	V
			I <sub>OH</sub> = -8 mA	4.5	3.94	_	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 50 μA	4.5	_	0.0	0.1	V
			I <sub>OL</sub> = 8 mA	4.5		_	0.36	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	μA
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	μA
	I <sub>CCT</sub>	V <sub>IN</sub> = 3.4 V		5.5	_	_	1.35	mA

### 9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Conditior	V <sub>CC</sub> (V)	Min	Max	Unit	
Positive threshold voltage	VP	—		4.5	_	1.9	V
				5.5	_	2.1	
Negative threshold voltage	V <sub>N</sub>	—		4.5	0.5	—	V
				5.5	0.6	_	
Hysteresis voltage	V <sub>H</sub>	_		4.5	0.4	1.4	V
				5.5	0.4	1.5	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -50 μA	4.5	4.4	_	V
			I <sub>OH</sub> = -8 mA	4.5	3.80	—	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 50 μA	4.5	_	0.1	V
			I <sub>OL</sub> = 8 mA	4.5	_	0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±1.0	μA
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	20.0	μA
	I <sub>CCT</sub>	V <sub>IN</sub> = 3.4 V		5.5		1.50	mA

#### 9.3. DC Characteristics (Note) (Unless otherwise specified, $T_a = -40$ to 125 °C)

Characteristics	Symbol	Test Conditior	V <sub>CC</sub> (V)	Min	Max	Unit	
Positive threshold voltage	VP	—		4.5	_	1.9	V
				5.5	_	2.1	
Negative threshold voltage	V <sub>N</sub>	_		4.5	0.5	_	V
				5.5	0.6	_	
Hysteresis voltage	V <sub>H</sub>	_		4.5	0.4	1.4	V
				5.5	0.4	1.5	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -50 μA	4.5	4.4	_	V
			I <sub>OH</sub> = -8 mA	4.5	3.70	—	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 50 μA	4.5	_	0.1	V
			I <sub>OL</sub> = 8 mA	4.5	_	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	±2.0	μA
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	40.0	μA
	I <sub>CCT</sub>	V <sub>IN</sub> = 3.4 V		5.5	_	1.50	mA

Note: For devices with the ordering part number TC7SET14FU,LJ(CT.

#### 9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		—	$5.0\pm0.5$	15	_	5.0	7.6	ns
					50	_	6.5	9.6	
Input capacitance	C <sub>IN</sub>		_	<u> </u>		_	4	10	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	_				18		pF

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# 9.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	—	$5.0\pm0.5$	15	1.0	9.0	ns
				50	1.0	11.0	
Input capacitance	C <sub>IN</sub>	_				10	pF

#### 9.6. AC Characteristics (Note) (Unless otherwise specified, T<sub>a</sub> = -40 to 125 °C, Input: t<sub>r</sub> = t<sub>f</sub> = 3 ns)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	—	$5.0\pm0.5$	15	1.0	9.5	ns
				50	1.0	12.0	
Input capacitance	C <sub>IN</sub>	—			_	10	рF

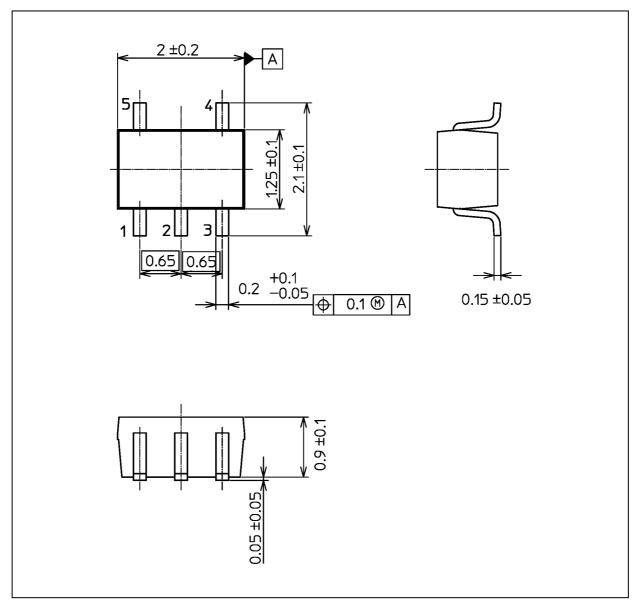
Note: For devices with the ordering part number TC7SET14FU,LJ(CT.



#### **Package Dimensions**

TC7SET14FU

Unit: mm



Weight: 0.006 g (typ.)

	Package Name(s)	
JEDEC: SOT-353		
Nickname: USV		

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