Notice for TAIYO YUDEN products

Please read this notice before using the TAIYO YUDEN products.

REMINDERS

Product information in this catalog is as of October 2011. All of the contents specified herein are subject to change without notice due to technical improvements, etc. Therefore, please check for the latest information carefully before practical application or usage of the Products.

Please note that Taiyo Yuden Co., Ltd. shall not be responsible for any defects in products or equipment incorporating such products, which are caused under the conditions other than those specified in this catalog or individual specification.

- Please contact Taiyo Yuden Co., Ltd. for further details of product specifications as the individual specification is available.
- Please conduct validation and verification of products in actual condition of mounting and operating environment before commercial shipment of the equipment.
- All electronic components or functional modules listed in this catalog are developed, designed and intended for use in general electronics equipment.(for AV, office automation, household, office supply, information service, telecommunications, (such as mobile phone or PC) etc.). Before incorporating the components or devices into any equipment in the field such as transportation,(automotive control, train control, ship control), transportation signal, disaster prevention, medical, public information network (telephone exchange, base station) etc. which may have direct influence to harm or injure a human body, please contact Taiyo Yuden Co., Ltd. for more detail in advance. Do not incorporate the products into any equipment in fields such as aerospace, aviation, nuclear control, submarine system, military, etc. where higher safety and reliability are especially required.

In addition, even electronic components or functional modules that are used for the general electronic equipment, if the equipment or the electric circuit require high safety or reliability function or performances, a sufficient reliability evaluation check for safety shall be performed before commercial shipment and moreover, due consideration to install a protective circuit is strongly recommended at customer's design stage.

- The contents of this catalog are applicable to the products which are purchased from our sales offices or distributors (so called "TAIYO YUDEN's official sales channel").

 It is only applicable to the products purchased from any of TAIYO YUDEN's official sales channel.
- Please note that Taiyo Yuden Co., Ltd. shall have no responsibility for any controversies or disputes that may occur in connection with a third party's intellectual property rights and other related rights arising from your usage of products in this catalog. Taiyo Yuden Co., Ltd. grants no license for such rights.
- Caution for export

Certain items in this catalog may require specific procedures for export according to "Foreign Exchange and Foreign Trade Control Law" of Japan, "U.S. Export Administration Regulations", and other applicable regulations. Should you have any question or inquiry on this matter, please contact our sales staff.



REFLOW

FEATURES

- Low equivalent series resistance (ESR).
- Low equivalent series inductor (ESL).
- The effect of noise removal in the high frequency.
- Decreased ripple voltage.
- Small size with high capacitance.

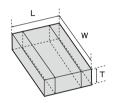
PART NUMBER

APPLICATIONS

- Decoupling capacitors
- Filtering capacitors

 J W	/	2	2	1	2 △	E	3 J	_ 1		0	6	M	D		Т	
DRated age (VDC) 4 6.3 10 16 25	termin K P	end nation Plated		4 ◆Dimen: (inch) (0204) (0306) (0508) ◆	L×W(mm) 0.52×1.0 0.8×1.6 1.25×2.0 ension tolerance Standard △=Blank space		emperature acteristics code B X5R X6S X7S X7R		K M minal [µ	apacitance elerance ±10% ±20% capacitance (F) 1.0 10.0	P V D	(mm) 0.3 0.5 0.85	F	OSpecial code Standard OPackaging \$\phi\$178mm Tapi (2mm pitch) 0204 Type \$\phi\$178mm Tapi (4mm pitch) 0306, 0508 Tyr	ng	Dinternal code △ Standard △=Blank space

■ STANDARD EXTERNAL DIMENSIONS/STANDARD QUANTITY



Type		Dimension [mm]			Standard quantity [pcs]			
Type	L	W	Т		Paper tape	Embossed tape		
□WK105 (0204 inch)	0.52±0.05	1.00±0.05	0.30±0.05	Р	10000	_		
□WK107 (0306 inch)	0.80±0.10	1.60±0.10	0.50±0.05	٧	-	4000		
□WK212 (0508 inch)	1.25±0.15	2.00±0.15	0.85±0.10	D	4000	_		

■ AVAILABLE CAPACITANCE RANGE

	Time						10)5										107						2	12	
Cap	Type		X7S			X	6S				X5R			X7	'R	X	7S	X6S		X	5R		X	SS	X5	iR
[μF]	VDC	10	6.3	4	16	10	6.3	4	25	16	10	6.3	4	25	16	6.3	4	4	25	16	10	6.3	6.3	4	10	6.3
	[3-digit]																									
0.10	104	Р	Р		Р				Р			Р		٧					٧							
0.22	224		Р	Р		Р				Р			Р		V					V						
0.47	474						Р	Р			Р	Р			V					V						
1.0	105							Р				Р				V					V	V				
2.2	225																٧				V	٧				
4.7	475																	V				٧	D		D	
10	106																						D		D	
22	226																							D		D

^{*}Letters in the table indicate thickness.

Temp.char.Code			Temperature characteristics			Capacitance tolerance
remp.char.code	Applicable	e standard	Temperature range(°C)	Ref. Temp.(°C)	(%)	
BJ	JIS	В	-25~+85	20	±10	
	EIA	X5R	-55~+85	25	±15	1.40(1/)
C6	EIA	X6S	−55~+105	25	±22	±10(K) ±20(M)
C7	EIA	X7S	−55~+125	25	±22	±20(W)
B7	EIA	X7R	−55~+125	25	±15	

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REPRESENTATIVE PART NUMBERS

●105TYPE(0204 case size)

[Temperature Characteristic BJ:X5R]

· 0.3mm thickness(P)

Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
25V	TWK105 BJ104MP		X5R	0.1	±20	5	0.3±0.05	R	150%		
16V	EWK105 BJ224MP		X5R	0.22	±20	10	0.3±0.05	R	150%		
10V	LWK105 BJ474MP		X5R	0.47	±20	10	0.3±0.05	R	150%		
6.3V	JWK105 BJ104MP		X5R*1	0.1	±20	5	0.3±0.05	R	150%		
	JWK105 BJ474MP		X5R*1	0.47	±20	10	0.3±0.05	R	150%		
	JWK105 BJ105MP	•	X5R	1	±20	10	0.3±0.05	R	150%		
4V	AWK105 BJ224MP		X5R*1	0.22	±20	10	0.3±0.05	R	150%		

^{*1} We may provide X6S/X7S for some itemes according to the individual specification.

[Temperature Characteristic C6:X6S C7:X7S]

· 0.3mm thickness(P)

Rated			T	Conneitones	Canasitanas	Z 4	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	Capacitance tolerance	tanδ (%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
16V	EWK105 C6104MP		X6S	0.1	±20	5	0.3±0.05	R	150%		
10V	LWK105 C7104MP		X7S	0.1	±20	10	0.3±0.05	R	150%		
	LWK105 C6224MP		X6S	0.22	±20	10	0.3±0.05	R	150%		
6.3V	JWK105 C7104MP		X7S	0.1	±20	5	0.3±0.05	R	150%		
	JWK105 C7224MP		X7S	0.22	±20	10	0.3±0.05	R	150%		
	JWK105 C6474MP		X6S	0.47	±20	10	0.3±0.05	R	150%		
4V	AWK105 C6224MP		X6S	0.22	±20	10	0.3±0.05	R	150%		
	AWK105 C6474MP		X6S	0.47	±20	10	0.3±0.05	R	150%		
	AWK105 C6105MP		X6S	1	±20	10	0.3±0.05	R	150%		

●107TYPE(0306 case size)

[Temperature Characteristic BJ:X5R]

Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
25V	TWK107 BJ104MV		X5R	0.1	±20	5	0.5±0.05	R	150%		
16V	EWK107 BJ224MV		X5R	0.22	±20	5	0.5±0.05	R	150%		
	EWK107 BJ474MV		X5R	0.47	±20	5	0.5±0.05	R	150%		
10V	LWK107 BJ105MV		X5R	1	±20	10	0.5±0.05	R	150%		
	LWK107 BJ225MV		X5R	2.2	±20	10	0.5±0.05	R	150%		
6.3V	JWK107 BJ105MV		X5R*1	1	±20	10	0.5±0.05	R	150%		
	JWK107 BJ225MV		X5R	2.2	±20	10	0.5±0.05	R	150%		
	JWK107 BJ475MV		X5R	4.7	±20	10	0.5±0.05	R	150%		

^{*1} We may provide X7R/X7S for some itemes according to the individual specification.

[Temperature Characteristic B7:X7R C7:X7S C6:X6S]

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance [µF]	Capacitance tolerance	tanδ (%)	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
25V	TWK107 B7104MV		X7R	0.1	±20	5	0.5±0.05	R	150%		
16V	EWK107 B7224MV		X7R	0.22	±20	5	0.5±0.05	R	150%		
	EWK107 B7474MV		X7R	0.47	±20	5	0.5±0.05	R	150%		
6.3V	JWK107 C7105MV		X7S	1	±20	10	0.5±0.05	R	150%		
4V	AWK107 C7225MV		X7S	2.2	±20	10	0.5±0.05	R	150%		
	AWK107 C6475MV		X6S	4.7	±20	10	0.5±0.05	R	150%		

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■ REPRESENTATIVE PART NUMBERS

212TYPE(0508 case size)

[Temperature Characteristic BJ:X5R]

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance [µF]	Capacitance tolerance	tanδ [%]	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
10V	LWK212 BJ475□D		X5R	4.7	±10, ±20	10	0.85±0.1	R	150%		
	LWK212 BJ106MD		X5R	10	±20	10	0.85±0.1	R	150%		
6.3V	JWK212 BJ226MD		X5R	22	±20	10	0.85±0.1	R	150%		

Capacitance tolerance code is applied to \square of part number.

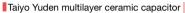
[Temperature Characteristic C6:X6S]

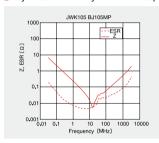
Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	Capacitance tolerance	tanδ (%)	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
6.3V	JWK212 C6475□D		X6S	4.7	±10, ±20	10	0.85±0.1	R	150%		
	JWK212 C6106MD		X6S	10	±20	10	0.85±0.1	R	150%		
4V	AWK212 C6226MD		X6S	22	±20	10	0.85±0.1	R	150%		

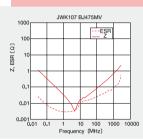
Capacitance tolerance code is applied to \square of part number.

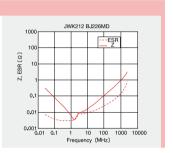
■ ELECTRICAL CHARACTERISTICS

Example of Impedance ESR vs. Frequency characteristics









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1)Minimum Quantity

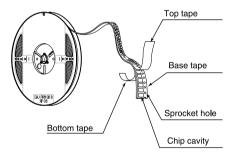
Taped package

	Thickness		Standard q	uantity [pcs]	
Туре	mm	code	Paper tape	Embossed tape	
☐MK042	0.2	C,D	_	40000	
□MK063	0.3	P,T	15000		
□2K096	0.3	Р			
□2K096	0.45	K	10000		
□WK105	0.3	P			
	0.2	С	20000] —	
☐MK105	0.3	Р	15000		
	0.5	V, W	10000		
□VK105	0.5	W	10000		
	0.45	K	4000		
□MK107 □WK107	0.5	V	_	4000	
	0.8	Α			
	0.5	V			
□2K110	0.6	В	4000		
	0.8	Α	4000	_	
□MK212	0.45	K			
□MK212 □WK212	0.85	D			
UVINZIZ	1.25	G	_	3000	
□4K212	0.85	D			
□2K212	0.85	D	4000	_	
	0.85	D			
□MK316	1.15	F		3000	
LIVING	1.25	G	_	3000	
	1.6	L			
	0.85	D			
	1.15	F		2000	
□MK325	1.9	N] –		
	2.0max	Y			
	2.5	М		500(T), 1000(P)	
□MK432	2.5	М	_	500	

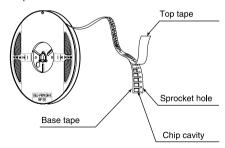
②Taping material

※No bottom tape for pressed carrier tape

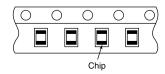
Paper tape

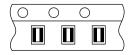


Embossed tape



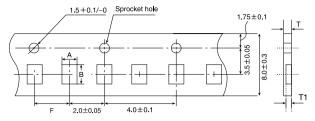
● Chip filled





③Representative taping dimensions

- Paper Tape (8mm wide)
- Pressed carrier tape (2mm pitch)

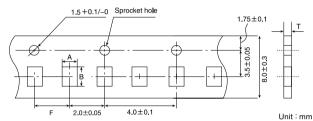


Unit: mm

T	Chip	Cavity	Insertion Pitch	Tape Thickness			
Type	Α	В	F	Т	T1		
□MK063	0.37	0.67					
□2K096	0.65	1.02		0.45max.	0.42max.		
□WK105			2.0±0.05				
MK105(*1C)	0.65	1.15		0.4max.	0.3max.		
MK105(*1P)				0.45max.	0.42max.		

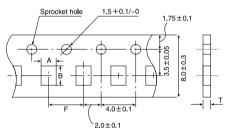
^{*1} Thickness, C: 0.2mm, P: 0.3mm

• Punched carrier tape (2mm pitch)



Type	Chip Cavity		Insertion Pitch	Tape Thickness
Type	Α	В	F	Т
□2K096	0.72	1.02		0.6max.
□MK105 □VK105	0.65	1.15	2.0±0.05	0.8max.

• Punched carrier tape (4mm pitch)



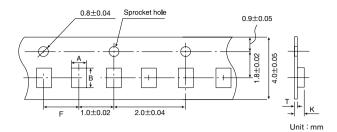
Unit: mm

Time	Chip	Cavity	Insertion Pitch	Tape Thickness
Type	Α	В	F	Т
□MK107 □WK107	1.0	1.8		1.1max.
□2K110	1.15	1.55		1.0max.
☐MK212 ☐WK212	1.05	2.4	4.0±0.1	
□4K212 □2K212	1.65	2.4		1.1max.
☐MK316	2.0	3.6		

Note: Taping size might be different depending on the size of the product.

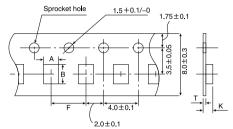
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Embossed tape (4mm wide)



T	Chip Cavity I		Insertion Pitch	Tape Thickness	
Type	Α	В	F	K	Т
☐MK042	0.23	0.43	1.0±0.02	0.5max.	0.25max.

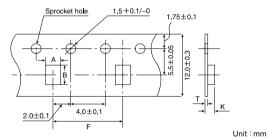
Embossed tape (8mm wide)



Unit: mm

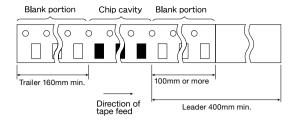
	Time	Chip (Cavity	Insertion Pitch	Tape Th	ickness
	Type	Α	В	F	K	Т
	□WK107	1.0	1.8	4.0±0.1	1.3max	0.25±0.1
Π	☐MK212	1.65	2.4			
	☐MK316	2.0	3.6		3.4max.	0.6max.
	☐MK325	2.8	3.6			

Embossed tape (12mm wide)

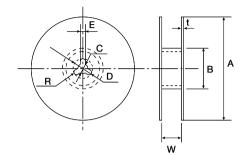


Туре	Chip (Cavity	Insertion Pitch	Tape Th	ickness
	Α	В	F	K	Т
□MK432	3.7	4.9	8.0±0.1	4.0max.	0.6max.

4Trailer and Leader



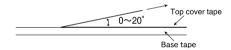
5Reel size



		Unit: mm
А	В	С
φ178±2.0	φ50min.	φ13.0±0.2
D	E	R
φ21.0±0.8	2.0±0.5	1.0
	t	W
4mm wide tape	1.5max.	5±1.0
8mm wide tape	2.5max.	10±1.5
12mm wide tape	2.5max.	14±1.5

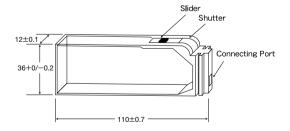
6Top Tape Strength

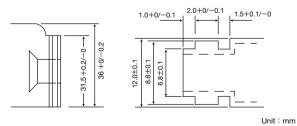
The top tape requires a peel-off force of 0.1 to 0.7N in the direction of the arrow as illustrated below.



7Bulk Cassette

The exchange of individual specification is necessary. Please contact Taiyo Yuden sales channels.





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Multilayer Ceramic Capacitors

Super Low Distortion Multilayer Ceramic Capacitors and Medium-High Voltage Multilayer Ceramic Capacitors are noted separately.

1.Operating Temperature Range							
	Temperature Compensating	Standard		−55 to +125°C			
Specified	(Class 1)	High Frequency Type	_55	-55 to +125 C			
				Specification	Temperature Range		
			В	В	on Temperature Range -25 to +85°C -55 to +85°C -55 to +125°C -55 to +105°C -55 to +105°C -55 to +125°C -25 to +85°C		
				-55 to +85°C			
Value	11: 1 D (11: 1)		B7	X7R	-55 to +125℃		
	High Permittivity (Class 2)		C	X6S	-55 to +105°C		
		C	C7	X7S	-55 to +125°C		
			F	F	-25 to +85°C		
			-	Y5V	−30 to +85°C		

2. Storage Conditions						
	Temperature Compensating	Standard		-55 to	n +125℃	
	(Class 1)	High Frequency Type		-55 to +125 C		
					Specification	Temperature Range
				ВΙ	В	-25 to +85°C
Specified			BJ X5R —55			-55 to +85°C
Value	High Permittivity (Class 2)		[B7	X7R	-55 to +125°C
	High Fermittivity (Class 2)		1 ⊢	C6	X6S	-55 to +105°C
				C7	X7S	-55 to +125°C
				_	F	-25 to +85°C
			F	r	Y5V	-30 to +85°C

3. Rated Voltage					
Specified Value	Temperature Compensating	Standard	50VDC, 25VDC, 16VDC		
	(Class 1)	High Frequency Type	50VDC, 16VDC		
	High Permittivity (Class 2)		50VDC, 35VDC, 25VDC, 16VDC, 10VDC, 6.3VDC, 4VDC		

4. Withstanding Voltage (Between terminals)

	Temperature Compensating	Standard	
Specified Value	(Class 1)	High Frequency Type	No breakdown or damage
value	High Permittivity (Class 2)		

[Test Methods and Remarks]

	Class 1	Class 2	
Applied voltage	Rated voltage×3	Rated voltage×2.5	
Duration	1 to 5 sec.		
Charge/discharge current	50mA max.		

	/ ipplied reliage	riated renager o	riated ventage E.e.			
	Duration	1 to 5 sec.				
Charge/discharge current		50mA max.				
-						
5	i. Insulation Resistance					

Value	Temperature Compensating	Standard	10000 MΩ min.	
	(Class 1)	High Frequency Type	TOOO MISS MIN.	
	High Permittivity (Class 2) No	C≤0.047 μ F : 10000 MΩ min. C>0.047 μ F : 500MΩ· μ F		

[Test Methods and Remarks] Applied voltage: Rated voltage Duration: 60±5 sec.

Charge/discharge current: 50mA max.

6. Capacitance (Tolerance)

Specified Value	(Class I)	Standard	$ \begin{array}{ c c c c c c }\hline C & 0.5pF \leq C \leq 5pF : \pm 0.25pF \\ U \triangle & 0.5pF < C \leq 10pF : \pm 0.5pF \\ \hline C > 10pF : \pm 5\% \\ \hline \end{array} \begin{array}{ c c c c c c c c c c c c c c c c c c c$			
		High Frequency Type	CH 0.5pF≦C≦2pF : ±0.1pF RH C>2pF : ±5%			
	High Permittivity (Class 2)		BJ, B7, C6,C7: ±10% or ±20%, F: -20%/+80%			

[Test Methods and Remarks]

	Class 1		Class 2	
	Standard	High Frequency Type	C≦10 <i>μ</i> F	C>10µF
Preconditioning	None		Thermal treatment (at 150°C for 1hr) Note 2	
Measuring frequency	1MHz±10%		1kHz±10%	120±10Hz
Measuring voltage Note 1	0.5 to 5Vrms		1±0.2Vrms	0.5±0.1Vrms
Bias application	None			

7. Q or Dissipation Factor

Specified Value	Temperature Compensating	Standard	C<30 pF : Q≥400+20C、C≥30 pF : Q≥1000 (C : Nominal capacitance)	
	(Class 1)	High Frequency Type	Refer to detailed specification	
	High Permittivity (Class 2) Note 1		BJ, B7, C6,C7: 2.5% max., F:7% max.	

[Test Methods and Remarks]

treet metriode and remarker					
	Cla	ss 1	Class 2		
	Standard	High Frequency Type	C≦10μF	C>10μF	
Preconditioning	No	one	Thermal treatment (at 150°C for 1hr) Note 2		
Measuring frequency	1MHz±10%	1GHz	1kHz±10%	120±10Hz	
Measuring voltage Note 1	0.5 to	5Vrms	1±0.2Vrms	0.5±0.1Vrms	
Bias application		No	one		

High Frequency Type Measuring equipment: HP4291A Measuring jig: HP16192A

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8. Temperature Characteristic (Without voltage application)

			Temperature Characteristic [ppm/			To	olerance		
		Standard	C	: 0 CH, C	CJ, CK				
	Temperature Compensating		R□	: -220 RH			H±60		
	(Class 1)			: -330 SH, S			J±120		
		High Frequency Type		: −470 TJ, Tł			K±250		
				: -750 UJ, U					
0			SL	: +350 to -10	00				
Specified Value				Specification	Capacitance ch	ange	Reference to	emperature	Temperature Range
			BJ	В	±10%		20	C	-25 to +85°C
				X5R	±15%		25	C	-55 to +85°C
	High Permittivity (Class 2)		B7	X7R	±15%		25	C	-55 to +125°C
			C6	X6S	±22%		25	C	-55 to +105°C

X7S

Y5V

±22%

+30/-80%

+22/-82%

[Test Methods and Remarks]

Class 1

Capacitance at 20°C and 85°C shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.

C7

F

$$\frac{(C_{85}-C_{20})}{C_{20}\times\triangle T}$$
 × 10⁶ (ppm/°C) $\triangle T$ =65

Class 2

Capacitance at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.

			_		
Step	B、F	X5R, X7R, X6S, X7S, Y5V	1		
1	Minimum operating temperature				
2	20℃	25℃]		
3	Maximum operating temperature				

 $\frac{(C-C_2)}{C_2} \times 100(\%)$

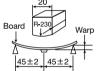
C : Capacitance in Step 1 or Step 3 C₂ : Capacitance in Step 2

9. Deflect	9. Deflection					
Specified Value	Temperature Compensating (Class 1)			No abnormality Within $\pm 5\%$ or ± 0.5 pF, whichever is larger.		
			Appearance : Capacitance change :	No abnormality Within±0.5 pF		
			Appearance :	No abnormality Within +12.5% (R.L. R7, C6, C7), Within +20% (F)		

[Test Methods and Remarks]

Multilayer Ceramic Capacitors

	Board	Thickness	Warp	Duration
042、063 Type	along apovy ragin aubatrata	0.8mm	1mm	10 sec.
The other types	glass epoxy-resin substrate	1.6mm	11111111	TO Sec.



25°C

20°C

25℃

Array Type

	Board	Thickness	Warp	Duration
096、110、212 Type	glass epoxy-resin substrate	1.6mm	1mm	10 sec.

Capacitance measurement shall be conducted with the board bent

-55 to +125°C

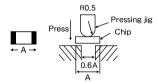
-25 to +85°C

-30 to +85°C

10. Body Strength

0	Temperature Compensating	Standard	_
Specified Value	(Class 1)	High Frequency Type	No mechanical damage.
	High Permittivity (Class 2)		_

[Test Methods and Remarks] High Frequency Type Applied force: 5N Duration: 10 sec.



11. Adhesive Strength of Terminal Electrodes

0 :6 1	remperature Compensating	Stariuaru	
Specified Value	(Class 1)	High Frequency Type	No terminal separation or its indication.
value	High Permittivity (Class 2)		

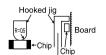
[Test Methods and Remarks]

Multilayer Ceramic Capacitors

	Applied force	Duration
042、063 Type	2N	30±5 sec.
105 Type or more	5N	30±5 sec.

Array Type

	Applied force	Duration
096 Type	2N	30±5 sec.
110、212 Type	5N	30±5 sec.



mlcc_reli-R2

12. Solderability

0 '6 1		Standard	
Specified Value	(Class 1)	High Frequency Type	At least 95% of terminal electrode is covered by new solder.
	High Permittivity (Class 2)		

[Test Methods and Remarks]

	Solder type	Solder temperature	Duration
Eutectic solder	H60A or H63A	230±5℃	4±1 sec.
Lead-free solder	Sn-3.0Ag-0.5Cu	245±3℃	4±1 Sec.

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13. Resistance to Soldering No abnormality Within ±2.5% or ±0.25pF, whichever is larger. Appearance: Capacitance change: Q: Insulation resistance: Standard Initial value Initial value Temperature Compensating Withstanding voltage (between terminals): No abnormality (Class 1) Appearance: No abnormality Capacitance change: Within ±2.5% Specified High Frequency Type Q: Initial value Insulation resistance: Initial value Withstanding voltage (between terminals): No abnormality Appearance: Capacitance change: No abnormality Within ±7.5% (BJ, B7, C6, C7) Within ±20% (F) High Permittivity (Class 2) Note 1 Dissipation factor: Initial value Insulation resistance: Initial value Withstanding voltage (between terminals): No abnormality

[Test Methods and Remarks]

Class 1

042, 063 Type	105 Type Array (096, 110 Type)
None	
150°C, 1 to 2 min.	80 to 100°C, 2 to 5 min. 150 to 200°C, 2 to 5 min.
270±5℃	
3±0.5 sec.	
6 to 24 hrs (Standard condition) Note 5	
	150°C, 1 to 2 min. 27(3±0

Class 2

Oldoo Z			
	042、063 Type	105, 107, 212 Type Array(096, 110,212 Type)	316, 325 Type
Preconditioning	Thermal treatment (at 150°C for 1 hr) Note 2		
Preheating	150℃, 1 to 2 min.	80 to 100°C, 2 to 5 min. 150 to 200°C, 2 to 5 min.	80 to 100°C, 5 to 10 min. 150 to 200°C, 5 to 10 min.
Solder temp.	270±5℃		
Duration	3±0.5 sec.		
Recovery	24±2 hrs (Standard condition) Note 5		

14. Temp	erature Cycle (Thermal Shock)			
Temperature Compensatinc	Temperature Compensating	Standard	Appearance: Capacitance change: Q: Insulation resistance: Withstanding voltage	No abnormality Within ±2.5% or ±0.25pF, whichever is larger. Initial value (between terminals): No abnormality
Specified Value	(Class 1)	High Frequency Type	Appearance: Capacitance change: Q: Insulation resistance: Withstanding voltage	No abnormality Within ±0.25pF Initial value Initial value (between terminals): No abnormality
High Permittivity (Class 2) Note 1		Appearance: Capacitance change: Dissipation factor: Insulation resistance: Withstanding voltage	No abnormality Within ±7.5% (BJ, B7, C6, C7) Within ±20% (F) Initial value Initial value (between terminals): No abnormality	

[Test Methods and Remarks]

	Class 1			Class 2	
Preconditioning	None		Thermal treatment (at 150°C for 1 hr) Note 2		
	Step	Temperature	(°C)	Time(min.)	
	1	Lowest operating tempera	ature +0/-3	30±3	
1 cycle	2	2 Normal temperature		2 to 3	
	3	Highest operating temper	ature +0/-3	30±3	
	4	Normal temperature		2 to 3	
Number of cycles	5 times				
Recovery	6 to 24 hrs (Standard condition) Note 5 24±2 hrs (Standard cond		tandard conditio	n) Note 5	

15. Humidity (Steady State) Appearance: Capacitance change: Q: No abnormality Within ±5% or ±0.5pF, whichever is larger. C<10pF: Q≧200+10C Standard 10≦C<30pF: Q≥275+2.5C Temperature Compensating C≧30pF: Q≧350 (C: Nominal capacitance) (Class 1) Insulation resistance: 1000 MΩ min. No abnormality Within ±0.5pF, Appearance: Specified High Frequency Type Capacitance change: . Value Insulation resistance: 1000 M Ω min. No abnormality Appearance: Capacitance change: Within ±12.5% (BJ, B7, C6, C7) Within ±30% (F) High Permittivity (Class 2) Note 1 Dissipation factor: 5.0% max. (BJ, B7, C6, C7) 11.0% max.(F) 50 MΩ μ F or 1000 MΩ whichever is smaller. Insulation resistance:

[Test Methods and Remarks]

Class I		
	Standard	High Frequency Type
Preconditioning	None	
Temperature	40±2℃ 60±2℃	
Humidity	90 to 95%RH	
Duration	500+24/-0 hrs	
Recovery	6 to 24 hrs (Standard condition) Note 5	

Class 2

All items	
Thermal treatment (at 150°C for 1 hr)	Note 2
40±2℃	
90 to 95%RH	
500+24/-0 hrs	
24±2 hrs (Standard condition) No	ote 5
	Thermal treatment (at 150°C for 1 hr) 40±2°C 90 to 95%RH

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16. Humidity Loading No abnormality Within ±7.5% or ±0.75pF, whichever is larger. Appearance: Capacitance change: C<30pF: Q≥100+10C/3 C≥30pF: Q≥200 Standard (C: Nominal capacitance) Temperature Compensating Insulation resistance: 500 MΩ min. (Class 1) No abnormality Appearance C \leq 2pF: Within \pm 0.4 pF C>2pF: Within \pm 0.75 pF Capacitance change: Specified High Frequency Type (C: Nominal capacitance) Value Insulation resistance: 500 MΩ min. Appearance: No abnormality Within ±12.5% (BJ, B7, C6, C7) Within ±30% (F) Capacitance change: High Permittivity (Class 2) Note 1 Dissipation factor: 5.0% max. (BJ, B7, C6, C7) 11.0% max.(F) Insulation resistance: 25 M $\Omega\mu$ F or 500 M Ω , whichever is smaller.

Test Methods and Remarks

	High Frequency Type	
None		
40±2℃	60±2℃	
90 to 95%RH		
500+24/-0 hrs		
Rated voltage		
50mA max.		
6 to 24 hrs (Standard condition) Note 5		
	40±2℃ 90 to 500+ Rate 50r	

Class 2

Olass Z	
	All items
Preconditioning	Voltage treatment
	(Rated voltage are applied for 1 hour at 40°C) Note 3
Temperature	40±2℃
Humidity	90 to 95%RH
Duration	500+24/-0 hrs
Applied voltage	Rated voltage
Charge/discharge current	50mA max.
Recovery	24±2 hrs (Standard condition) Note 5

17. High	17. High Temperature Loading						
Specified Value	Temperature Compensating (Class 1)	Standard	Appearance: Capacitance change: Q: Insulation resistance:	No abnormality			
		High Frequency Type	Appearance: Capacitance change: Insulation resistance:	No abnormality Within $\pm 3\%$ or ± 0.3 pF, whichever is larger. 1000 M Ω min.			
	High Permittivity (Class 2) Note 1		Appearance: Capacitance change: Dissipation factor:	No abnormality Within ±12.5%(BJ, B7, C6, C7) Within ±30%(F) 5.0% max.(BJ, B7, C6, C7) 11.0% max.(F)			
			Insulation resistance:	50 MΩμF or 1000 MΩ, whichever is smaller.			

[Test Methods and Remarks]

Class 1

	Standard	High Frequency Type			
Preconditioning		None			
Temperature	1:	25±3℃			
Duration	1000+48/-0 hrs				
Applied voltage	Rated voltage×2				
Charge/discharge current	50mA max.				
Recovery	6 to 24hr (Stand	lard condition) Note 5			

Class 2

	BJ, F	C6	B7, C7	
Preconditioning	Voltage treatment (Twice the rated voltage shall be applied for 1 hour at 85°C, 105°C or 125°C) Note 3, 4			
Temperature	85±2℃	105±3℃	125±3℃	
Duration	1000+48/-0 hrs			
Applied voltage	Rated voltage×2 Note 4			
Charge/discharge current	50mA max.			
Recovery	24±2 hrs (Standard condition) Note 5			

The figures indicate typical specifications. Please refer to individual specifications in detail. Note 1

Note 2

Thermal treatment: Initial value shall be measured after test sample is heat-treated at 150+0/—10°C for an hour and kept at room temperature for 24±2hours.

Voltage treatment: Initial value shall be measured after test sample is voltage-treated at 150+0/—10°C for an hour and kept at room temperature for 24±2hours.

Voltage treatment: Initial value shall be measured after test sample is voltage-treated for an hour at both the temperature and voltage specified in the test conditions, and Note 3 kept at room temperature for 24±2hours.

Note 4 150% of rated voltage is applicable to some items. Please refer to their specifications for further information. Note 5 Standard condition: Temperature: 5 to 35°C, Relative humidity: 45 to 85 % RH, Air pressure: 86 to 106kPa

When there are questions concerning measurement results, in order to provide correlation data, the test shall be conducted under the following condition.

Temperature: $20\pm2^{\circ}$ C, Relative humidity: 60 to 70 % RH, Air pressure: 86 to 106kPa Unless otherwise specified, all the tests are conducted under the "standard condition".

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1. Circuit Design

- Verification of operating environment, electrical rating and performance
 - 1. A malfunction of equipment in fields such as medical, aerospace, nuclear control, etc. may cause serious harm to human life or have severe social ramifications Therefore, any capacitors to be used in such equipment may require higher safety and reliability, and shall be clearly differentiated from them used in general purpose applications
- Precautions
- ◆Operating Voltage (Verification of Rated voltage)
 1. The operating voltage for capacitors must always be their rated voltage or less.
 - If an AC voltage is loaded on a DC voltage, the sum of the two peak voltages shall be the rated voltage or less. For a circuit where an AC or a pulse voltage may be used, the sum of their peak voltages shall also be the rated voltage or less.
 - 2. Even if an applied voltage is the rated voltage or less reliability of capacitors may be deteriorated in case that either a high frequency AC voltage or a pulse voltage having rapid rise time is used in a circuit.

2. PCB Design

- ◆Pattern configurations (Design of Land-patterns)

 1. When capacitors are mounted on PCBs, the amount of solder used (size of fillet) can directly affect the capacitor performance. Therefore, the following items must be carefully considered in the design of land patterns:
- (1) Excessive solder applied can cause mechanical stresses which lead to chip breaking or cracking. Therefore, please consider appropriate land-patterns for proper amount of solder.

 (2) When more than one component are jointly soldered onto the same land, each component's soldering point shall be separated by solder-resist.

 Pattern configurations (Capacitor layout on PCBs)

Precautions

Technical consider

ations

After capacitors are mounted on boards, they can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering of the boards, etc.). For this reason, land pattern configurations and positions of capacitors shall be carefully considered to minimize stresses.

- ◆Pattern configurations (Design of Land-patterns)
 - The following diagrams and tables show some examples of recommended land patterns to prevent excessive solder amounts.

 (1) Recommended land dimensions for typical chip capacitors
 - - ●Multilayer Ceramic Capacitors: Recommended land dimensions (unit: mm)

Wave-soldering

Тур	е	107	212	212 316		
Size	L	1.6	2.0	3.2	3.2	
Size	W	0.8	1.25	1.6	2.5	
Α		0.8 to 1.0	1.0 to 1.4	1.8 to 2.5	1.8 to 2.5	
В		0.5 to 0.8	0.8 to 1.5	0.8 to 1.7	0.8 to 1.7	
С		0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5	

Reflow-soldering

Type		042	063	105	107	212	316	325	432
Size	L	0.4	0.6	1.0	1.6	2.0	3.2	3.2	4.5
	W	0.2	0.3	0.5	0.8	1.25	1.6	2.5	3.2
Α		0.15 to 0.25	0.20 to 0.30	0.45 to 0.55	0.8 to 1.0	0.8 to 1.2	1.8 to 2.5	1.8 to 2.5	2.5 to 3.5
В		0.15 to 0.20	0.20 to 0.30	0.40 to 0.50	0.6 to 0.8	0.8 to 1.2	1.0 to 1.5	1.0 to 1.5	1.5 to 1.8
С		0.15 to 0.30	0.25 to 0.40	0.45 to 0.55	0.6 to 0.8	0.9 to 1.6	1.2 to 2.0	1.8 to 3.2	2.3 to 3.5

Note: Recommended land size might be different according to the allowance of the size of the product.

●LWDC: Recommended land dimensions for reflow-soldering (unit: mm)

Type		105	107	212
Size	L	0.52	0.8	1.25
Size	W	1.0	1.6	2.0
A		0.18 to 0.22	0.25 to 0.3	0.5 to 0.7
В		0.2 to 0.25	0.3 to 0.4	0.4 to 0.5
С		0.9 to 1.1	1.5 to 1.7	1.9 to 2.1

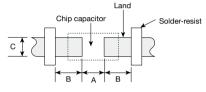
• Array type: Recommended land dimensions for reflow-soldering (unit: mm)

Type		096 (2 circuits)	110 (2 circuits)	212 (2 circuits)	212 (4 circuits)
Size	L	0.9	1.37	2.0	2.0
Size	W	0.6	1.0	1.25	1.25
а		0.25 to 0.35	0.35 to 0.45	0.5 to 0.6	0.5 to 0.6
b		0.15 to 0.25	0.55 to 0.65	0.5 to 0.6	0.5 to 0.6
С		0.15 to 0.25	0.3 to 0.4	0.5 to 0.6	0.2 to 0.3
d		0.45	0.64	1.0	0.5

(2) Examples of good and bad solder application

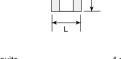
Items	Not recommended	Recommended
Mixed mounting of SMD and leaded components	Lead wire of component	Solder-resist
Component placement close to the chassis	Chassis Solder(for grounding)	Solder-resist
Hand-soldering of leaded components near mounted components	Lead wire of component- Soldering iron	Solder-resist -
Horizontal component placement		Solder-resist

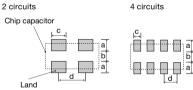








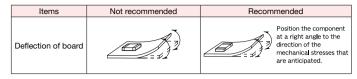




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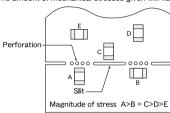
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- ◆Pattern configurations (Capacitor layout on PCBs)
- 1-1. The following is examples of good and bad capacitor layouts; capacitors shall be located to minimize any possible mechanical stresses from board warp or deflection.



Technical considerations

1-2. The amount of mechanical stresses given will vary depending on capacitor layout. Please refer to diagram below.



1-3. When PCB is split, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, please consider the PCB, split methods as well as chip location.

3. Mounting

- Adjustment of mounting machine
 - When capacitors are mounted on PCB, excessive impact load shall not be imposed on them. 2. Maintenance and inspection of mounting machines shall be conducted periodically

Precautions

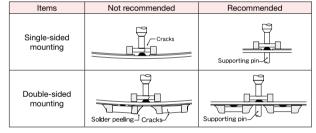
Selection of Adhesives

1. When chips are attached on PCBs with adhesives prior to soldering, it may cause capacitor characteristics degradation unless the following factors are appropriately checked: size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, please contact us for further information.

◆Adjustment of mounting machine

- 1. When the bottom dead center of a pick-up nozzle is too low, excessive force is imposed on capacitors and causes damages. To avoid this, the following points shall be considerable
 - (1) The bottom dead center of the pick-up nozzle shall be adjusted to the surface level of PCB without the board deflection.

 - (2) The pressure of nozzle shall be adjusted between 1 and 3 N static loads.
 (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins shall be used on the other side of the PCB. The following diagrams show some typical examples of good and bad pick-up nozzle placement:



Technical considerations

As the alignment pin is worn out, adjustment of the nozzle height can cause chipping or cracking of capacitors because of mechanical impact on the capacitors. To avoid this, the monitoring of the width between the alignment pins in the stopped position, maintenance, check and replacement of the pin shall be conducted periodically.

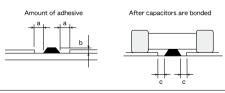
Selection of Adhesives

Some adhesives may cause IR deterioration. The different shrinkage percentage of between the adhesive and the capacitors may result in stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect components. Therefore, the following precautions shall be noted in the application of adhesives.

- (1) Required adhesive characteristics
 - a. The adhesive shall be strong enough to hold parts on the board during the mounting & solder process.
 b. The adhesive shall have sufficient strength at high temperatures.

 - c. The adhesive shall have good coating and thickness consistency.
 - d. The adhesive shall be used during its prescribed shelf life. e. The adhesive shall harden rapidly.
 - f. The adhesive shall have corrosion resistance
 - g. The adhesive shall have excellent insulation characteristics.
- h. The adhesive shall have no emission of toxic gasses and no effect on the human body.
- (2) The recommended amount of adhesives is as follows;

[Recommended condition]				
Figure	Figure 212/316 case sizes as examples			
а	0.3mm min			
b	100 to 120 μm			
С	Adhesives shall not contact land			



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Selection of Flux

Since flux may have a significant effect on the performance of capacitors, it is necessary to verify the following conditions prior to use;

- (1) Flux used shall be less than or equal to 0.1 wt% (in CI equivalent) of halogenated content. Flux having a strong acidity content shall not be applied. (2) When shall capacitors are soldered on boards, the amount of flux applied shall be controlled at the optimum level.
- (3) When water-soluble flux is used, special care shall be taken to properly clean the boards.

Precautions

◆Solderina

Temperature, time, amount of solder, etc. shall be set in accordance with their recommended conditions.

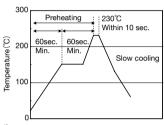
Sn-Zn solder paste can adversely affect MLCC reliability. Please contact us prior to usage of Sn-Zn solder

- 1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate flux, or highly acidic flux is used, it may lead to corrosion of terminal electrodes or degradation of insulation resistance on the surfaces of the capacitors.
- 1-2. Flux is used to increase solderability in wave soldering. However if too much flux is applied, a large amount of flux gas may be emitted and may adversely affect the solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system.
- 1-3. Since the residue of water-soluble flux is easily dissolved in moisture in the air, the residues on the surfaces of capacitors in high humidity conditions may cause a degradation of insulation resistance and reliability of the capacitors. Therefore, the cleaning methods and the capability of the machines used shall also be considered carefully when water-soluble flux is used

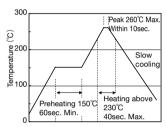
- Ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling.
- Therefore, the soldering must be conducted with great care so as to prevent malfunction of the components due to excessive thermal shock
- Preheating: Capacitors shall be preheated sufficiently, and the temperature difference between the capacitors and solder shall be within 100 to 130°C.
 Cooling: The temperature difference between the capacitors and cleaning process shall not be greater than 100°C.

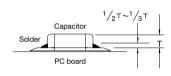
[Reflow soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]





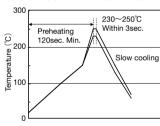
Caution

- (i) The ideal condition is to have solder mass (fillet) controlled to 1/2 to 1/3 of the thickness of a capacitor.
- ②Because excessive dwell times can adversely affect solderability, soldering duration shall be kept as
- close to recommended times as possible.

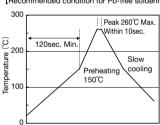
Technical considerations

[Wave soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]

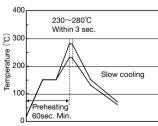


Caution

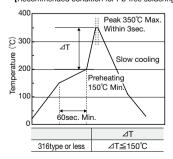
①Wave soldering must not be applied to capacitors designated as for reflow soldering only.

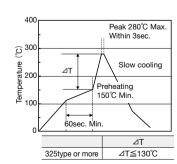
[Hand soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]





- ①Use a 50W soldering iron with a maximum tip diameter of 1.0 mm.
- ②The soldering iron shall not directly touch capacitors

^{*} This catalog contains the typical specification only due to the limitation of space. When you consider the purchase of our products, please check our specification. For details of each product (characteristics graph, reliability information, precautions for use, and so on), see our Web site (http://www.ty-top.com/) or CD catalogs.

Precautions

◆Cleaning conditions

- 1. When PCBs are cleaned after capacitors mounting, please select the appropriate cleaning solution in accordance with the intended use of the cleaning. (e.g. to
- remove soldering flux or other materials from the production process.)

 2. Cleaning condition shall be determined after it is verified by using actual cleaning machine that the cleaning process does not affect capacitor's characteristics.
- 1. The use of inappropriate cleaning solutions can cause foreign substances such as flux residue to adhere to capacitors or deteriorate their outer coating, resulting in a degradation of the capacitor's electrical properties (especially insulation resistance)

Technical considerations

2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may adversely affect the performance of the capacitors.

In the case of ultrasonic cleaning, too much power output can cause excessive vibration of PCBs which may lead to the cracking of capacitors or the soldered portion, or decrease the terminal electrodes' strength. Therefore, the following conditions shall be carefully checked;

Ultrasonic output : 20 W/ ℓ or less Ultrasonic frequency: 40 kHz or less Ultrasonic washing period : 5 min. or less

6. Resin coating and mold

Precautions

1. With some type of resins, decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal stor-

age conditions resulting in the deterioration of the capacitor's performance.

2. When a resin's hardening temperature is higher than capacitor's operating temperature, the stresses generated by the excessive heat may lead to damage or destruction of capacitors

The use of such resins, molding materials etc. is not recommended

7. Handling

- ◆Splitting of PCB

 1. When PCBs are split after components mounting, care shall be taken so as not to give any stresses of deflection or twisting to the board.
- 2. Board separation shall not be done manually, but by using the appropriate devices

Precautions

Mechanical considerations

Be careful not to subject capacitors to excessive mechanical shocks.

- (1) If ceramic capacitors are dropped onto a floor or a hard surface, they shall not be used.
- (2) Please be careful that the mounted components do not come in contact with or bump against other boards or components

8. Storage conditions

◆Storage
1.To maintain the solderability of terminal electrodes and to keep packaging materials in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.

Recommended conditions

Precautions

Ambient temperature Below 30°C Below 70% RH Humidity

The ambient temperature must be kept below 40°C. Even under ideal storage conditions, solderability of capacitor is deteriorated as time passes, so capacitors shall be used within 6 months from the time of delivery.

- · Ceramic chip capacitors shall be kept where no chlorine or sulfur exists in the air.
- 2. The capacitance values of high dielectric constant capacitors will gradually decrease with the passage of time, so care shall be taken to design circuits . Even if capacitance value decreases as time passes, it will get back to the initial value by a heat treatment at 150°C for 1hour.

Technical consider-

If capacitors are stored in a high temperature and humidity environment, it might rapidly cause poor solderability due to terminal oxidation and quality loss of taping/ packaging materials. For this reason, capacitors shall be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the capacitors.

*RCR-2335B (Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA.

Please check the guide regarding precautions for deflection test, soldering by spot heat, and so on.

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