

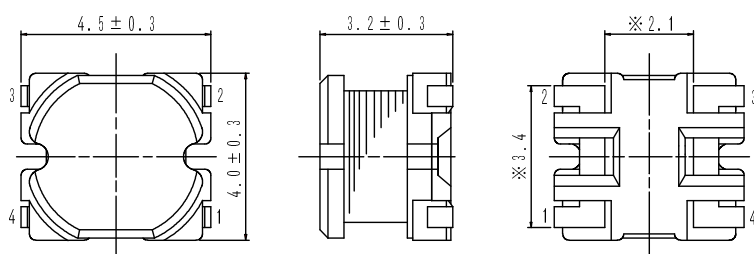
SMD Power Inductor CR43



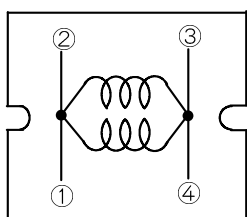
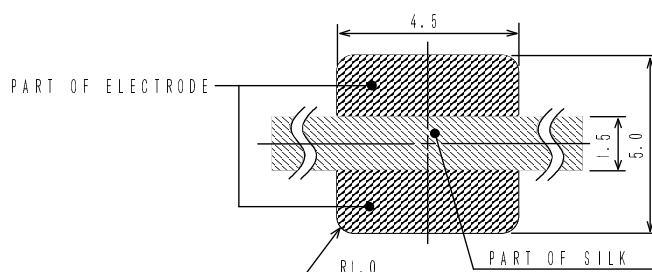
Halogen
Free



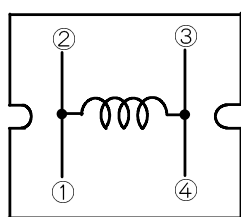
Dimension - [mm]



Land pattern and Schematics - [mm]



(1.0~8.2 μ H)



(10~68 μ H)

Description

- Ferrite drum core construction.
- Magnetically unshielded.
- L $\times$ W $\times$ H: 4.8 $\times$ 4.3 $\times$ 3.5 mm Max.
- Product weight: 145mg(Ref.)
- Moisture Sensitivity Level: 1
- RoHS compliance.
- Halogen Free available.

Environmental Data

- Operating temperature range: -40°C~+100°C (including coil's self temperature rise)
- Storage temperature range: -40°C~+100°C
- Solder reflow temperature: 260 °C peak.

Packaging

- Carrier tape and reel packaging
- 12.9" diameter reel
- 1500pcs per reel

Applications

- Ideally used in A/V equipment, LCD TV, DSC/DVC, Game Machine, DVC, HDD, Notebook PC, etc as DC-DC converter inductors.

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Electrical Characteristics

Part Name	Stamp	Inductance (μ H) [within] ※ 1	D.C.R. (Ω) [Max.] (at 20°C)	Rated Current (A) ※2
CR43NP-1R0MC	1R0	$1.0 \pm 20\%$	48.7m	2.56
CR43NP-1R4MC	1R4	$1.4 \pm 20\%$	56.2m	2.52
CR43NP-1R8MC	1R8	$1.8 \pm 20\%$	63.7m	1.95
CR43NP-2R2MC	2R2	$2.2 \pm 20\%$	71.2m	1.75
CR43NP-2R7MC	2R7	$2.7 \pm 20\%$	78.7m	1.58
CR43NP-3R3MC	3R3	$3.3 \pm 20\%$	86.2m	1.44
CR43NP-3R9MC	3R9	$3.9 \pm 20\%$	93.7m	1.33
CR43NP-4R7MC	4R7	$4.7 \pm 20\%$	108.7m	1.15
CR43NP-5R6MC	5R6	$5.6 \pm 20\%$	125.7m	0.99
CR43NP-6R8MC	6R8	$6.8 \pm 20\%$	131.2m	0.95
CR43NP-8R2MC	8R2	$8.2 \pm 20\%$	146.2m	0.84
CR43NP-100MC	100	$10 \pm 20 \%$	0.182	1.04
CR43NP-120MC	120	$12 \pm 20 \%$	0.210	0.97
CR43NP-150MC	150	$15 \pm 20 \%$	0.235	0.85
CR43NP-180MC	180	$18 \pm 20 \%$	0.338	0.74
CR43NP-220MC	220	$22 \pm 20 \%$	0.378	0.68
CR43NP-270MC	270	$27 \pm 20 \%$	0.522	0.62
CR43NP-330KC	330	$33 \pm 10 \%$	0.540	0.56
CR43NP-390KC	390	$39 \pm 10 \%$	0.587	0.52
CR43NP-470KC	470	$47 \pm 10 \%$	0.844	0.44
CR43NP-560KC	560	$56 \pm 10 \%$	0.937	0.42
CR43NP-680KC	680	$68 \pm 10 \%$	1.117	0.37

※1. Inductance measuring frequency: 1.0μ H $\sim$ 8.2μ H ; at 7.96 MHz
 10μ H $\sim$ 68μ H ; at 2.52 MHz

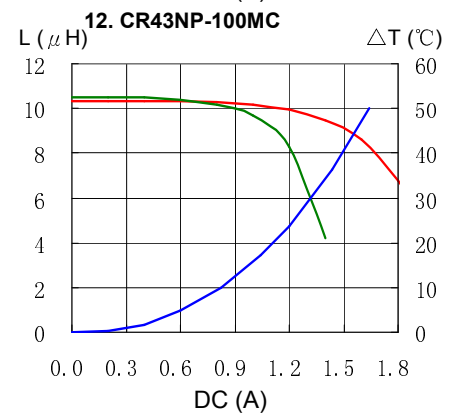
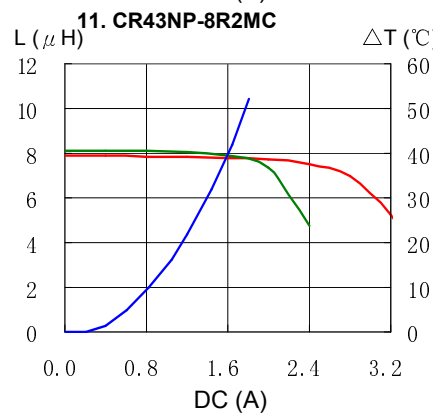
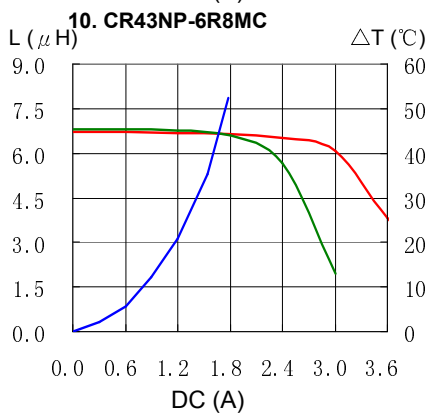
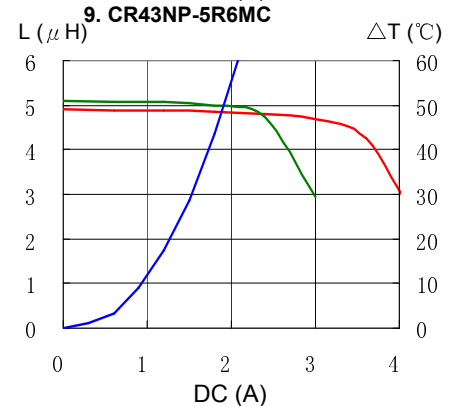
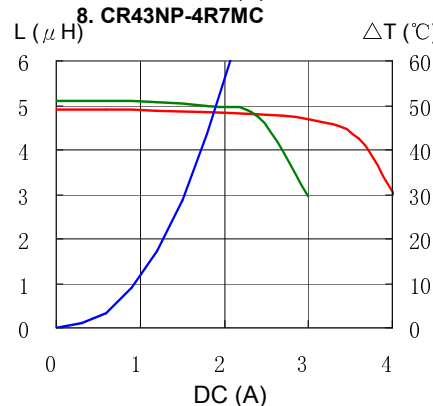
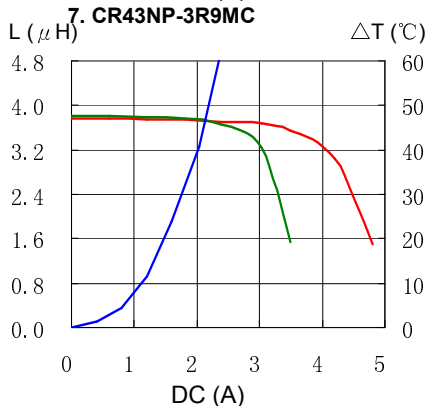
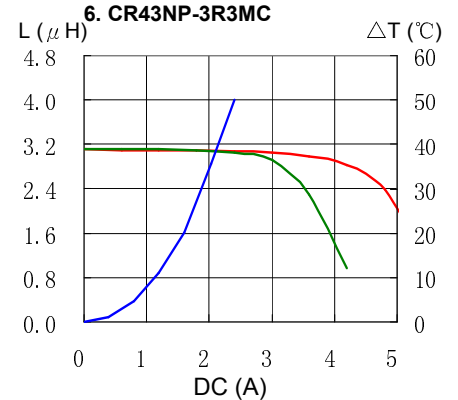
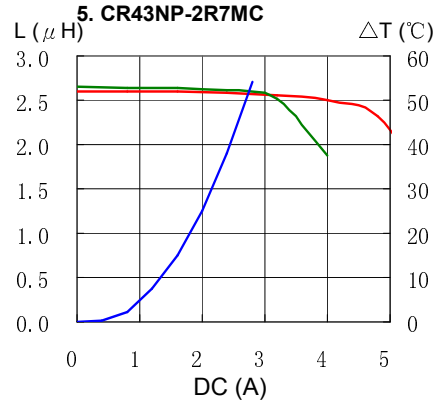
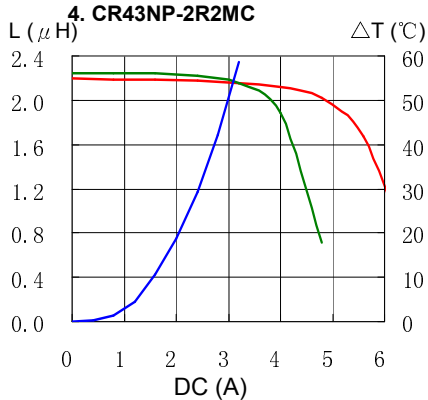
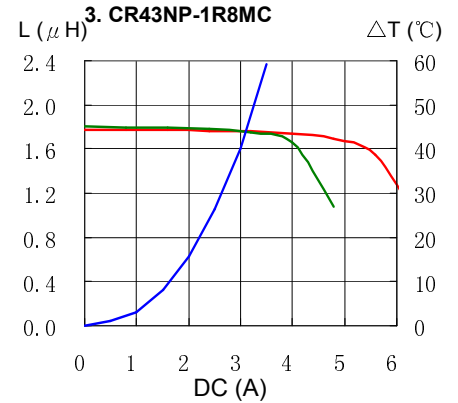
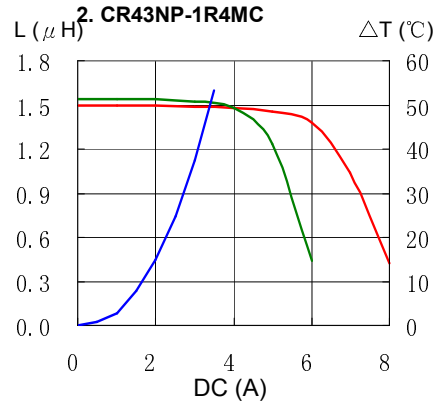
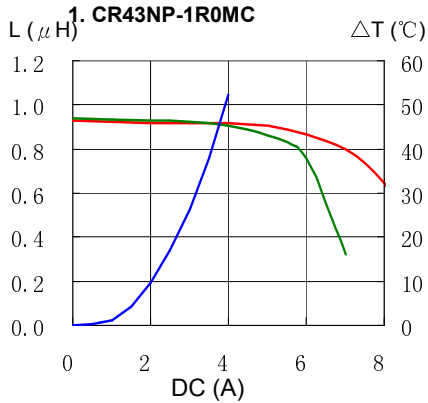
※2. Rated current: The D.C. current at which the inductance decreases to 90% of it's initial value or when $\Delta t=40^\circ\text{C}$, whichever is lower ($T_a=20^\circ\text{C}$).

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Saturation Current & Temperature Rise Graph

— L (20°C) — L (100°C) — ΔT

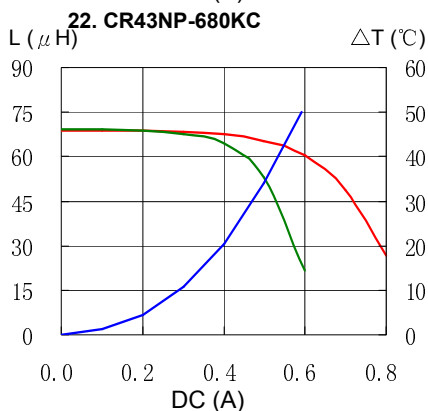
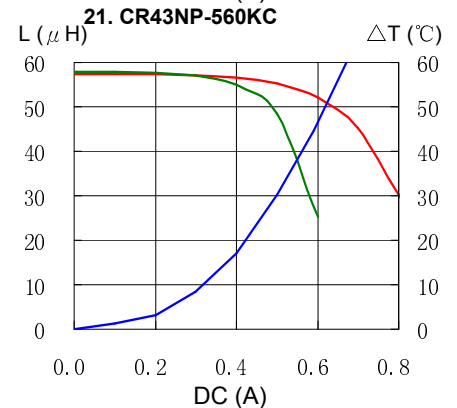
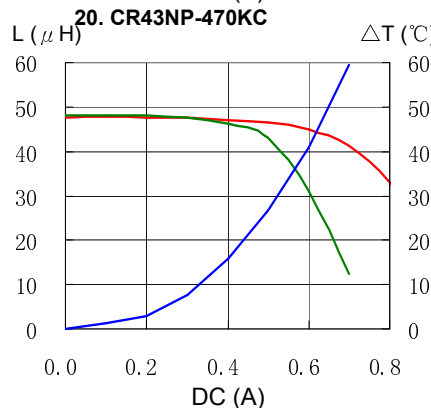
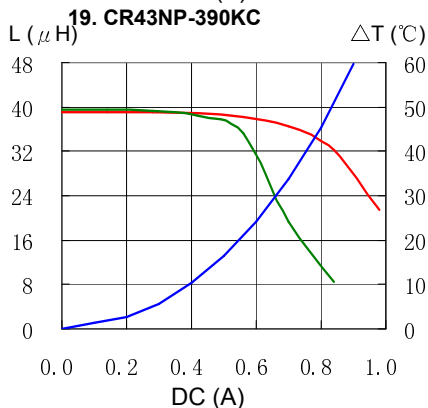
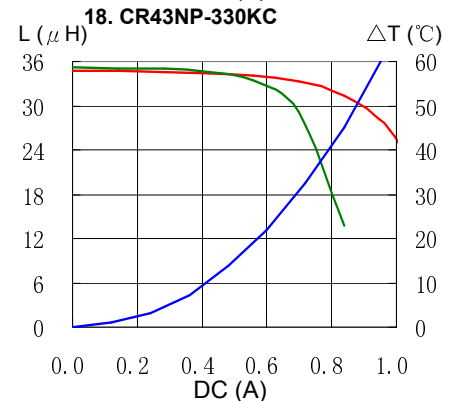
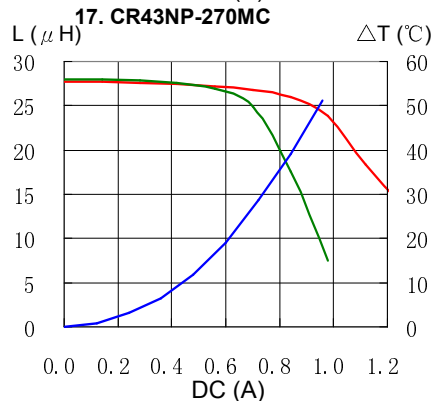
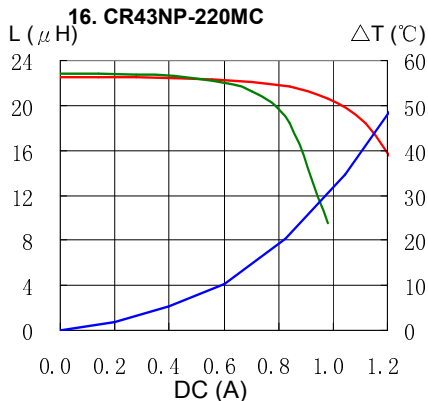
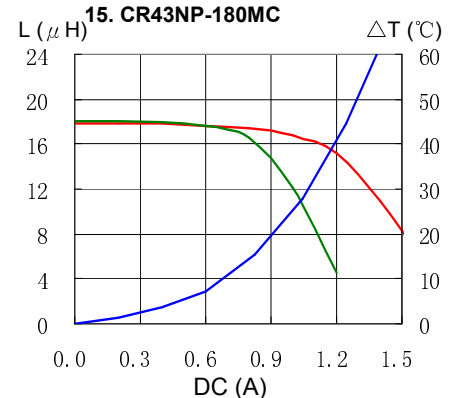
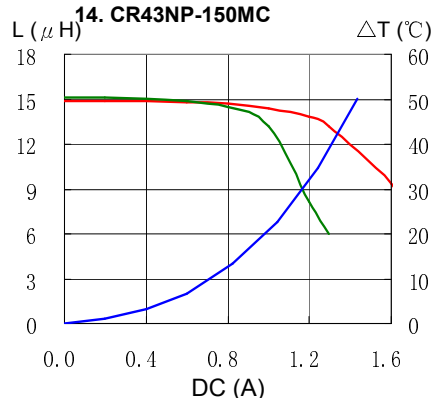
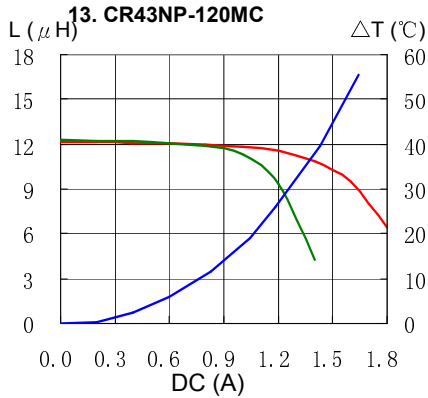


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Saturation Current & Temperature Rise Graph

— L (20°C) — L (100°C) — ΔT



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Solder Reflow Condition



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