



P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY

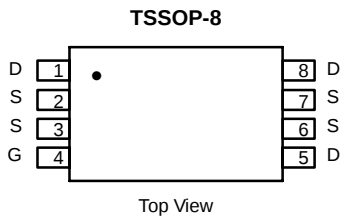
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-12	0.0085 @ $V_{GS} = -4.5$ V	-9.5
	0.0106 @ $V_{GS} = -2.5$ V	-8.5
	0.014 @ $V_{GS} = -1.8$ V	-7.5

FEATURES

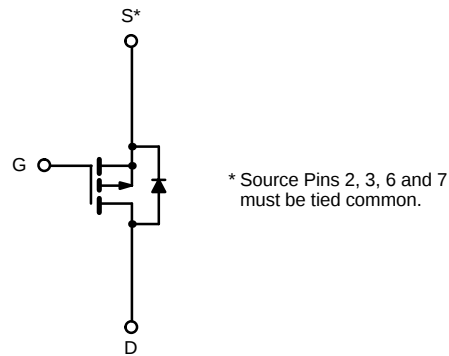
- TrenchFET® Power MOSFET

APPLICATIONS

- Load Switch



Ordering Information: Si6423DQ
Si6423DQ-T1 (with Tape and Reel)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	-12		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25°C	I _D	-9.5	-8.2	A
	T _A = 70°C		-8	-6.5	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	-30		
Continuous Source Current (Diode Conduction) ^a		I _S	-1.35	-0.95	W
Maximum Power Dissipation ^a	T _A = 25°C	P _D	1.5	1.05	
	T _A = 70°C		1.0	0.67	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	60	83	$^\circ\text{C/W}$
	Steady State		100	120	
Maximum Junction-to-Foot	Steady State	R_{thJF}	35	45	

Notes

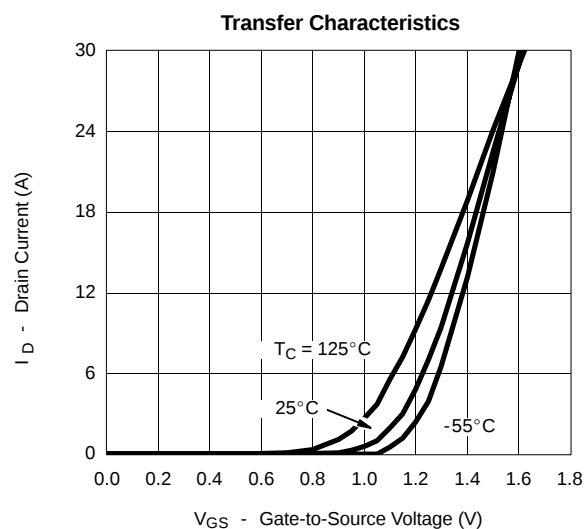
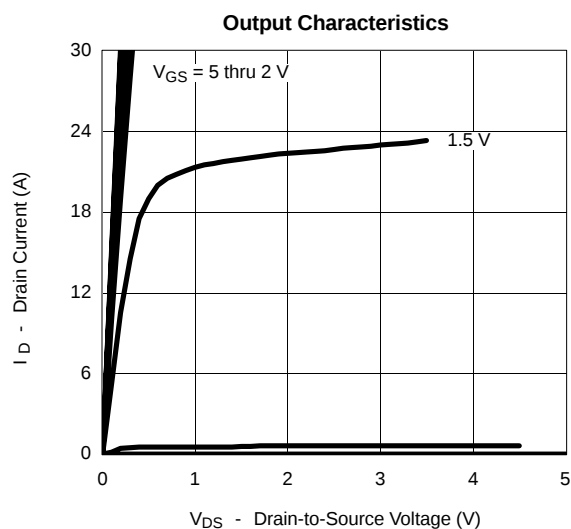
a. Surface Mounted on 1" x 1" FR4 Board.

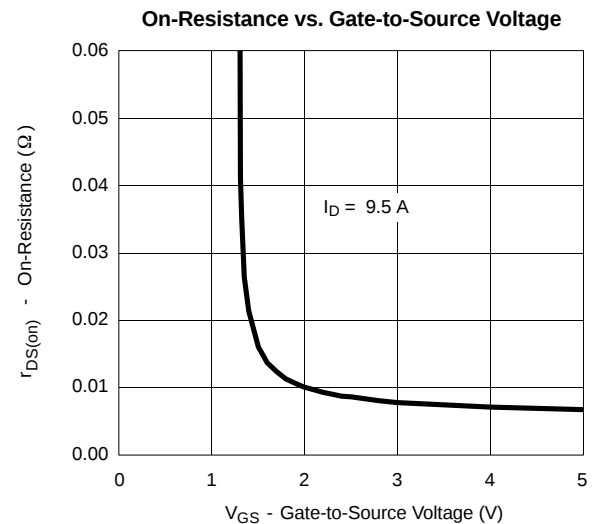
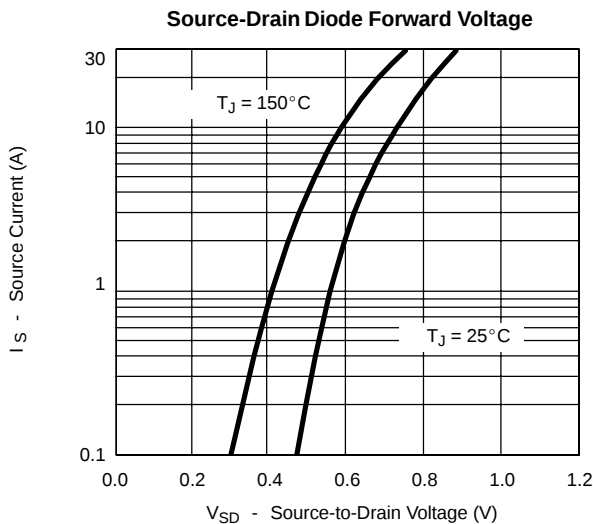
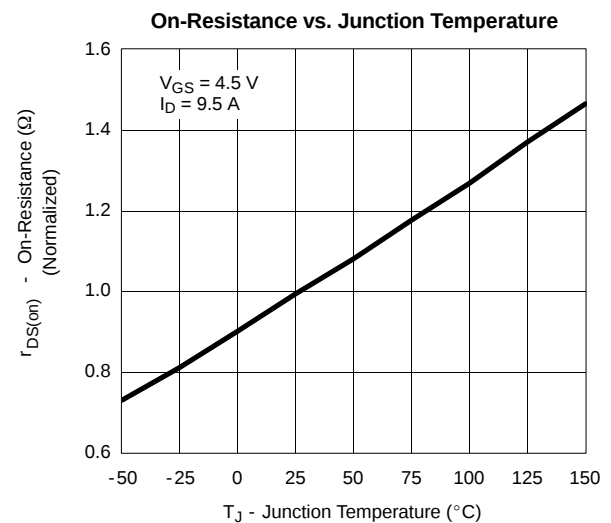
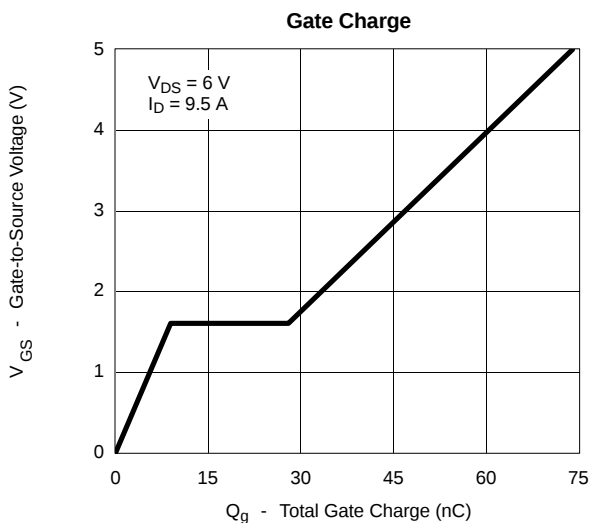
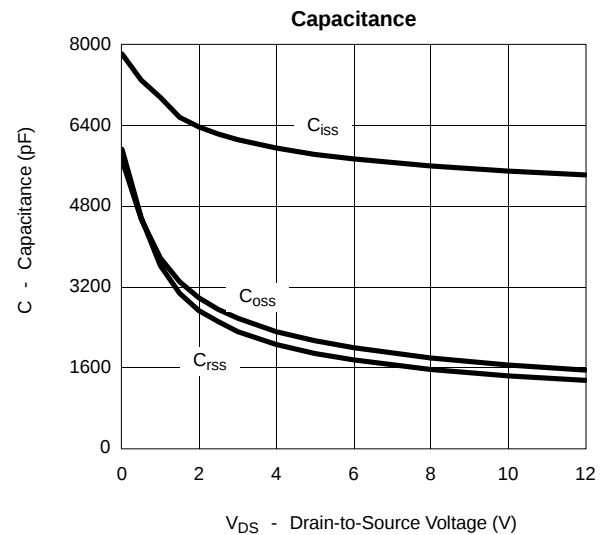
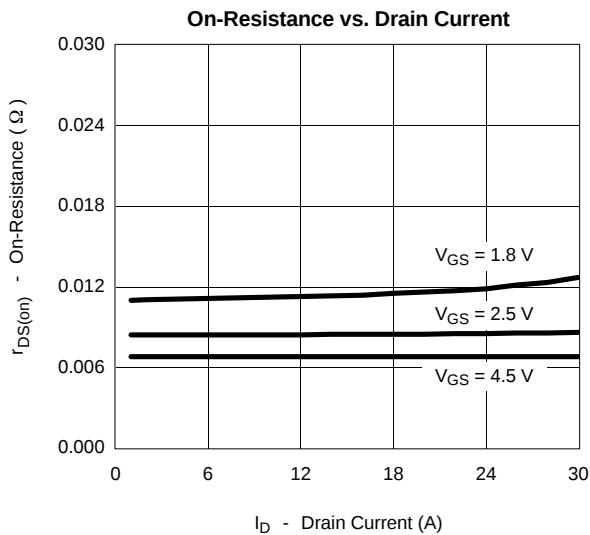
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

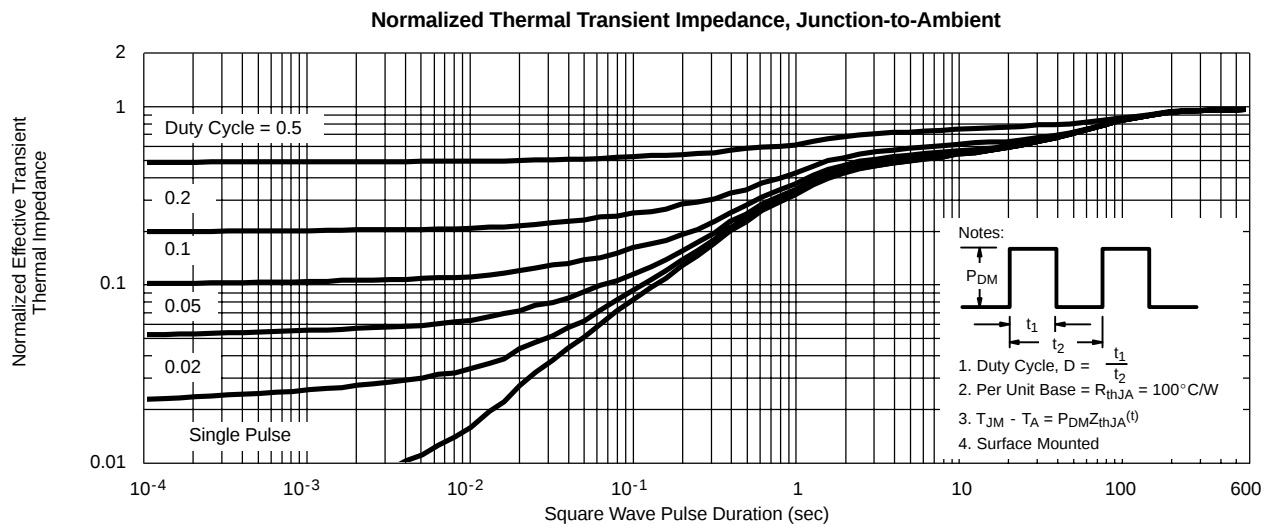
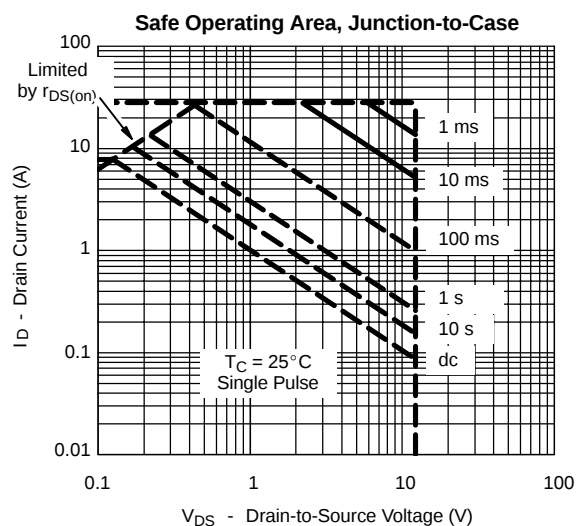
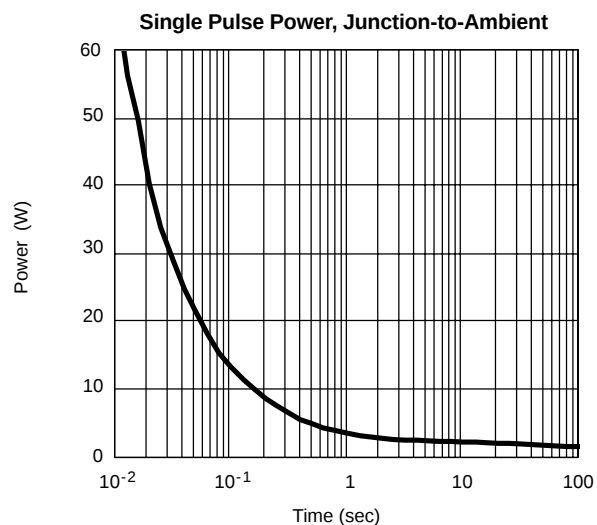
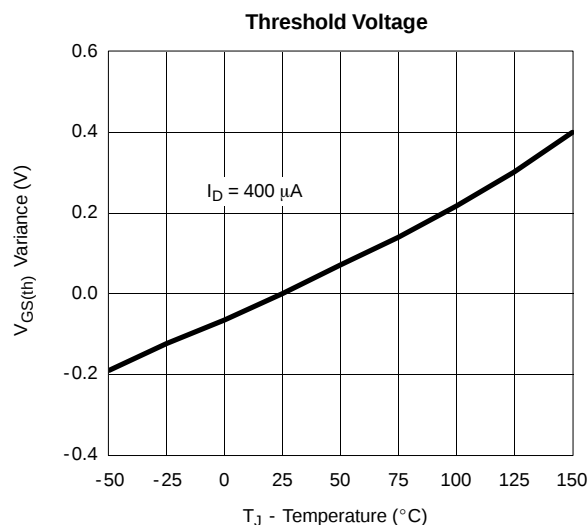
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -400\ \mu\text{A}$	-0.40		-0.8	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\ \text{V}$, $V_{GS} = \pm 8\ \text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -9.6\ \text{V}$, $V_{GS} = 0\ \text{V}$			-1	μA
		$V_{DS} = -9.6\ \text{V}$, $V_{GS} = 0\ \text{V}$, $T_J = 70^\circ\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\ \text{V}$, $V_{GS} = -4.5\ \text{V}$	-20			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = -4.5\ \text{V}$, $I_D = -9.5\ \text{A}$		0.0068	0.0085	Ω
		$V_{GS} = -2.5\ \text{V}$, $I_D = -8.5\ \text{A}$		0.0085	0.0106	
		$V_{GS} = -1.8\ \text{V}$, $I_D = -7.5\ \text{A}$		0.0112	0.014	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\ \text{V}$, $I_D = -9.5\ \text{A}$		45		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.3\ \text{A}$, $V_{GS} = 0\ \text{V}$		-0.58	-1.1	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -6\ \text{V}$, $V_{GS} = -5\ \text{V}$, $I_D = -9.5\ \text{A}$		74	110	nC
Gate-Source Charge	Q_{gs}			9.0		
Gate-Drain Charge	Q_{gd}			19		
Gate Resistance	R_g			3.6		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -6\ \text{V}$, $R_L = 6\ \Omega$ $I_D \approx -1\ \text{A}$, $V_{GEN} = -4.5\ \text{V}$, $R_G = 6\ \Omega$		50	75	ns
Rise Time	t_r			75	110	
Turn-Off Delay Time	$t_{d(off)}$			270	400	
Fall Time	t_f			200	300	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = -1.3\ \text{A}$, $di/dt = 100\ \text{A}/\mu\text{s}$		160	250	

Notes

- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

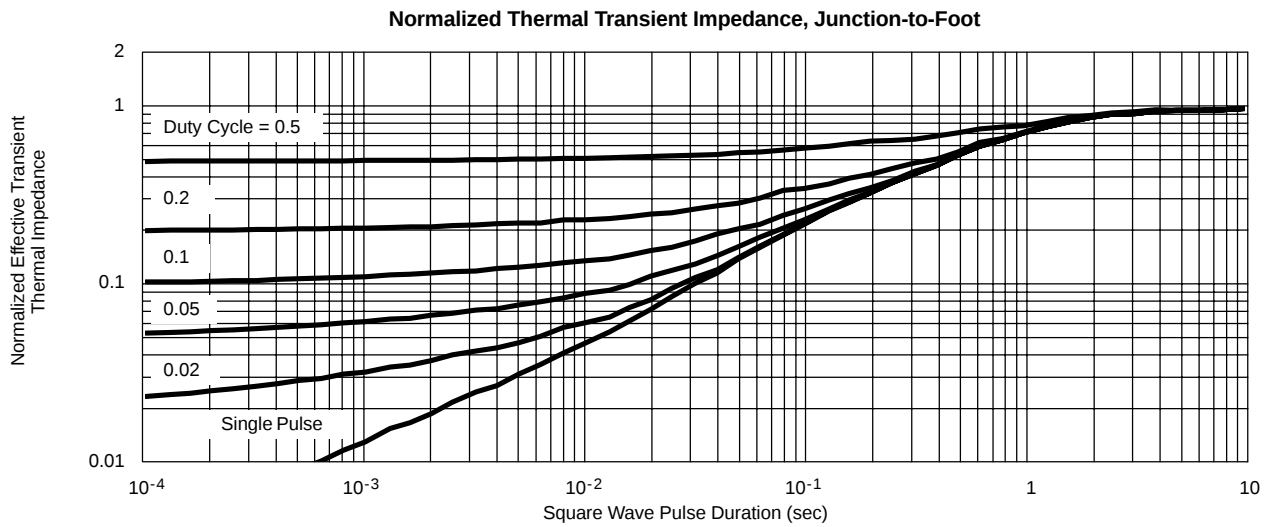
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

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