

Automotive 800 V, 15 A bridge rectifier diode



DPAK HV 2L



Features



- AEC-Q101 qualified
- · PPAP capable
- · Ultralow conduction losses
- · Ultralow reverse losses
- V_{RRM} guaranteed from -40 to +175 °C
- · High overcurrent capability
- High creepage DPAK
- MSL: Level 1
- ECOPACK2 compliant

Application

- Bridge function
- On board charger (OBC)
- · Reverse battery protection
- EV charging stations
- · By-Pass function
- · O-ring function

Description

The STBR1508-Y is a ultralow V_F rectifier. The high quality design of this diode has produced a device with consistently reproducible characteristics and intrinsic ruggedness. These characteristics make it ideal for heavy duty applications that demand long term reliability like automotive applications.

Thanks to its ultralow conduction losses, the STBR1508-Y is especially suitable for use as input bridge diode in battery chargers and charging stations. It is also ideal for usage in reverse battery protection circuit.

Product status link

STBR1508-Y

Product summary			
Symbol Value			
I _{F(AV)}	15 A		
V _{RRM}	800 V		
T _j	-40 to +175 °C		
V _F (typ.)	0.88 V		



1 Characteristics

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified)

Symbol	Param	Value	Unit	
V_{RRM}	Repetitive peak reverse voltage $T_j = -40 ^{\circ}\text{C}$ to +175 $^{\circ}\text{C}$		800	V
V _{RSM}	Non-repetitive surge reverse voltage $t_p = 10 \text{ ms square}$		900	V
I _{F(RMS)}	Forward rms current		21	Α
I _{F(AV)}	Average forward current $T_C = 160 ^{\circ}\text{C}, \delta = 0.5 \text{square wave}$		15	Α
I _{FSM}	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$		200	Α
T _{stg}	Storage temperature range		-65 to +175	°C
Tj	Operating junction temperature -40		-40 to +175	°C

Table 2. Thermal parameters

Symbol	Parameter	Typ. value	Unit
$R_{th(j-c)}$	Junction to case	0.56	°C/W

For more information, please refer to the following application note:

AN5088: Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
L (1)	Devene le disere coment	T _j = 25 °C	V _R = 800 V	-		1	
I _R ⁽¹⁾ Reverse leakage current	Reverse leakage current	T _j = 150 °C	VR - 800 V	-	5	50	μA
V _F ⁽²⁾ Forward voltage drop	Forward voltage drop	I _E = 15 A	-	1.00	1.09	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
		T _j = 150 °C	1F - 13 A	-	0.88	0.97	\ \ \

- 1. Pulse test: $t_p = 5 \text{ ms}$, $\delta < 2\%$
- 2. Pulse test: $t_p = 380 \ \mu s, \ \delta < 2\%$

To evaluate the conduction losses, use the following equation:

 $P = 0.75 \times I_{F(AV)} + 0.0147 \times I_{F}^{2}_{(RMS)}$

For more information, please refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses in a power diode

DS13979 - Rev 1 page 2/10



0

1.1 Characteristics (curves)

Figure 2. Forward voltage drop versus forward current (typical values) $I_{FM}(A)$ 1.0E+03 1.0E+02 T.=150°C 1.0E+01 1.0E+00 1.0E-01 V_{FM} (V) 1.0E-02 0.0 0.4 0.8 1.2 1.6

Figure 3. Forward voltage drop versus forward current (maximum values)

10

IF(AV)(A)

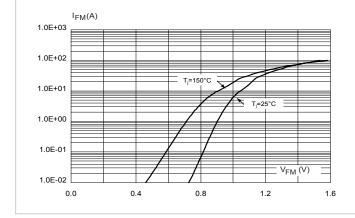


Figure 4. Relative variation of thermal impedance junction to case versus pulse duration

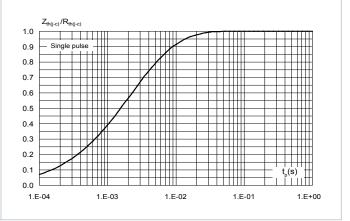


Figure 5. Junction capacitance versus reverse voltage applied (typical values)

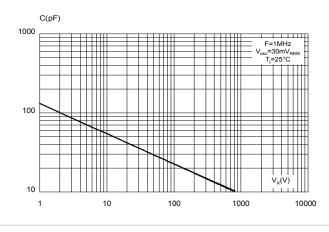
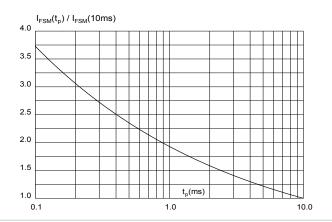


Figure 6. Relative variation of non-repetitive peak surge forward current versus pulse duration (sinusoidal waveform)



DS13979 - Rev 1 page 3/10



Figure 7. Relative variation of non-repetitive peak surge forward current versus initial junction temperature (sinusoidal waveform)

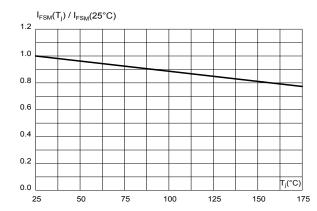


Figure 8. Non repetitive surge peak forward current versus number of cycles

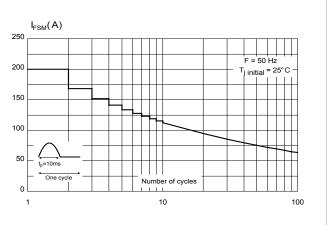
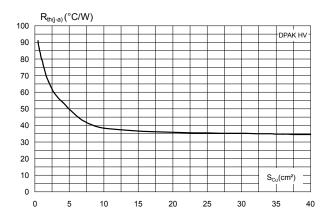


Figure 9. Thermal resistance junction to ambient versus copper surface under tab (typical values, epoxy printed board FR4, e_{Cu} = 70 μ m)



DS13979 - Rev 1 page 4/10



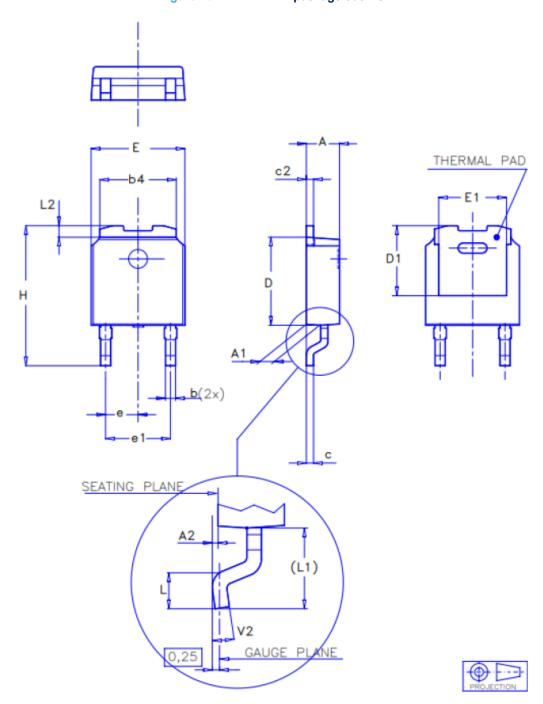
Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 DPAK HV 2L package information

- Epoxy meets UL 94,V0
- Cooling method: by conduction (C)

Figure 10. DPAK HV 2L package outline



DS13979 - Rev 1 page 5/10

8°



Note:

V2

0°

This package drawing may slightly differ from the physical package. However, all the specified dimensions are guaranteed.

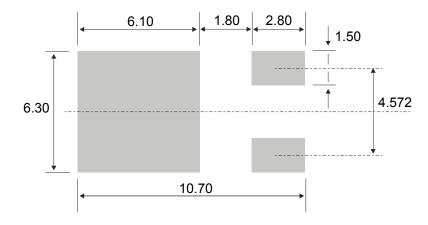
Dimensions Millimeters Inches (for reference only) Ref. Min. Max. Min. Max. Тур. Тур. Α 2.20 2.29 2.40 0.086 0.090 0.095 0.90 0.035 0.044 **A1** 1.10 0.03 0.23 0.001 0.010 A2 b 0.64 0.76 0.90 0.025 0.030 0.036 b4 5.10 5.20 5.40 0.201 0.204 0.213 0.45 0.60 0.017 0.024 С c2 0.48 0.60 0.018 0.024 D 6.00 6.20 0.236 0.245 D1 4.60 4.70 4.80 0.181 0.185 0.189 Ε 6.40 6.60 0.251 0.260 4.95 5.10 5.25 0.194 0.201 0.207 E1 2.16 2.28 2.40 0.085 0.090 0.095 е e1 4.40 4.60 0.173 0.182 Н 9.35 10.10 0.368 0.398 1.50 L 1.00 0.039 0.060 L1 2.60 2.80 3.00 0.102 0.110 0.119 0.025 L2 0.65 0.80 0.95 0.031 0.038

Table 4. DPAK HV 2L package mechanical data



0°

8°



Note: For package and tape orientation, reel and inner box dimensions and tape outline please check TN1173.

DS13979 - Rev 1 page 6/10



2.1.1 Creepage distance between anode and cathode

Table 5. Creepage distance between anode and cathode

Symbol	Parameter			Unit
Cd _{A-K}	Minimum creepage distance between A and K DPAK HV		3.0	mm

Note: DPAK HV creepage distance (anode to cathode) =3.0 mm min. (refer to IEC 60664-1)

DS13979 - Rev 1 page 7/10



3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STBR1508B2Y-TR	BR15 08B2Y	DPAK HV	0.355 g	2500	Tape and reel

DS13979 - Rev 1 page 8/10



Revision history

Table 7. Document revision history

Date	Revision	Changes
17-May-2022	1	First issue.

DS13979 - Rev 1 page 9/10



IMPORTANT NOTICE - READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgment.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. For additional information about ST trademarks, refer to www.st.com/trademarks. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2022 STMicroelectronics - All rights reserved

DS13979 - Rev 1 page 10/10