Mid-Power LED - 3030H Series

STW7C12H-E0

S1W0-3030xx7003-00000000-00002

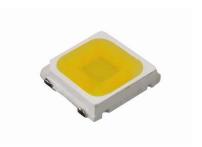
















Product Brief

Description

- This White Colored surface-mount LED comes in standard package dimension. Package Size: 3.3x3.0x0.75mm
- It has a substrate made up of a molded plastic reflector sitting on top of a lead frame.
- The die is attached within the reflector cavity and the cavity is encapsulated by silicone.
- The package design coupled with careful selection of component materials allow these products to perform with high reliability.

Features and Benefits

- Thermally Enhanced Package Design
- Mid Power to High Power up to 0.6W
- Max. Driving Current 200mA
- Compact Package Size
- High Color Quality with CRI Min.70, R9 Free
- Pb-free Reflow Soldering Application

Key Applications

- Horticulture
- Replacement lamps Bulb, Tube
- Commercial
- Industrial
- Residential

Table 1. Product Selection Table

Dout Number	Color	Nominal	Order Code	CRI
Part Number	Color	ССТ	Order Code	Min
		6500K	\$1W0-3030657003-00000000-00002	
	Cool White	5700K	\$1W0-3030577003-00000000-00002	
		5000K	\$1W0-3030507003-00000000-00002	
STW7C12H- E00000IZ	Neutral White	4000K	\$1W0-3030407003-00000000-00002	70
		3500K	\$1W0-3030357003-00000000-00002	
	Warm White	3000K	\$1W0-3030307003-00000000-00002	
		2700K	S1W0-3030277003-00000000-00002	·



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Performance Characteristics

Table 2. Product Selection Guide, $I_F = 65 \text{mA}$, $T_j = 25^{\circ}\text{C}$, RH30%

Typ. CRI, Ra	Nominal CCT [K] ^[1]	Min. PPF [µmol/s]	Max. PPF [µmol/s]	Typ. PPE [µmol/J]	Typ. Flux [lm]	Typ. Efficacy [lm/W]
	6500	0.525	0.590	3.12	39.6	226.5
	5700	0.525	0.590	3.12	40.4	231.5
	5000	0.525	0.590	3.12	41.3	236.5
70	4000	0.515	0.580	3.08	41.1	235.5
	3500	0.515	0.580	3.08	40.8	233.5
	3000	0.500	0.560	3.02	39.3	225.0
	2700	0.500	0.560	3.02	38.0	217.5

Notes:

⁽¹⁾ Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram.

⁽²⁾ Seoul Semiconductor maintains a tolerance of $\pm 5\%$ on PPF and power measurements.

Performance Characteristics

Table 3. Characteristics, I_F=65mA, T_i= 25°C, RH30%

Doromotor	Cumb al		Value			
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Forward Current	I _F	5	65	-	mA	
Forward Voltage ^[1]	V_{F}	2.60	2.70	2.80	V	
CRI ^[1]	R _a	70		80		
R9	R ₉	-100				
Viewing Angle [2]	2Θ _{1/2}	-	120	-	Deg.	
Thermal resistance (J to S) [3]	Rθ _{J-S}	-	7.5		°C/W	
Turn-on voltage	V _F (1uA)	1.9	-	2.7	V	
ESD Sensitivity(HBM) [4]	-		Class 3A J	EDEC JS-001-	2017	

Table 4. Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Forward Current	I _F	200	mA
Power Dissipation	P _D	0.6	W
Junction Temperature ^[5]	T _j	125	°C
Operating Temperature	T _{opr}	-40 ~ + 85	°C
Storage Temperature	T_{stg}	-40 ~ + 100	°C

Notes:

(1) Tolerance : VF : \pm 0.1V, PPF : \pm 5%, Ra : \pm 2, x,y : \pm 0.005

(2) $2\Theta_{1/2}$ is the off-axis where the luminous intensity is 1/2 of the peak intensity.

(3) Thermal resistance: Rth_{JS} (Junction / solder)

(4) A zener diode is included for ESD Protection.

(5) $T_J = T_S + R\theta_{J-S} * P$ $T_J [^{\circ}C] = LED$ Junction Temperature $T_S [^{\circ}C] = LED$ Solder Temperature $P [W] = I_F * V_F$

• LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power *dissipation does not* exceed the absolute maximum rating of the product.

- Thermal resistance can be increased substantially depending on the heat sink design/operating condition, and the maximum possible driving current will decrease accordingly.
- All measurements were made under the standardized environment of Seoul Semiconductor.

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Fig 1. Color Spectrum, T_i = 25°C

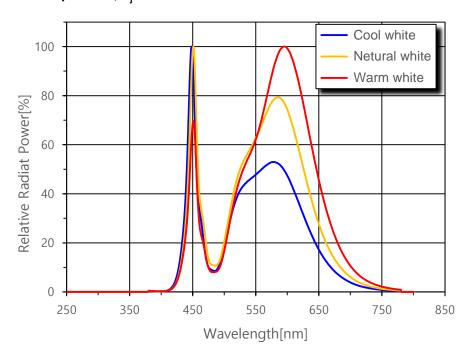
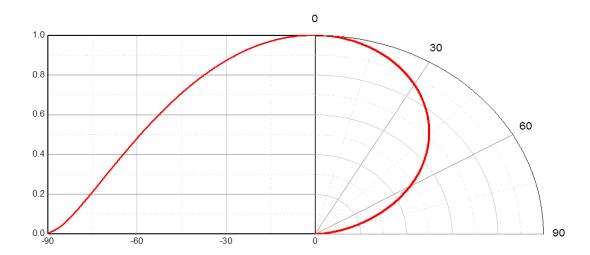


Fig 2. Radiant Pattern, T_i = 25°C



Characteristics Graph

Fig 3. Forward Voltage vs. Forward Current, $T_j = 25^{\circ}C$

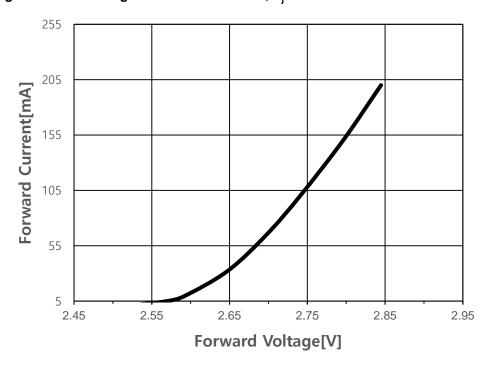
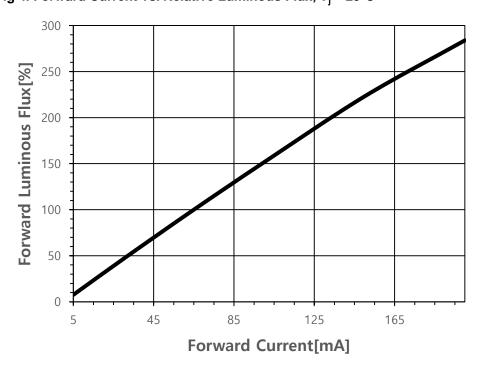


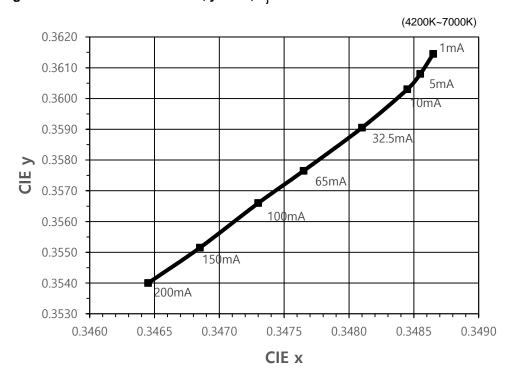
Fig 4. Forward Current vs. Relative Luminous Flux, $T_i = 25^{\circ}C$

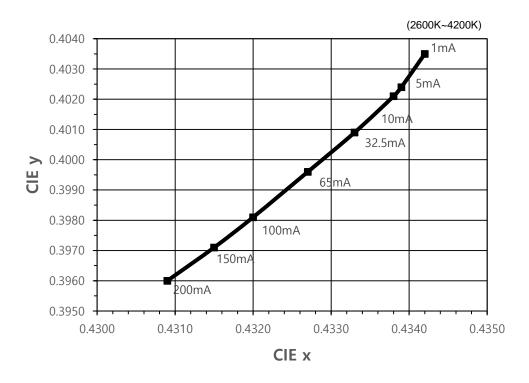


Use of less than 5mA is not recommended

Characteristics Graph

Fig 5. Forward Current vs. CIE x, y Shift, T_i = 25°C





· Use of less than 5mA is not recommended

Fig 6. Junction Temperature vs. Relative Luminous Flux, I_F=65mA

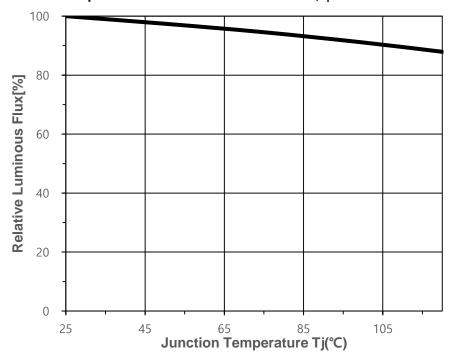


Fig 7. Junction Temperature vs. Relative Forward Voltage, I_F=65mA

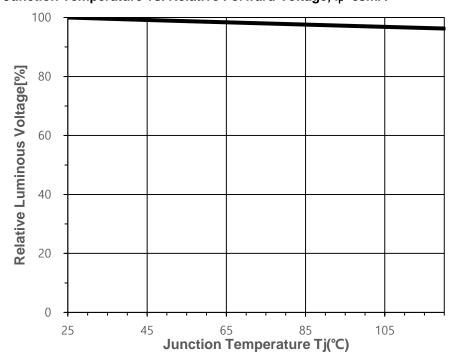
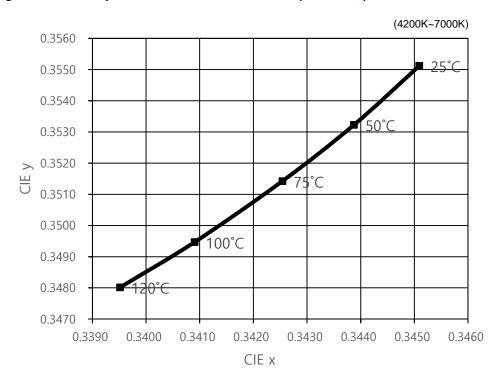


Fig 8. Chromaticity Coordinate vs. Junction Temperature, I_F=65mA



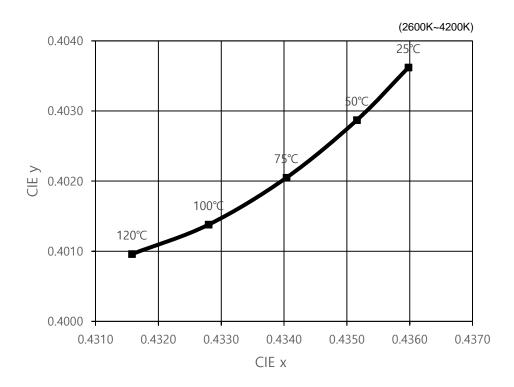
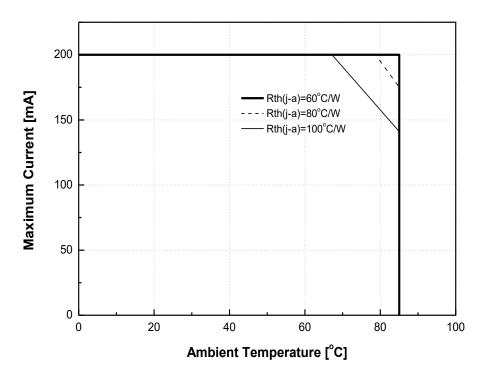


Fig 9. Ambient Temperature vs. Maximum Forward Current, $T_{i_max} = 125$ °C



Color Bin Structure

Table 5. Bin Code description, $T_j=25^{\circ}C$, $I_F=65mA$

Part Number		PPF [µmol/s	5]	Color Chromaticity	Typical Forward Voltage (V)			
	Bin Code	Min.	Max.	Coordinate	Bin Code	Min.	Max.	
	PL	0.500	0.560		Y0	2.60	2.70	
STW7C12H- E00000IZ	PM 0.515 0.580	Refer to page 14-16.	Y1	2.70	2.80			
	PH	0.525	0.590					

Table 6. PPF rank distribution

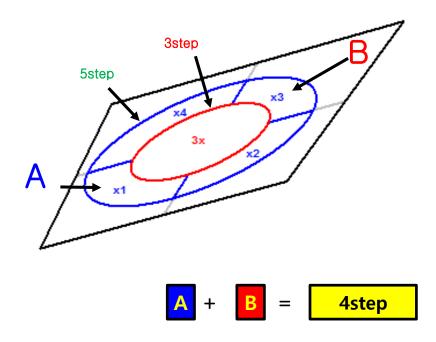
Available ranks

сст	CIE	PPF Rank
6500K	А	РН
5700K	В	РН
5000K	С	РН
4000K	Е	РМ
3500K	F	PM
3000K	G	PL
2700K	Н	PL

^{*}Notes:

All measurements were made under the standardized environment of Seoul Semiconductor. In order to ensure availability, single color rank will not be orderable.

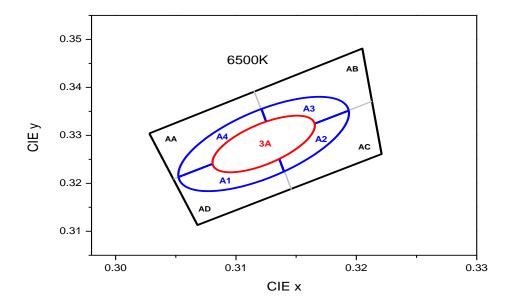
Color Bin Structure



* Only for reference

Item	Bin #1	Bin #2	Note
	3x	3x	3step Kitting
CIE	x1	х3	Faton Vitting
	x2	x4	5step Kitting
VF		Do not specify rank, randomly assigned.	
LM		Do not specify rank, randomly assigned.	

Color Bin Structure



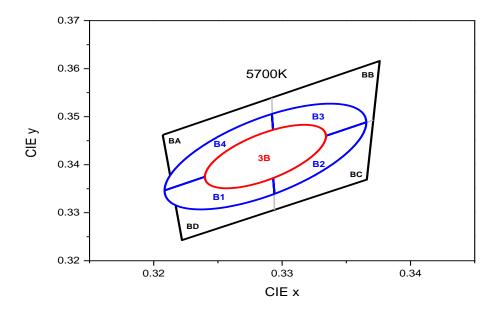
3A						
Center point	0.3123 : 0.3282					
Major Axis a	0.0067					
Minor Axis b	0.0029					
Ellipse Rotation Angle	59					

5step					
Center point	0.3123 : 0.3282				
Major Axis a	0.0112				
Minor Axis b	0.0048				
Ellipse Rotation Angle	59				

А	1	А	2	А	3	А	.4
CIE X	CIE Y						
0.3052	0.3213	0.314	0.3224	0.3194	0.3352	0.3121	0.3356
0.3081	0.3241	0.3136	0.3251	0.3165	0.3324	0.3125	0.3329
0.314	0.3224	0.3194	0.3352	0.3121	0.3356	0.3052	0.3213
0.3136	0.3251	0.3165	0.3324	0.3125	0.3329	0.3081	0.3241

А	A	А	В	AC		AD	
CIE X	CIE Y						
0.3028	0.3304	0.3115	0.3393	0.3131	0.329	0.3048	0.3209
0.3048	0.3209	0.3131	0.329	0.3146	0.3187	0.3068	0.3113
0.3131	0.329	0.3213	0.3371	0.3221	0.3261	0.3146	0.3187
0.3115	0.3393	0.3205	0.3481	0.3213	0.3371	0.3131	0.329

Color Bin Structure



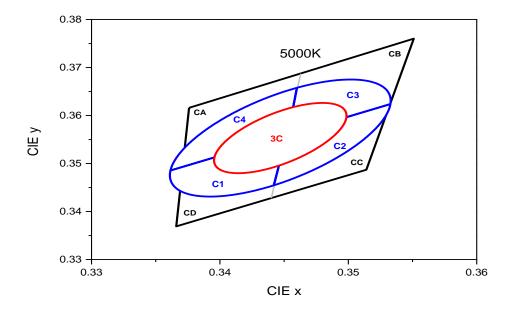
3B							
Center point	0.3287 : 0.3417						
Major Axis a	0.0075						
Minor Axis b	0.0032						
Ellipse Rotation Angle	59						

5step							
Center point	0.3287 : 0.3417						
Major Axis a	0.0124						
Minor Axis b	0.0053						
Ellipse Rotation Angle	59						

В	B1		B2		В3		4
CIE X	CIE Y						
0.3209	0.3347	0.3294	0.3339	0.3365	0.3488	0.3292	0.3505
0.324	0.3375	0.3293	0.3373	0.3334	0.346	0.3293	0.3472
0.3294	0.3339	0.3365	0.3488	0.3292	0.3505	0.3209	0.3347
0.3293	0.3373	0.3334	0.346	0.3293	0.3472	0.324	0.3375

В	A	В	В	В	С	В	D
CIE X	CIE Y						
0.3207	0.3462	0.3292	0.3539	0.3293	0.3423	0.3215	0.3353
0.3215	0.3353	0.3293	0.3423	0.3294	0.3306	0.3222	0.3243
0.3293	0.3423	0.3371	0.3493	0.3366	0.3369	0.3294	0.3306
0.3292	0.3539	0.3376	0.3616	0.3371	0.3493	0.3293	0.3423

Color Bin Structure



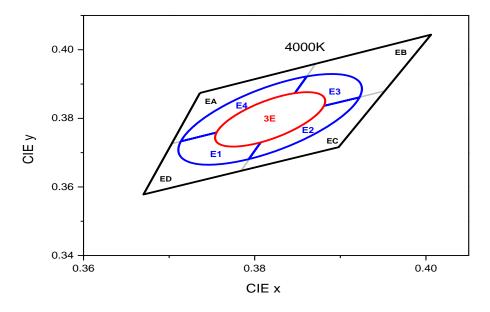
3C						
Center point	0.3447 : 0.3553					
Major Axis a	0.0082					
Minor Axis b	0.0035					
Ellipse Rotation Angle	60					

5step							
Center point	0.3447 : 0.3553						
Major Axis a	0.0137						
Minor Axis b	0.0059						
Ellipse Rotation Angle	60						

C	C1		C2		C3		4
CIE X	CIE Y						
0.3361	0.3485	0.3442	0.3455	0.3533	0.3624	0.346	0.3658
0.3396	0.3513	0.3446	0.3496	0.3498	0.3596	0.3457	0.3617
0.3442	0.3455	0.3533	0.3624	0.346	0.3658	0.3361	0.3485
0.3446	0.3496	0.3498	0.3596	0.3457	0.3617	0.3396	0.3513

С	CA		СВ		cc		D
CIE X	CIE Y						
0.3376	0.3616	0.3463	0.3687	0.3452	0.3558	0.3371	0.3493
0.3371	0.3493	0.3452	0.3558	0.344	0.3428	0.3366	0.3369
0.3452	0.3558	0.3533	0.3624	0.3514	0.3487	0.344	0.3428
0.3463	0.3687	0.3551	0.376	0.3533	0.3624	0.3452	0.3558

Color Bin Structure



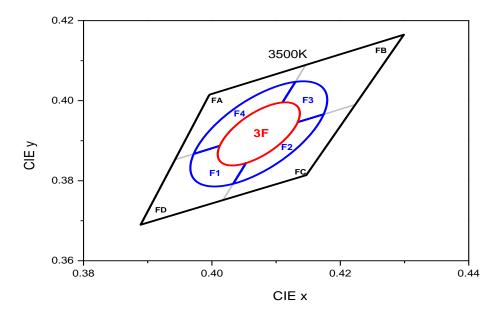
3E						
Center point	0.3818 : 0.3797					
Major Axis a	0.0094					
Minor Axis b	0.0040					
Ellipse Rotation Angle	54					

5step						
Center point	0.3818 : 0.3797					
Major Axis a	0.0157					
Minor Axis b	0.0067					
Ellipse Rotation Angle	54					

E	E1		E2		E3		4
CIE X	CIE Y						
0.3714	0.3733	0.3793	0.3681	0.3922	0.3861	0.3861	0.3923
0.3756	0.3759	0.3807	0.373	0.388	0.3836	0.3847	0.3874
0.3793	0.3681	0.3922	0.3861	0.3861	0.3923	0.3714	0.3733
0.3807	0.373	0.388	0.3836	0.3847	0.3874	0.3756	0.3759

E	EA		ЕВ		EC		D
CIE X	CIE Y						
0.3736	0.3874	0.3871	0.3959	0.3828	0.3803	0.3703	0.3726
0.3703	0.3726	0.3828	0.3803	0.3784	0.3647	0.367	0.3578
0.3828	0.3803	0.3952	0.388	0.3898	0.3716	0.3784	0.3647
0.3871	0.3959	0.4006	0.4044	0.3952	0.388	0.3828	0.3803

Color Bin Structure



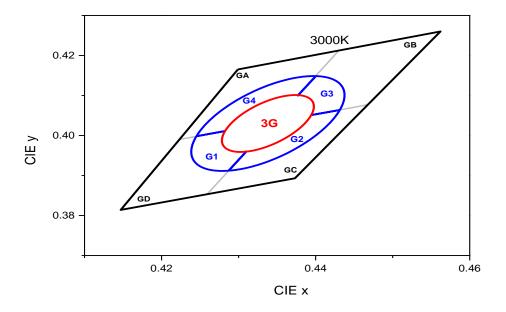
3F							
Center point	0.4073 : 0.3917						
Major Axis a	0.0093						
Minor Axis b	0.0041						
Ellipse Rotation Angle	54						

5step						
Center point	0.4073 : 0.3917					
Major Axis a	0.0155					
Minor Axis b	0.0069					
Ellipse Rotation Angle	54					

F	F1		F2		F3		4
CIE X	CIE Y						
0.3972	0.3867	0.4033	0.3792	0.4174	0.3966	0.413	0.4047
0.4012	0.3887	0.4053	0.3844	0.4133	0.3946	0.411	0.3996
0.4033	0.3792	0.4174	0.3966	0.413	0.4047	0.3972	0.3867
0.4053	0.3844	0.4133	0.3946	0.411	0.3996	0.4012	0.3887

F	FA		FB		FC		D
CIE X	CIE Y						
0.3996	0.4015	0.4146	0.4089	0.4082	0.392	0.3943	0.3853
0.3943	0.3853	0.4082	0.392	0.4017	0.3751	0.3889	0.369
0.4082	0.392	0.4223	0.399	0.4147	0.3814	0.4017	0.3751
0.4146	0.4089	0.4299	0.4165	0.4223	0.399	0.4082	0.392

Color Bin Structure



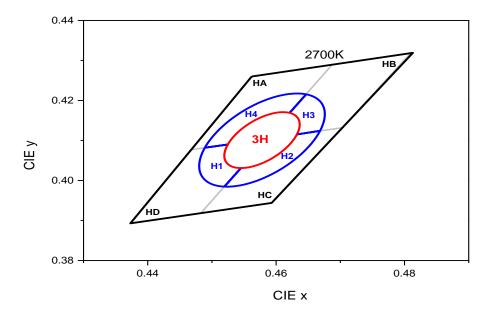
3G							
Center point	0.4338 : 0.4030						
Major Axis a	0.0083						
Minor Axis b	0.0041						
Ellipse Rotation Angle	53						

5step						
Center point	0.4338 : 0.4030					
Major Axis a	0.0139					
Minor Axis b	0.0068					
Ellipse Rotation Angle	53					

G	G1		G2		G3		4
CIE X	CIE Y						
0.4245	0.3998	0.4287	0.3912	0.4432	0.4064	0.44	0.4148
0.4282	0.4011	0.431	0.396	0.4394	0.4051	0.4377	0.41
0.4287	0.3912	0.4432	0.4064	0.44	0.4148	0.4245	0.3998
0.431	0.396	0.4394	0.4051	0.4377	0.41	0.4282	0.4011

G	6A	G	В	G	С	G	D
CIE X	CIE Y						
0.4299	0.4165	0.443	0.4212	0.4345	0.4033	0.4223	0.399
0.4223	0.399	0.4345	0.4033	0.4259	0.3853	0.4147	0.3814
0.4345	0.4033	0.4468	0.4077	0.4373	0.3893	0.4259	0.3853
0.443	0.4212	0.4562	0.426	0.4468	0.4077	0.4345	0.4033

Color Bin Structure



3H							
Center point	0.4578 : 0.4101						
Major Axis a	0.0081						
Minor Axis b	0.0042						
Ellipse Rotation Angle	54						

5step						
Center point	0.4578 : 0.4101					
Major Axis a	0.0135					
Minor Axis b	0.007					
Ellipse Rotation Angle	54					

Н	H1		H2		H3		4
CIE X	CIE Y						
0.4489	0.4082	0.4519	0.3984	0.4668	0.4124	0.4647	0.4216
0.4525	0.409	0.4545	0.4031	0.4632	0.4115	0.4621	0.4169
0.4519	0.3984	0.4668	0.4124	0.4647	0.4216	0.4489	0.4082
0.4545	0.4031	0.4632	0.4115	0.4621	0.4169	0.4525	0.409

Н	НА		НВ		НС		D
CIE X	CIE Y						
0.4562	0.426	0.4687	0.4289	0.4585	0.4104	0.4468	0.4077
0.4468	0.4077	0.4585	0.4104	0.4483	0.3919	0.4373	0.3893
0.4585	0.4104	0.4703	0.4132	0.4593	0.3944	0.4483	0.3919
0.4687	0.4289	0.481	0.4319	0.4703	0.4132	0.4585	0.4104

ESD Protection Device

Mechanical Dimensions

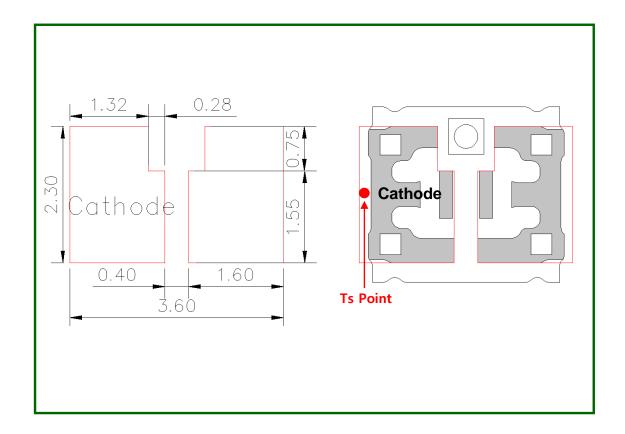
Top View Bottom (Transmittance) View Cathode 3.30 Cathode Circuit Cathode Anode Anode

(1) All dimensions are in millimeters.

(2) Scale: none

(3) Undefined tolerance is $\pm 0.07 \text{mm}$

Recommended Solder Pad



Notes:

- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) This drawing without tolerances are for reference only
- (4) Undefined tolerance is $\pm 0.1 \text{mm}$

Reflow Soldering Characteristics

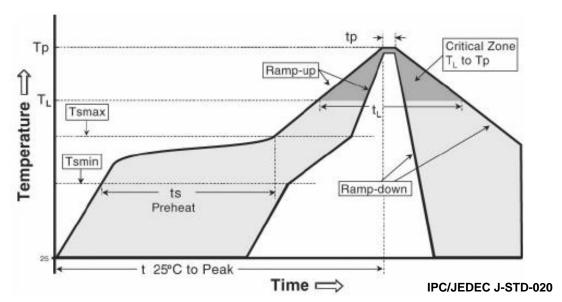


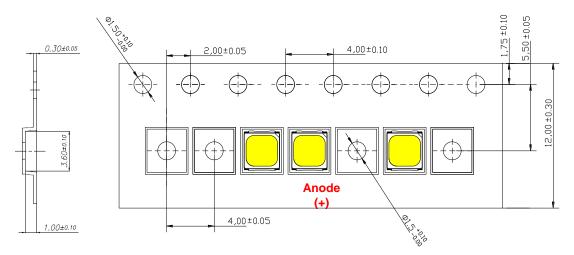
Table 7.

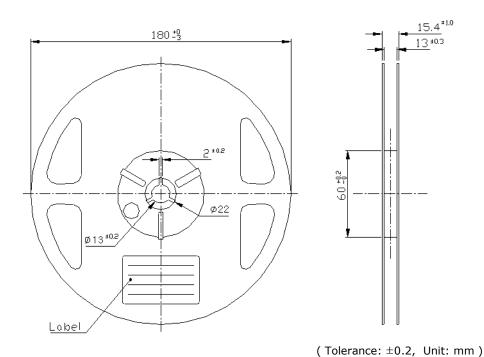
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (Tsmax to Tp)	3° C/second max.	3° C/second max.
Preheat - Temperature Min (Tsmin) - Temperature Max (Tsmax) - Time (Tsmin to Tsmax) (ts)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (TL) - Time (tL)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak Temperature (Tp)	215℃	260°C
Time within 5°C of actual Peak Temperature (tp)2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Caution

- (1) Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
- (3) Die slug is to be soldered.
- (4) When soldering, do not put stress on the LEDs during heating.
- (5) After soldering, do not warp the circuit board.

Emitter Tape & Reel Packing

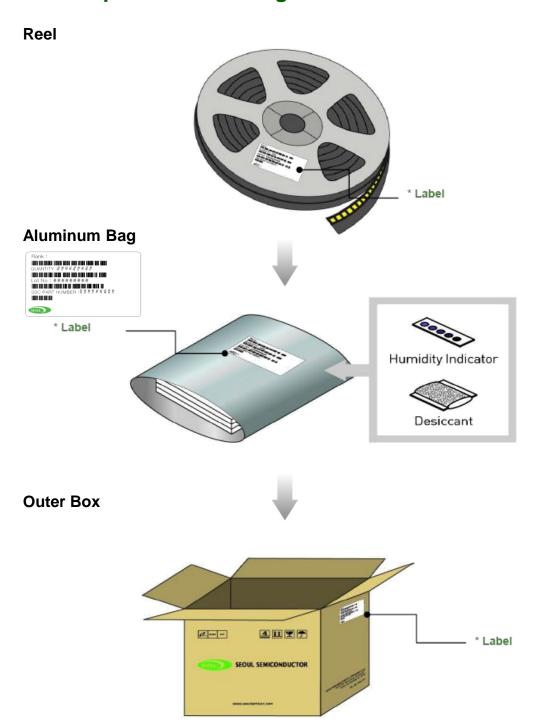




- (1) Quantity: 4,000pcs/Reel
- (2) Cumulative Tolerance : Cumulative Tolerance/10 pitches to be ± 0.2 mm
- (3) Adhesion Strength of Cover Tape
 Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at the angle of 10° to the carrier tape.
- (4) Package: P/N, Manufacturing data Code No. and Quantity to be indicated on a damp proof Package.



Emitter Tape & Reel Packing





Product Nomenclature

Table 8. Part Numbering System: $X_1X_2X_3X_4X_5X_6X_7X_8-X_9X_{10}$

Code	Description	Part Number	Value
X ₁	Company	S	
X ₂	Top View LED series	Т	
X ₃ X ₄	Color Specification	W7	CRI 80
X ₅	Package series	С	C series
X ₆ X ₇	Characteristic code	12	
X ₈	Revision	Н	
X ₉ X ₁₀	Characteristic	E0	

Code	Description	Order Code	Value
X ₁	Company	S	Seoul Semiconductor
X ₂	Level of Integration	1	Discrete LED
X_3X_4	Technology	Wo	General White
X ₅			
$X_6X_7X_8X_9$	Dimension	3030	
X ₁₀ X ₁₁	CCT	xx	
X ₁₂ X ₁₃	CRI	70	
X ₁₄ X ₁₅	Vf	03	
X ₁₆			
X ₁₇ X ₁₈ X ₁₉	Characteristic code Flux Rank	000	
X ₂₀ X ₂₁ X ₂₂	Characteristic code Vf Rank	000	
X ₂₃ X ₂₄	Characteristic code Color Step	00	
X ₂₅			
X ₂₆ X ₂₇	Туре	00	
X ₂₈ X ₂₉ X ₃₀	Internal code	002	

Table 9. Lot Numbering System $: Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$

Lot Number Code	Description	Lot Number	Value
Y ₁ Y ₂	Year		
Y ₃	Month		
Y ₄ Y ₅	Day		
Y ₆	Top View LED series		
Y ₇ Y ₈ Y ₉ Y ₁₀	Mass order		
Y ₁₁ Y ₁₂ Y ₁₃ Y ₁₄ Y ₁₅ Y ₁₆ Y ₁₇	Internal Number		

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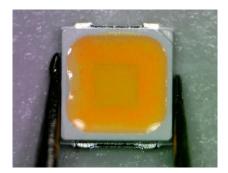


Handling of Silicone Resin for LEDs

(1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.



- (3) When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that mechanical pressure on the surface of the resin must be prevented. This is assured by choosing a pick and place nozzle which is larger than the LED's reflector area.
- (4) Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust.

As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.

- (5) SSC suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin.

 Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (6) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this. product with acid or sulfur material in sealed space.

Precaution for Use

(1) Storage

RH50%.

To avoid the moisture penetration, we recommend store in a dry box with a desiccant. The recommended storage temperature range is 5° C to 30° C and a maximum humidity of

(2) Use Precaution after Opening the Packaging

Use proper SMT techniques when the LED is to be soldered dipped as separation of the lens may affect the light output efficiency.

Pay attention to the following:

- a. Recommend conditions after opening the package
 - Sealing
 - Temperature : 5 ~ 30°C Humidity : less than RH60%
- b. If the package has been opened more than 4 week(MSL_2a) or the color of the desiccant changes, components should be dried for 10-24hr at 65±5°C
- (3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.
- (4) Do not rapidly cool device after soldering.
- (5) Components should not be mounted on warped (non coplanar) portion of PCB.
- (6) Radioactive exposure is not considered for the products listed here in.
- (7) Gallium arsenide is used in some of the products listed in this publication.
 These products are dangerous if they are burned or shredded in the process of disposal.
 It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.
- (8) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.
- (9) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.
- (10) Don't recommend to use it for cold storage lighting

Precaution for Use

- (10) The appearance and specifications of the product may be modified for improvement without notice.
- (11) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.
- (12) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.
- (13) Attaching LEDs, do not use adhesives that outgas organic vapor.
- (14) The driving circuit must be designed to allow forward voltage only when it is ON or OFF.
 If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.
- (15) Similar to most Solid state devices;
 LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS).
 Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.
- a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)

Precaution for Use

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
 (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires.
- This damage usually appears due to the thermal stress produced during the EOS event.
- c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:
 - A surge protection circuit
 - An appropriately rated over voltage protection device
 - A current limiting device

Company Information

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Company Information

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

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