

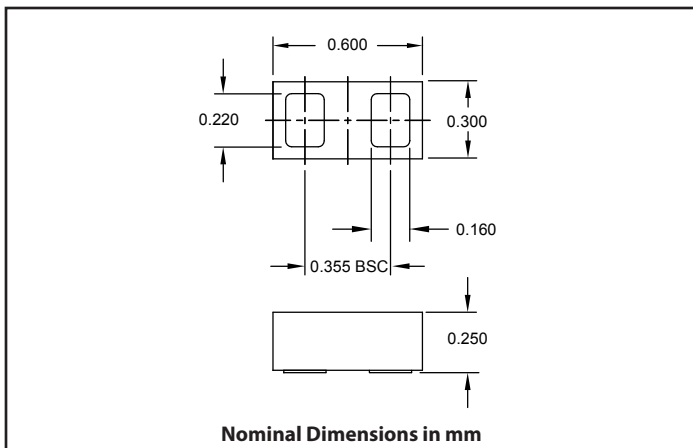
PROTECTION PRODUCTS

Description

μClamp® TVS diodes are designed to protect sensitive electronics from damage or latch-up due to ESD. It features large cross-sectional area junctions for conducting high transient currents. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

μClamp®2411ZA is in a 2-pin SLP0603P2X3F package. It measures 0.6 x 0.3 mm with a nominal height of only 0.25mm. Leads are finished with lead-free NiAu. Each device will protect one line operating at 24 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, tablets, and notebook computers.

Package Dimension



Features

- Transient protection for data and power lines to IEC 61000-4-2 (ESD)
IEC 61000-4-4 (EFT)
Cable Discharge Event (CDE)
- Ultra-small package
- Protects one data or DC Power line
- Low clamping voltage
- Working voltage: 24V
- Low leakage current
- Solid-state silicon-avalanche technology

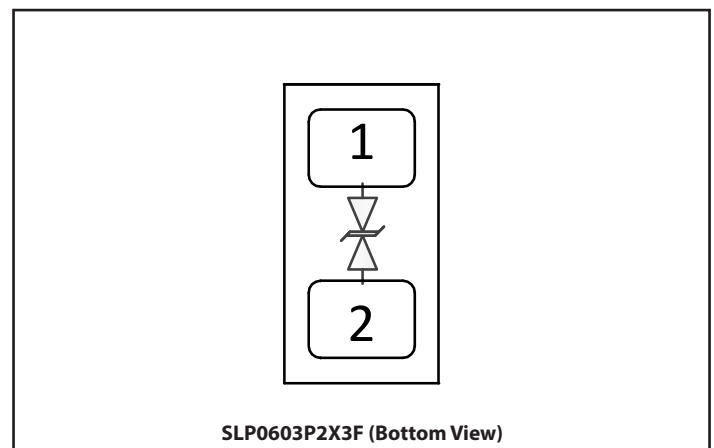
Mechanical Characteristics

- SLP0603P2X3F package
- Pb-Free, Halogen Free, RoHS/WEEE compliant
- Nominal Dimensions: 0.6 x 0.3 x 0.25 mm
- Lead Finish: NiAu
- Marking: Marking code
- Packaging: Tape and Reel

Applications

- Cellular Handsets & Accessories
- Notebook Computers
- Tablets
- Portable Instrumentation
- Chip-on-Glass Driver IC data line
- Peripherals
- 24V DC Power Rails

Schematic & Pin Configuration



Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	P _{PK}	150	W
Peak Pulse Current (tp = 8/20μs)	I _{PP}	3	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V _{ESD}	±20 ±17	kV
Operating Temperature	T _J	-40 to +85	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

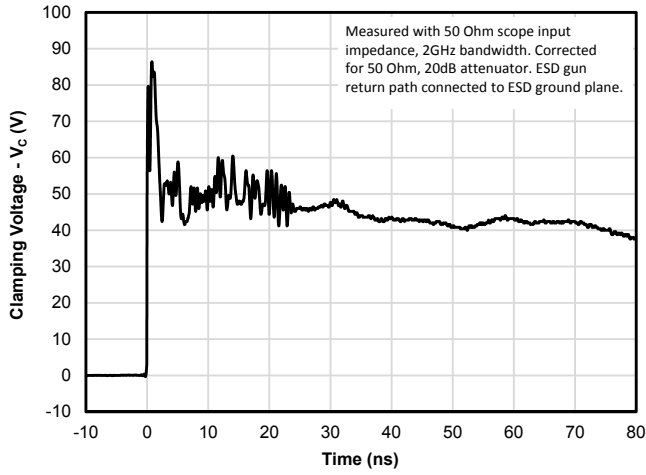
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}	Pin 1 to 2 or Pin 2 to Pin 1			24	V
Reverse Breakdown Voltage	V _{BR}	I _{BR} = 1mA, Pin 1 to 2 or Pin 2 to 1	27	32	36	V
Reverse Leakage Current	I _R	V _{RWM} = 24V, Pin 1 to 2 or Pin 2 to Pin 1		0.05	0.100	μA
Clamping Voltage	V _C	I _{PP} = 2A, tp = 8/20μs			45	V
ESD Clamping Voltage ²	V _C	I = 4A, tlp = 0.2/100ns		36		V
		I = 16A, tlp = 0.2/100ns		48		
Dynamic Resistance ^{2,3}	R _{DYN}	tlp = 0.2/100ns		1		Ω
Junction Capacitance	C _J	I/O to GND, V _R = 0V, f = 1MHz		9	15	pF

Notes:

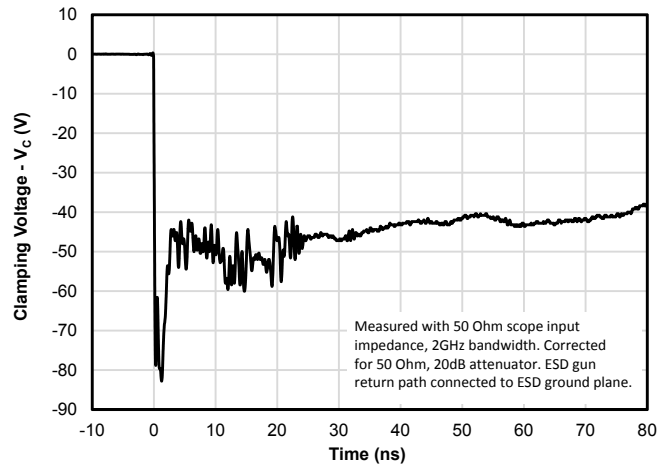
- ESD gun return path connected to ESD ground plane.
- Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns, I_{TLP} and V_{TLP} averaging window: t1 = 70ns to t2 = 90ns.
- Dynamic resistance calculated from I_{TLP} = 4A to I_{TLP} = 16A

Typical Characteristics

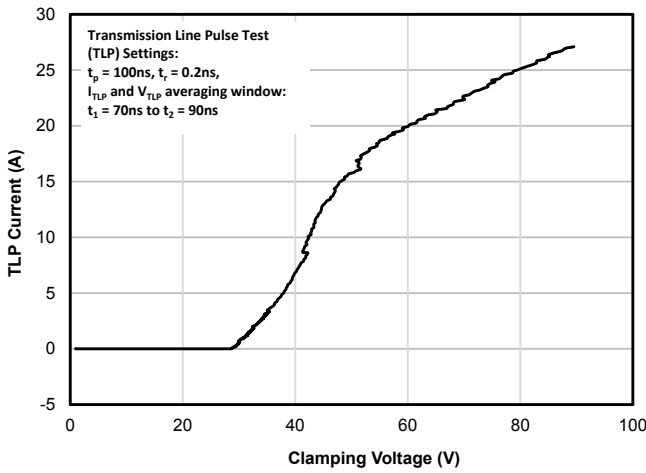
ESD Clamping (8kV Contact per IEC 61000-4-2)



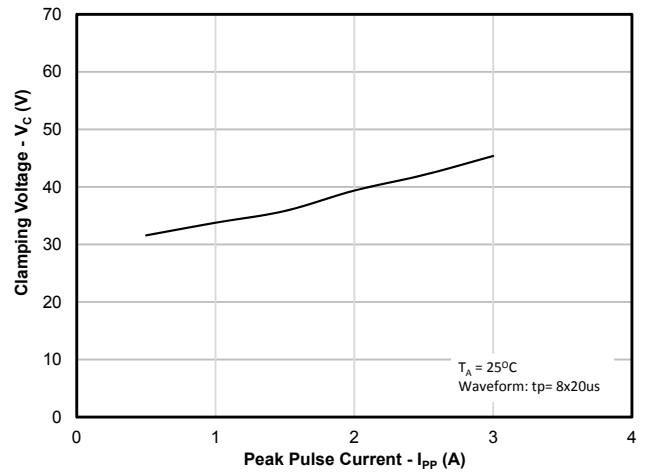
ESD Clamping (-8kV Contact per IEC 61000-4-2)



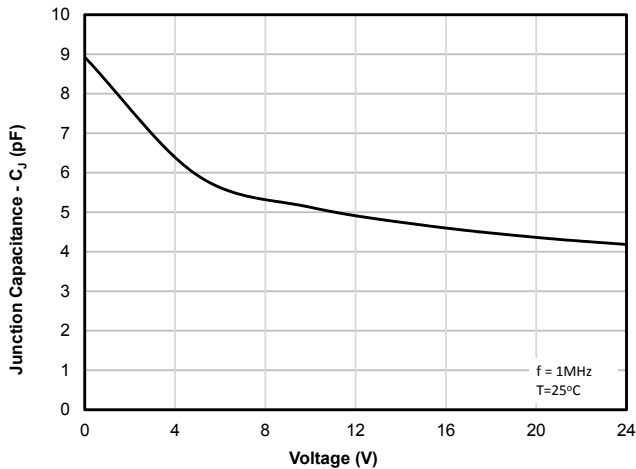
TLP Characteristic (Positive Pulse)



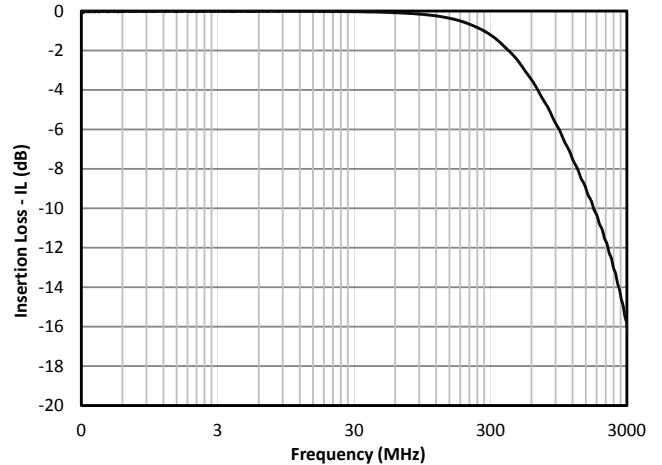
Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Reverse Voltage



Insertion Loss-S21



Application Information

Assembly Guidelines

The small size of this device means that some care must be taken during the mounting process to insure reliable solder joints. The figure at the right details Semtech's recommended mounting pattern. Recommended assembly guidelines are shown in Table 1. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. Exact manufacturing parameters will require some experimentation to get the desired solder application. Semtech's recommended mounting pattern is based on the following design guidelines:

Land Pattern

The recommended land pattern follows IPC standards and is designed for maximum solder coverage. Detailed dimensions are shown elsewhere in this document.

Solder Stencil

Stencil design is one of the key factors which will determine the volume of solder paste which is deposited onto the land pad. The area ratio of the stencil aperture will determine how well the stencil will print. The area ratio takes into account the aperture shape, aperture size, and stencil thickness. An area ratio of 0.70 – 0.75 is preferred for the subject package. The area ratio of a rectangular aperture is given as:

$$\text{Area Ratio} = (L * W) / (2 * (L + W) * T)$$

Where:

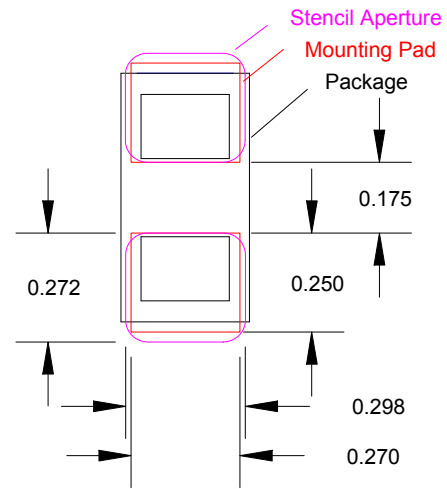
L = Aperture Length

W = Aperture Width

T = Stencil Thickness

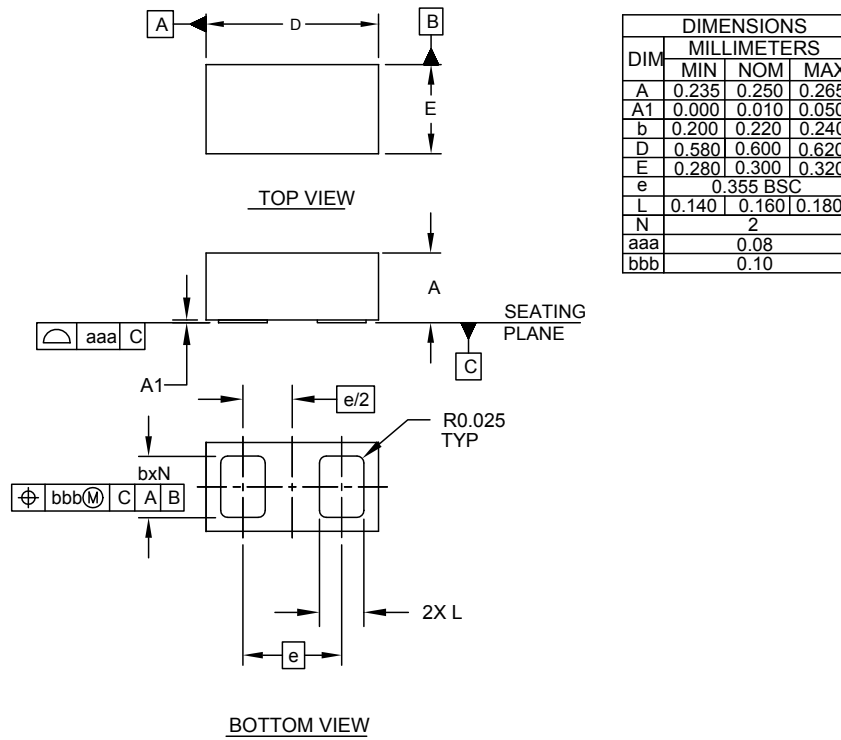
Semtech recommends a stencil thickness of 0.100mm for this device. The stencil should be laser cut with electropolished finish. The stencil should have a positive taper of approximately 5 degrees. Electropolishing and tapering the walls results in reduced surface friction and better paste release. For small pitch components, Semtech recommends a square aperture with rounded corners for consistent solder release. Due to the small aperture size, a solder paste with Type 4 or smaller particles are recommended.

Recommended Mounting Pattern



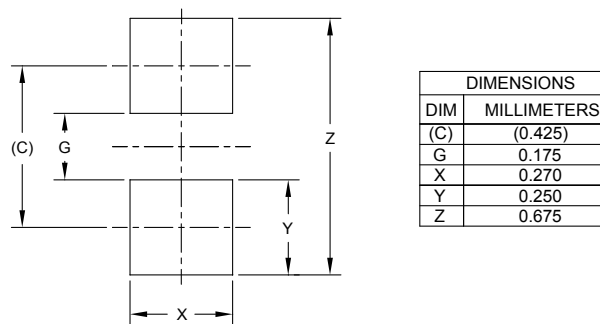
Assembly Parameter	Recommendation
Solder Stencil Design	Laser cut, Electro-polished
Aperture shape	Rectangular with rounded corners
Solder Stencil Thickness	0.100 mm (0.004")
Solder Paste Type	Type 4 size sphere or smaller
Solder Reflow Profile	Per JEDEC J-STD-020
PCB Solder Pad Design	Non-Solder mask defined
PCB Pad Finish	OSP OR NiAu

Outline Drawing - SLP0603P2X3F



NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Land Pattern - SLP0603P2X3F

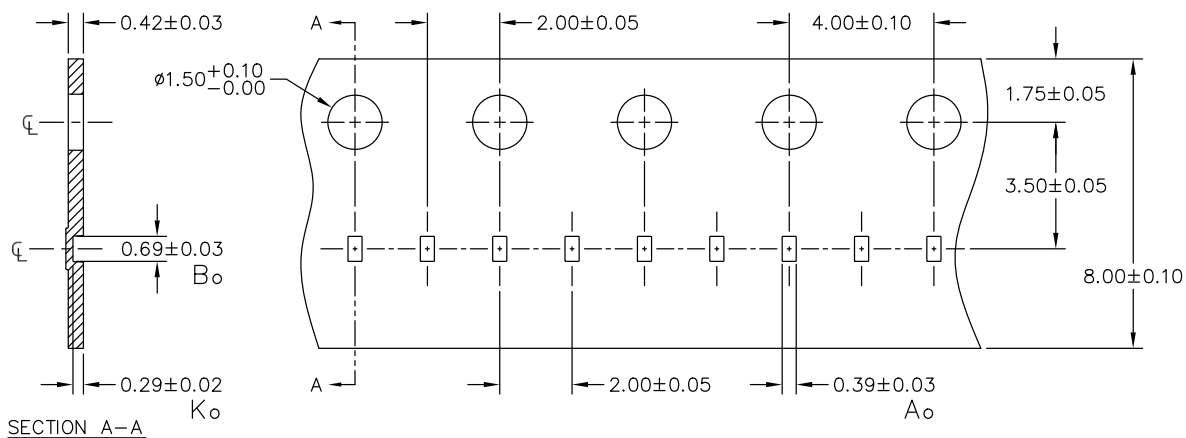


NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

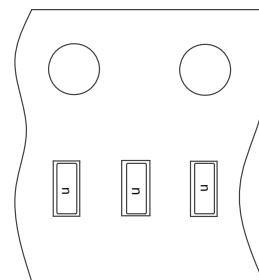
Marking Code

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Tape and Reel Specification



NOTES: ALL DIMENSIONS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.



Ordering Information

Part Number	Qty per Reel	Reel Size
μClamp2411ZATFT	15,000	7"
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