SEIKO EPSON CORPORATION

Clock OSC SG3225EEN

Product name SG3225EEN 156.250000MHz CJGA
Product Number / Ordering code X1G0052210008xx

Please refer to the 8.Packing information about xx (last 2 digits)

Output waveform LV-PECL
Pb free / Complies with EU RoHS directive
Reference weight Typ. 26 mg

1.Absolute maximum ratings

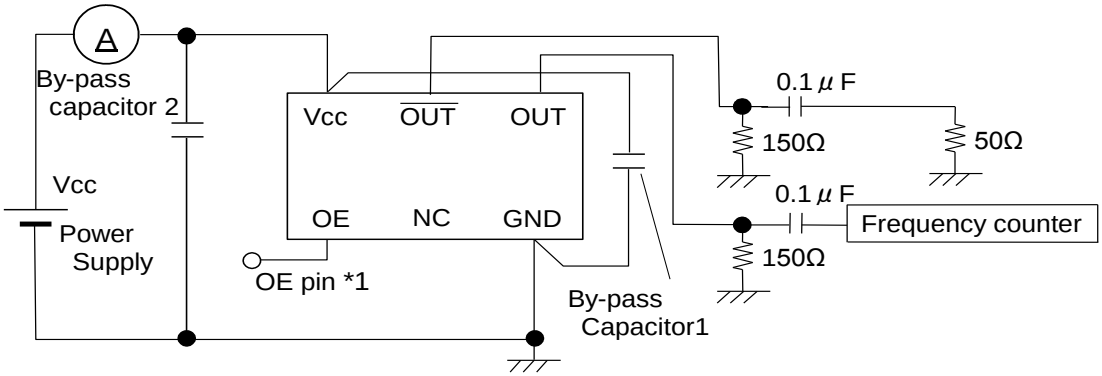
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions / Remarks
Maximum supply voltage	Vcc-GND	-0.5	-	+4	V	0
Storage temperature	T_stg	-55	-	+125	ºC	Storage as single product
Input voltage	Vin	-0.5	-	Vcc+0.5	V	ST or OE Terminal

2.Specifications(characteristics)

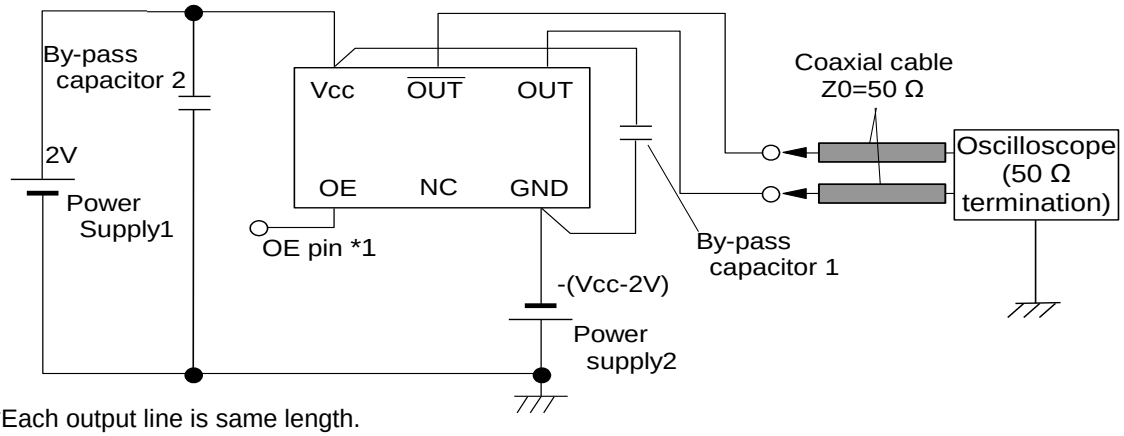
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions / Remarks
Output frequency	f0		156.2500		MHz	
Supply voltage	Vcc	3.135	3.3	3.465	V	-
Operating temperature	T_use	-40	-	+85	ºC	-
Frequency tolerance	f_tol	-50	-	+50	$x10^{-6}$	-
Current consumption	Icc	-	-	60	mA	OE = Vcc , L_ECL = 50 ohm
Stand-by current	I_std	-	-	-	mA	-
Disable current	I_dis	-	-	25	mA	OE=GND
Symmetry	SYM	45	50	55	%	At output crossing point
Output voltage(LV-PECL)	V_{OH}	Vcc-1.1	-	-	V	-
	V_{OL}	-	-	Vcc-1.5	V	-
Output load condition(ECL)	L_ECL	-	50	-	Ω	Terminated to Vcc - 2.0V
Input voltage	V_{IH}	70% Vcc	-	-		OE Terminal
	V_{IL}	-	-	30% Vcc		OE Terminal
Rise time	t_r	-	-	0.35	ps	At 20% to 80% output swing
Fall time	tf	-	-	0.35	ps	At 20% to 80% output swing
Start-up time	t_str	-	-	10	ms	-
Phase jitter	t_{PJ}	-	45	70	fs	Off set Frequency: 12kHz to 20MHz
Phase noise	L(f)	-	-47.3	-	dBc/Hz	Off set 1Hz
		-	-81.9	-	dBc/Hz	Off set 10Hz
		-	-110.7	-	dBc/Hz	Off set 100Hz
		-	-136.4	-	dBc/Hz	Off set 1kHz
		-	-153.1	-	dBc/Hz	Off set 10kHz
		-	-158.6	-	dBc/Hz	Off set 100kHz
		-	-162.6	-	dBc/Hz	Off set 1MHz
Frequency aging	f_age	-	-	-	$x10^{-6}$/Year	Included in Frequency tolerance 10 years

3.Test circuit

1) To observe frequency and current



2) To observe outputwave



*Each output line is same length.

3) Measurement condition

A) Oscilloscope

- •Bandwidth should be 5 times higher than DUT's output frequency (2.5 GHz).
- •Probe ground should be placed closely from test point and lead length should be as short as possible.

B) By-pass capacitor 1 (approx. 0.1 μF) places closely between Vcc and GND.

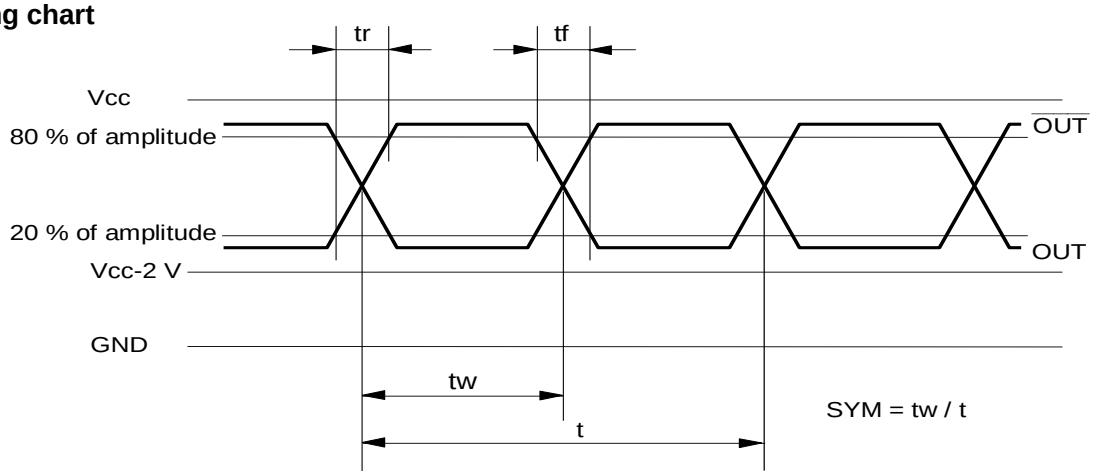
C) By-pass capacitor 2 (approx. 10 μF) places closely between power supply terminals on the board.

D) Use the current meter whose internal impedance value is small.

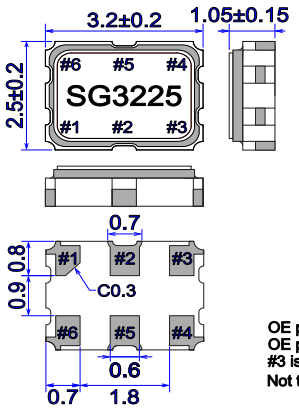
E) Power supply

- • Start up time (0 Vg90 %Vcc) of power source should be more than 150 μs
- • Impedance of power supply should be as low as possible.

4.Timing chart



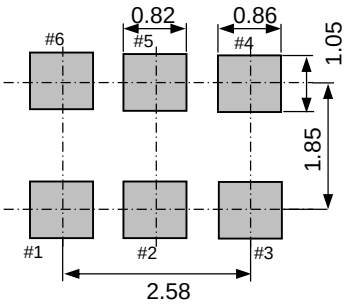
5.External dimensions (Unit: mm)



Pin	Connection
#1	OE
#2	N.C.(Open or Vcc))
#3	GND
#4	OUT(Positive)
#5	OUT(Negative)
#6	Vcc

OE pin = HIGH : Specified frequency output
OE pin = LOW : Output is high impedance
#3 is connected to the cover.
Not to scale.

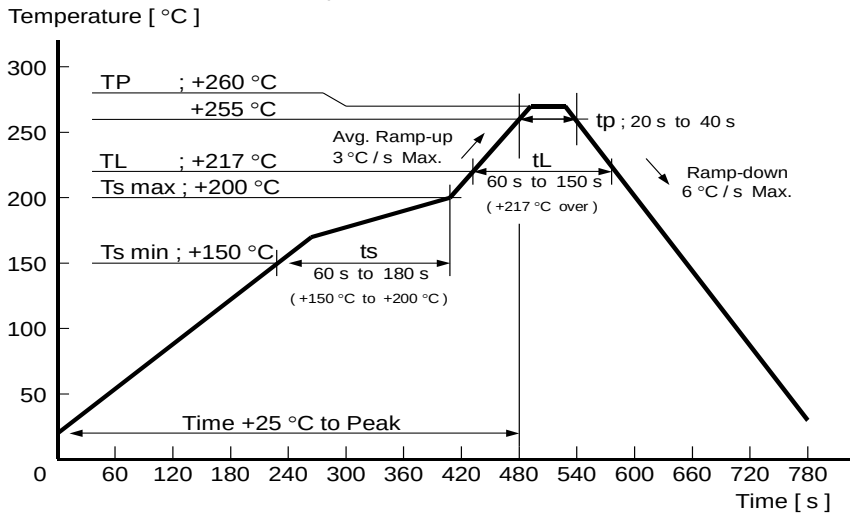
6.Footprint(Recommended) (Unit: mm)



In order to achieve optimum jitter performance, the 0.1 µF an7d 10 µF capacitor is required. These capacitors should be placed as close to Vcc (#6 pin) as possible. It is also recommended that the capacitors are placed on the device side of the PCB.

7.Reflow profile

Reflow condition (Follow of JEDEC STD-020D.01)



8.Packing information

[1]Product number last 2 digits code(xx) description The recommended code is "00"

X1G0052210008xx

Code	Condition	Code	Condition
01	Any Q'ty vinyl bag(Tape cut)	13	500pcs / Reel
11	Any Q'ty / Reel	14	1000pcs / Reel
12	250pcs / Reel	00	2000pcs / Reel

[2] Taping specification

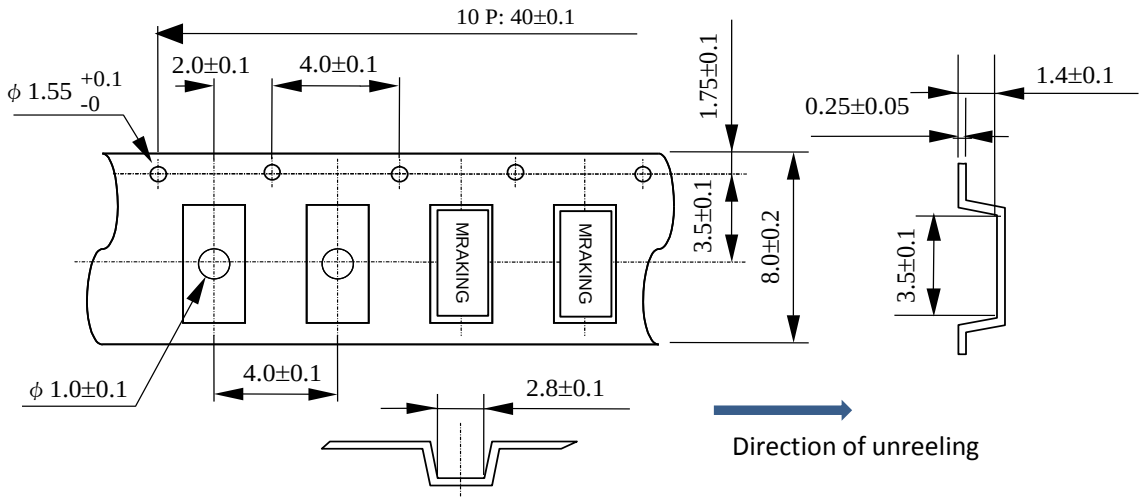
Subject to EIA-481 & IEC-60286

(1) Tape dimensions

Material of the Carrier Tape : PS

Material of the Top Tape : PET+PE

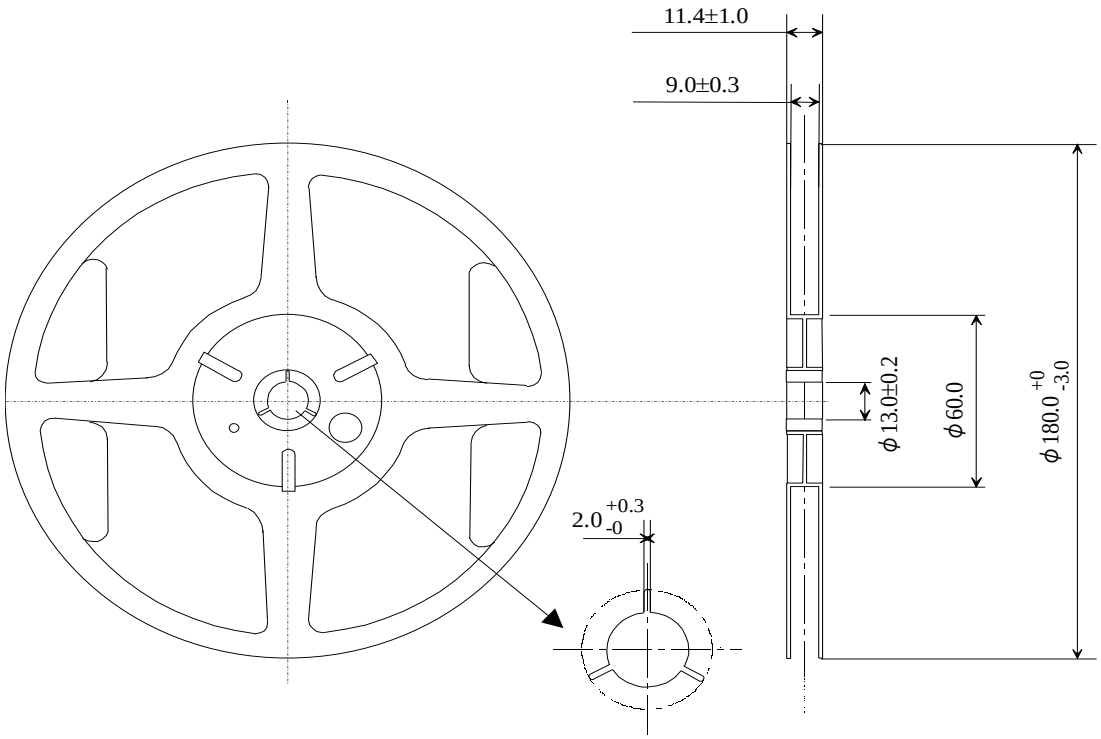
Unit: mm



(2) Reel dimensions

Center material : PS

Material of the Reel : PS



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