

Middle Power LED Series  
2835 0.2W 3V Ra90

LM281BA+



Designed for better lm/\$ (Ambient, Linear)

#### Features & Benefits

- 0.2W Class mid power LED
- Standard form factor for design flexibility (2.8 × 3.5 × 0.65mm)



## Table of Contents

1.	Characteristics	-----	3
2.	Product Code Information	-----	6
3.	Typical Characteristics Graphs	-----	16
4.	Outline Drawing & Dimension	-----	19
5.	Reliability Test Items & Conditions	-----	20
6.	Soldering Conditions	-----	21
7.	Tape & Reel	-----	22
8.	Label Structure	-----	24
9.	Packing Structure	-----	25
10.	Precautions in Handling & Use	-----	28



## 1. Characteristics

### a) Absolute Maximum Rating

Item	Symbol	Rating	Unit	Condition
Ambient / Operating Temperature	T <sub>a</sub>	-40 ~ +80	°C	-
Storage Temperature	T <sub>stg</sub>	-40 ~ +80	°C	-
LED Junction Temperature	T <sub>j</sub>	115	°C	-
Forward Current	I <sub>F</sub>	70	mA	-
Peak Pulsed Forward Current	I <sub>FP</sub>	140	mA	Duty 1/10, pulse width 10ms
Assembly Process Temperature	-	260 <10	°C s	-
ESD (HBM)	-	2	kV	-

**Note:**

Proper current derating must be observed to maintain junction temperature below the maximum at all time.



**b) Electro-optical Characteristics (If = 60 mA, Ts = 25 °C)**

Item	Unit	Rank	Bin	Min.	Typ.	Max.
Forward Voltage (VF)	V	WA	A2	2.9	-	3.0
			A3	3.0	-	3.1
			A4	3.1	-	3.2
			A5	3.2	-	3.3
Color Rendering Index (Ra)	-	5		90	-	-
Special CRI (R9)	-	-		50		
Thermal Resistance (junction to solder point)	°C/W			-	25	-
Beam Angle	°			-	120	-

**Note:**

Samsung maintains measurement tolerance of: forward voltage = ±0.1 V, CRI = ±3, R9 = ±6.5



**b) Electro-optical Characteristics (If = 60 mA, Ts = 25 °C)**

Item	CRI (Ra) Min.	Nominal CCT (K)	Bin	60mA	
				Min.	Max.
Luminous Flux (Φv)	90	2700	S2	18.0	19.5
			S3	19.5	21.0
		3000	S2	18.5	20.0
			S3	20.0	21.5
Luminous Flux (Φv)	90	3500	S2	19.0	21.5
			S3	21.5	23.0
		4000	S2	19.5	21.0
			S3	21.0	22.5
Luminous Flux (Φv)	90	5000	S2	20.0	21.5
			S3	21.5	23.0
		5700	S2	20.0	21.5
			S3	21.5	23.0
Luminous Flux (Φv)	90	6500	S2	19.5	21.0
			S3	21.0	22.5

**Note:**

Samsung maintains measurement tolerance of: forward voltage = ±0.1V, luminous flux = ±5 %, CRI = ±3



## 2. Product Code Information

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	
S	P	M	W	H	2	2	2	8	6	D	7	W	A	R	0	S	3	
Digit	PKG Information					Code	Specification											
1 2 3	Samsung Package Middle Power					SPM	Middle power											
4 5	Color					WH	White											
6	Product Version					2	2 <sup>nd</sup> version											
7 8 9	Form Factor					228	2.8 x 3.5 x 0.65 mm; 2 pads; 1chip;											
10	Sorting Current (mA)					6	60 mA											
11	Chromaticity Coordinates					D	ANSI Standard											
12	CRI					7	Min. 90											
13 14	Forward Voltage (V)					WA	2.9~3.4	Bin code	A2	2.9 ~ 3.0								
									A3	3.0 ~ 3.1								
									A4	3.1 ~ 3.2								
									A5	3.2 ~ 3.3								
15 16	CCT (K)					W☆	2700	Bin Code:	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG									
						V☆	3000		V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG									
						U☆	3500		U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG									
						T☆	4000		T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG									
						R☆	5000		R1, R2, R3, R4, R5, R6, R7, R8, R9, RA, RB, RC, RD, RE, RF, RG									
						Q☆	5700		Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, QA, QB, QC, QD, QE, QF, QG									
						P☆	6500		P1, P2, P3, P4, P5, P6, P7, P8, P9, PA, PB, PC, PD, PE, PF, PG									
						☆ : "0" (Whole bin) "M" (Quarter bin) or "K" (kitting bin)												
17 18	Luminous Flux					S2	Bin Code	S2										
						S3		S3										



**a) Luminous Flux Bins ( $I_F = 60 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )**

CRI ( $R_a$ ) Min.	Nominal CCT (K)	Product Code	Flux Bin	Flux Range ( $\phi_v, \text{Im}$ )
2700		SPMWH22286D7WAW☆S2	S2	18.0 ~ 19.5
		SPMWH22286D7WAW☆S3	S3	19.5 ~ 21.0
3000		SPMWH22286D7WAV☆S2	S2	18.5 ~ 20.0
		SPMWH22286D7WAV☆S3	S3	20.0 ~ 21.5
3500		SPMWH22286D7WAU☆S2	S2	21.5 ~ 23.0
		SPMWH22286D7WAU☆S3	S3	23.0 ~ 24.5
90		SPMWH22286D7WAT☆S2	S2	19.5 ~ 21.0
		SPMWH22286D7WAT☆S3	S3	21.0 ~ 22.5
4000		SPMWH22286D7WAR☆S2	S2	20.0 ~ 21.5
		SPMWH22286D7WAR☆S3	S3	21.5 ~ 23.0
5000		SPMWH22286D7WAQ☆S2	S2	20.0 ~ 21.5
		SPMWH22286D7WAQ☆S3	S3	21.5 ~ 23.0
5700		SPMWH22286D7WAP☆S2	S2	19.5 ~ 21.0
		SPMWH22286D7WAP☆S3	S3	21.0 ~ 22.5
6500		SPMWH22286D7WAP☆S2	S2	19.5 ~ 21.0
		SPMWH22286D7WAP☆S3	S3	21.0 ~ 22.5

**Note:**

"☆" can be "0" (Whole bin), "M" (Quarter bin) or "K" (Kitting bin) of the color binning



## b) Kitting rule

### 1) Kitting bin Concept

1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, Im).
2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (A2+A2), (A3+A3), (A4+A4) or (A5+A5).
3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)

### [Kitting example]

D	E	F	G
9	A	B	C
5	6	7	8
1	2	3	4

### [Binning Information]

	Bin #1	Bin #2
VF	A2	A2
	A3	A3
	A4	A4
	A5	A5
CIE	1, 2, 5 bin	C, F, G bin
	6, 7, A, B bin	6, 7, A, B bin
	3, 4, 8 bin	9, D, E bin



c) Color Bins (I<sub>f</sub> = 60 mA, T<sub>s</sub> = 25 °C)

CRI (R <sub>a</sub> ) Min.	Nominal CCT (K)	Product Code	Color Rank	Chromaticity Bins
		SPMWH22286D7WAW0S☆	W0 (Whole bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG
2700		SPMWH22286D7WAWMS☆	WM (Quarter bin)	W6, W7, WA, WB
		SPMWH22286D7WAWKS☆	WK (Kitting bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG
3000		SPMWH22286D7WAV0S☆	V0 (Whole bin)	V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG
		SPMWH22286D7WAVMS☆	VM (Quarter bin)	V6, V7, VA, VB
		SPMWH22286D7WAVKS☆	VK (Kitting bin)	V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG
3500		SPMWH22286D7WAU0S☆	U0 (Whole bin)	U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG
		SPMWH22286D7WAUMS☆	UM (Quarter bin)	U6, U7, UA, UB
		SPMWH22286D7WAUKS☆	UK (Kitting bin)	U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG
90	4000	SPMWH22286D7WAT0S☆	T0 (Whole bin)	T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG
		SPMWH22286D7WATMS☆	TM (Quarter bin)	T6, T7, TA, TB
		SPMWH22286D7WATKS☆	TK (Kitting bin)	T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG
		SPMWH22286D7WAR0S☆	R0 (Whole bin)	R1, R2, R3, R4, R5, R6, R7, R8, R9 RA, RB, RC, RD, RE, RF, RG
5000		SPMWH22286D7WARMS☆	RM (Quarter bin)	R6, R7, RA, RB
		SPMWH22286D7WARKS☆	RK (Kitting bin)	R1, R2, R3, R4, R5, R6, R7, R8, R9 RA, RB, RC, RD, RE, RF, RG
		SPMWH22286D7WAQ0S☆	Q0 (Whole bin)	Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9 QA, QB, QC, QD, QE, QF, QG
5700		SPMWH22286D7WAQMS☆	QM (Quarter bin)	Q6, Q7, QA, QB
		SPMWH22286D7WAQKS☆	QK (Kitting bin)	Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9 QA, QB, QC, QD, QE, QF, QG
		SPMWH22286D7WAP0S☆	P0 (Whole bin)	P1, P2, P3, P4, P5, P6, P7, P8, P9 PA, PB, PC, PD, PE, PF, PG
6500		SPMWH22286D7WAPMS☆	PM (Quarter bin)	P6, P7, PA, PB
		SPMWH22286D7WAPKS☆	PK (Kitting bin)	P1, P2, P3, P4, P5, P6, P7, P8, P9 PA, PB, PC, PD, PE, PF, PG

**Note:** "☆" can be "S2" or "S3" of luminous flux bin

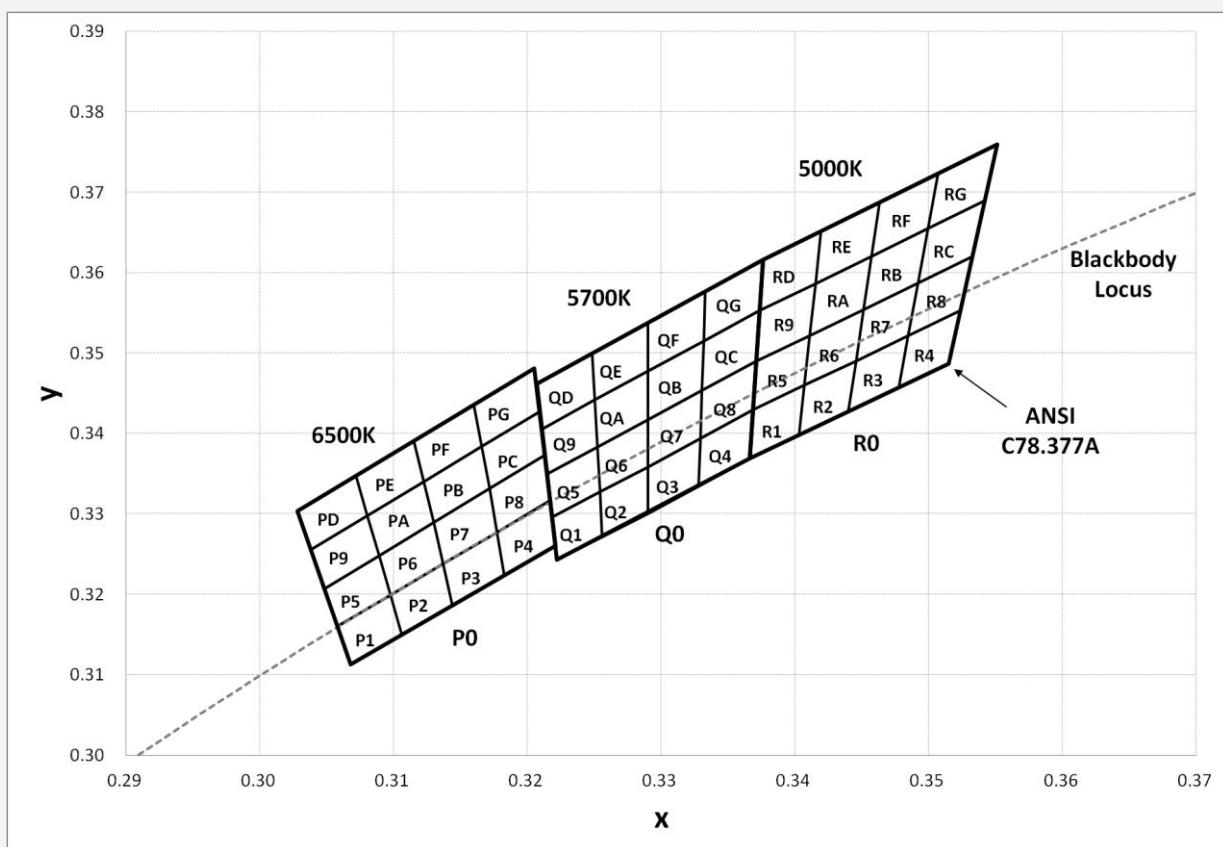
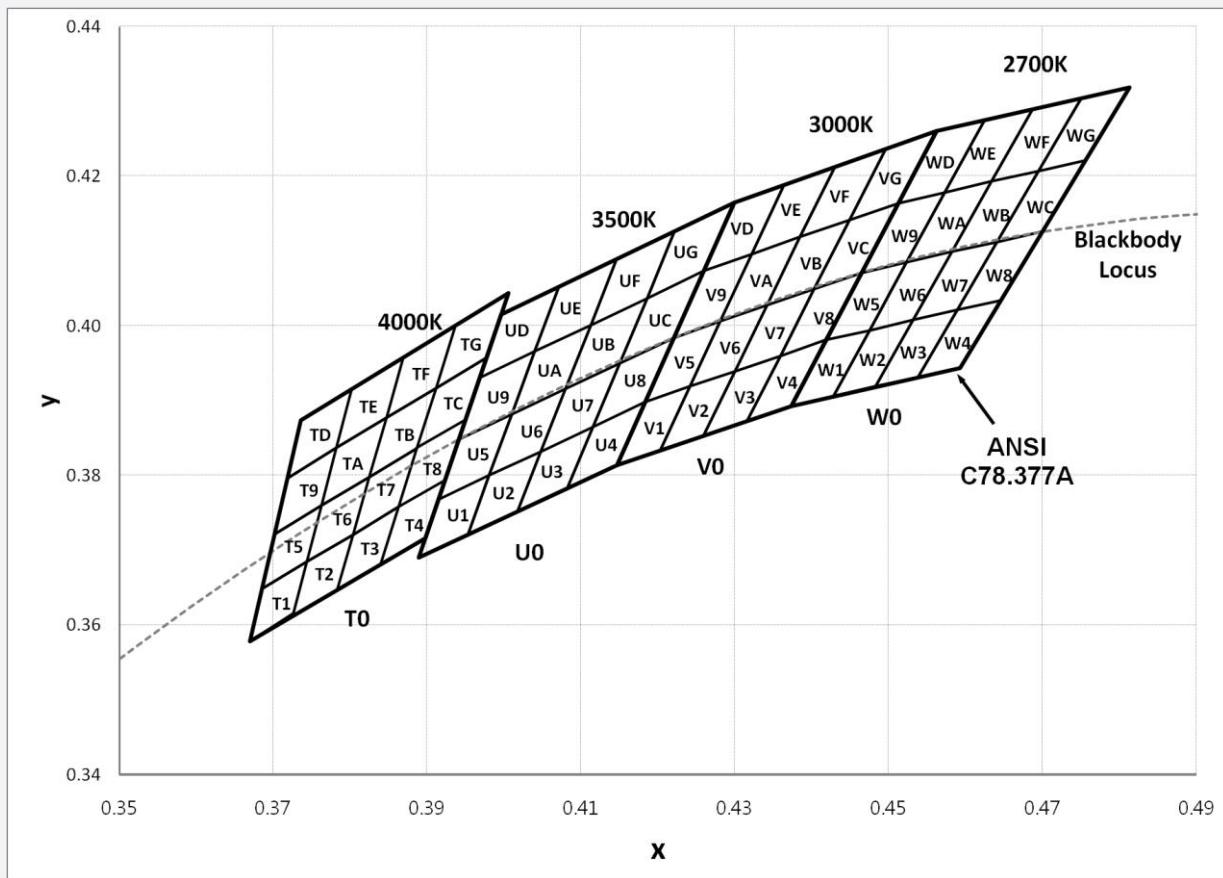


**d) Voltage Bins ( $I_F = 60 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )**

CRI ( $R_a$ ) Min.	Nominal CCT (K)	Product Code	Voltage Rank	Voltage Bin	Voltage Range (V)
-	-	-	A2		2.9 ~ 3.0
-	-	-	A3		3.0 ~ 3.1
-	-	WA			
-	-	-	A4		3.1 ~ 3.2
-	-	-	A5		3.2 ~ 3.3



e) Chromaticity Region & Coordinates ( $I_F = 60 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )



### e) Chromaticity Region & Coordinates

Region	CIE x	CIE y	Region	CIE x	CIE y
W rank (2700 K)					
W1	0.4373	0.3893	W9	0.4465	0.4071
	0.4418	0.3981		0.4513	0.4164
	0.4475	0.3994		0.4573	0.4178
	0.4428	0.3906		0.4523	0.4085
W2	0.4428	0.3906	WA	0.4523	0.4085
	0.4475	0.3994		0.4573	0.4178
	0.4532	0.4008		0.4634	0.4193
	0.4483	0.3919		0.4582	0.4099
W3	0.4483	0.3919	WB	0.4582	0.4099
	0.4532	0.4008		0.4634	0.4193
	0.4589	0.4021		0.4695	0.4207
	0.4538	0.3931		0.4641	0.4112
W4	0.4538	0.3931	WC	0.4641	0.4112
	0.4589	0.4021		0.4695	0.4207
	0.4646	0.4034		0.4756	0.4221
	0.4593	0.3944		0.4700	0.4126
W5	0.4418	0.3981	WD	0.4513	0.4164
	0.4465	0.4071		0.4562	0.4260
	0.4523	0.4085		0.4624	0.4274
	0.4475	0.3994		0.4573	0.4178
W6	0.4475	0.3994	WE	0.4573	0.4178
	0.4523	0.4085		0.4624	0.4274
	0.4582	0.4099		0.4687	0.4289
	0.4532	0.4008		0.4634	0.4193
W7	0.4532	0.4008	WF	0.4634	0.4193
	0.4582	0.4099		0.4687	0.4289
	0.4641	0.4112		0.4750	0.4304
	0.4589	0.4021		0.4695	0.4207
W8	0.4589	0.4021	WG	0.4695	0.4207
	0.4641	0.4112		0.4750	0.4304
	0.4700	0.4126		0.4813	0.4319
	0.4646	0.4034		0.4756	0.4221

Region	CIE x	CIE y	Region	CIE x	CIE y
V rank (3000 K)					
V1	0.4147	0.3814	V9	0.4221	0.3984
	0.4183	0.3898		0.4259	0.4073
	0.4242	0.3919		0.4322	0.4096
	0.4203	0.3833		0.4281	0.4006
V2	0.4203	0.3833	VA	0.4281	0.4006
	0.4242	0.3919		0.4322	0.4096
	0.4300	0.3939		0.4385	0.4119
	0.4259	0.3853		0.4342	0.4028
V3	0.4259	0.3853	VB	0.4342	0.4028
	0.4300	0.3939		0.4385	0.4119
	0.4359	0.3960		0.4449	0.4141
	0.4316	0.3873		0.4403	0.4049
V4	0.4316	0.3873	VC	0.4403	0.4049
	0.4359	0.3960		0.4449	0.4141
	0.4418	0.3981		0.4513	0.4164
	0.4373	0.3893		0.4465	0.4071
V5	0.4183	0.3898	VD	0.4259	0.4073
	0.4221	0.3984		0.4299	0.4165
	0.4281	0.4006		0.4364	0.4188
	0.4242	0.3919		0.4322	0.4096
V6	0.4242	0.3919	VE	0.4322	0.4096
	0.4281	0.4006		0.4364	0.4188
	0.4342	0.4028		0.4430	0.4212
	0.4300	0.3939		0.4385	0.4119
V7	0.4300	0.3939	VF	0.4385	0.4119
	0.4342	0.4028		0.4430	0.4212
	0.4403	0.4049		0.4496	0.4236
	0.4359	0.3960		0.4449	0.4141
V8	0.4359	0.3960	VG	0.4449	0.4141
	0.4403	0.4049		0.4496	0.4236
	0.4465	0.4071		0.4562	0.4260
	0.4418	0.3981		0.4513	0.4164



### e) Chromaticity Region & Coordinates

Region	CIE x	CIE y	Region	CIE x	CIE y
<b>U rank (3500 K)</b>					
U1	0.3889	0.3690	U9	0.3941	0.3848
	0.3915	0.3768		0.3968	0.3930
	0.3981	0.3800		0.4040	0.3966
	0.3953	0.3720		0.4010	0.3882
U2	0.3953	0.3720	UA	0.4010	0.3882
	0.3981	0.3800		0.4040	0.3966
	0.4048	0.3832		0.4113	0.4001
	0.4017	0.3751		0.4080	0.3916
U3	0.4017	0.3751	UB	0.4080	0.3916
	0.4048	0.3832		0.4113	0.4001
	0.4116	0.3865		0.4186	0.4037
	0.4082	0.3782		0.4150	0.3950
U4	0.4082	0.3782	UC	0.4150	0.3950
	0.4116	0.3865		0.4186	0.4037
	0.4183	0.3898		0.4259	0.4073
	0.4147	0.3814		0.4221	0.3984
U5	0.3915	0.3768	UD	0.3968	0.3930
	0.3941	0.3848		0.3996	0.4015
	0.4010	0.3882		0.4071	0.4052
	0.3981	0.3800		0.4040	0.3966
U6	0.3981	0.3800	UE	0.4040	0.3966
	0.4010	0.3882		0.4071	0.4052
	0.4080	0.3916		0.4146	0.4089
	0.4048	0.3832		0.4113	0.4001
U7	0.4048	0.3832	UF	0.4113	0.4001
	0.4080	0.3916		0.4146	0.4089
	0.4150	0.3950		0.4222	0.4127
	0.4116	0.3865		0.4186	0.4037
U8	0.4116	0.3865	UG	0.4186	0.4037
	0.4150	0.3950		0.4222	0.4127
	0.4221	0.3984		0.4299	0.4165
	0.4183	0.3898		0.4259	0.4073

Region	CIE x	CIE y	Region	CIE x	CIE y
<b>T rank (4000 K)</b>					
T1	0.3670	0.3578	T9	0.3702	0.3722
	0.3726	0.3612		0.3763	0.3760
	0.3744	0.3685		0.3782	0.3837
	0.3686	0.3649		0.3719	0.3797
	0.3726	0.3612		0.3763	0.3760
T2	0.3783	0.3646	TA	0.3825	0.3798
	0.3804	0.3721		0.3847	0.3877
	0.3744	0.3685		0.3782	0.3837
	0.3783	0.3646		0.3825	0.3798
T3	0.3840	0.3681	TB	0.3887	0.3836
	0.3863	0.3758		0.3912	0.3917
	0.3804	0.3721		0.3847	0.3877
	0.3840	0.3681		0.3887	0.3837
T4	0.3898	0.3716	TC	0.3950	0.3875
	0.3924	0.3794		0.3978	0.3958
	0.3863	0.3758		0.3912	0.3917
	0.3686	0.3649		0.3719	0.3797
T5	0.3744	0.3685	TD	0.3782	0.3837
	0.3763	0.3760		0.3802	0.3916
	0.3702	0.3722		0.3736	0.3874
	0.3744	0.3685		0.3782	0.3837
T6	0.3804	0.3721	TE	0.3847	0.3877
	0.3825	0.3798		0.3869	0.3958
	0.3763	0.3760		0.3802	0.3916
	0.3804	0.3721		0.3847	0.3877
T7	0.3863	0.3758	TF	0.3912	0.3917
	0.3887	0.3836		0.3937	0.4001
	0.3825	0.3798		0.3869	0.3958
	0.3863	0.3758		0.3912	0.3917
T8	0.3924	0.3794	TG	0.3978	0.3958
	0.3950	0.3875		0.4006	0.4044
	0.3887	0.3836		0.3937	0.4001



### e) Chromaticity Region & Coordinates

Region	CIE x	CIE y	Region	CIE x	CIE y
<b>R rank (5000 K)</b>					
R1	0.3366	0.3369	R9	0.3374	0.3554
	0.3369	0.3431		0.3371	0.3493
	0.3407	0.3460		0.3411	0.3522
	0.3403	0.3398		0.3415	0.3587
R2	0.3403	0.3398	RA	0.3415	0.3587
	0.3407	0.3460		0.3411	0.3522
	0.3446	0.3491		0.3451	0.3554
	0.3440	0.3427		0.3457	0.3621
R3	0.3446	0.3491	RB	0.3451	0.3554
	0.3440	0.3427		0.3457	0.3621
	0.3477	0.3458		0.3500	0.3655
	0.3485	0.3522		0.3492	0.3587
R4	0.3485	0.3522	RC	0.3492	0.3587
	0.3477	0.3458		0.3500	0.3655
	0.3514	0.3487		0.3542	0.3690
	0.3524	0.3554		0.3533	0.3620
R5	0.3371	0.3493	RD	0.3376	0.3616
	0.3369	0.3431		0.3374	0.3554
	0.3407	0.3460		0.3415	0.3587
	0.3411	0.3522		0.3420	0.3652
R6	0.3407	0.3460	RE	0.3415	0.3587
	0.3411	0.3522		0.3420	0.3652
	0.3451	0.3554		0.3463	0.3687
	0.3446	0.3491		0.3457	0.3621
R7	0.3446	0.3491	RF	0.3457	0.3621
	0.3451	0.3554		0.3463	0.3687
	0.3492	0.3587		0.3507	0.3724
	0.3485	0.3522		0.3500	0.3655
R8	0.3485	0.3522	RG	0.3500	0.3655
	0.3492	0.3587		0.3507	0.3724
	0.3533	0.3620		0.3551	0.3760
	0.3524	0.3554		0.3542	0.3690

Region	CIE x	CIE y	Region	CIE x	CIE y
<b>Q rank (5700 K)</b>					
Q1	0.3218	0.3298	Q9	0.3211	0.3407
	0.3222	0.3243		0.3215	0.3353
	0.3258	0.3275		0.3254	0.3388
	0.3256	0.3331		0.3252	0.3444
Q2	0.3256	0.3331	QA	0.3252	0.3444
	0.3258	0.3275		0.3254	0.3388
	0.3294	0.3306		0.3293	0.3423
	0.3294	0.3364		0.3293	0.3481
Q3	0.3294	0.3364	QB	0.3293	0.3481
	0.3294	0.3306		0.3293	0.3423
	0.3330	0.3338		0.3332	0.3458
	0.3331	0.3398		0.3333	0.3518
Q4	0.3331	0.3398	QC	0.3333	0.3518
	0.3330	0.3338		0.3332	0.3458
	0.3366	0.3369		0.3371	0.3493
	0.3369	0.3431		0.3374	0.3554
Q5	0.3215	0.3353	QD	0.3207	0.3462
	0.3218	0.3298		0.3211	0.3407
	0.3256	0.3331		0.3252	0.3444
	0.3254	0.3388		0.3250	0.3501
Q6	0.3254	0.3388	QE	0.3250	0.3501
	0.3256	0.3331		0.3252	0.3444
	0.3294	0.3364		0.3293	0.3481
	0.3293	0.3423		0.3292	0.3539
Q7	0.3293	0.3423	QF	0.3292	0.3539
	0.3294	0.3364		0.3293	0.3481
	0.3331	0.3398		0.3333	0.3518
	0.3332	0.3458		0.3334	0.3578
Q8	0.3332	0.3458	QG	0.3334	0.3578
	0.3331	0.3398		0.3333	0.3518
	0.3369	0.3431		0.3374	0.3554
	0.3371	0.3493		0.3376	0.3616



e) Chromaticity Region & Coordinates

Region	CIE x	CIE y	Region	CIE x	CIE y
P rank (6500 K)					
P1	0.3068	0.3113	P9	0.3048	0.3207
	0.3106	0.3150		0.3089	0.3249
	0.3098	0.3199		0.3080	0.3298
	0.3058	0.3160		0.3038	0.3256
P2	0.3106	0.3150	PA	0.3089	0.3249
	0.3144	0.3186		0.313	0.3290
	0.3137	0.3238		0.3123	0.3341
	0.3098	0.3199		0.3080	0.3298
P3	0.3144	0.3186	PB	0.3130	0.3290
	0.3183	0.3224		0.3172	0.3332
	0.3177	0.3278		0.3166	0.3384
	0.3137	0.3238		0.3123	0.3341
P4	0.3183	0.3224	PC	0.3172	0.3332
	0.3221	0.3261		0.3214	0.3373
	0.3218	0.3317		0.3210	0.3427
	0.3177	0.3278		0.3166	0.3384
P5	0.3058	0.3160	PD	0.3038	0.3256
	0.3098	0.3199		0.3080	0.3298
	0.3089	0.3249		0.3072	0.3348
	0.3048	0.3207		0.3028	0.3304
P6	0.3098	0.3199	PE	0.3080	0.3298
	0.3137	0.3238		0.3123	0.3341
	0.3130	0.3290		0.3115	0.3391
	0.3089	0.3249		0.3072	0.3348
P7	0.3137	0.3238	PF	0.3123	0.3341
	0.3177	0.3278		0.3166	0.3384
	0.3172	0.3332		0.3160	0.3436
	0.313	0.3290		0.3115	0.3391
P8	0.3177	0.3278	PG	0.3166	0.3384
	0.3218	0.3317		0.3210	0.3427
	0.3214	0.3373		0.3206	0.3481
	0.3172	0.3332		0.3160	0.3436

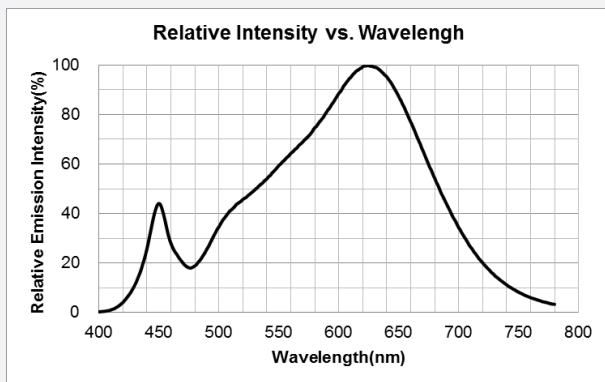
**Note:** Samsung maintains measurement tolerance of: Cx, Cy = ±0.005



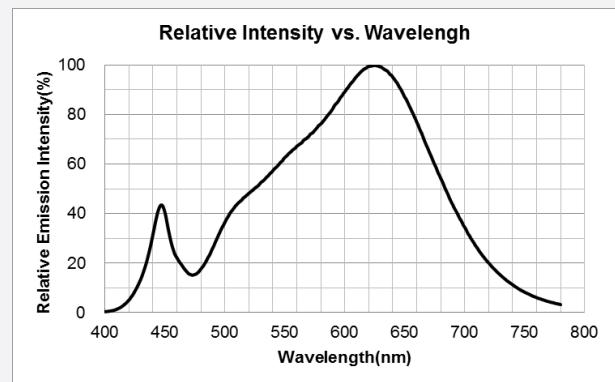
### 3. Typical Characteristics Graphs

#### a) Spectrum Distribution ( $I_F = 60 \text{ mA}$ , $T_s = 25^\circ\text{C}$ )

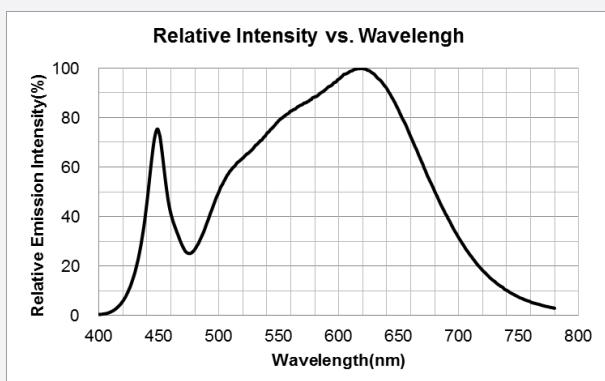
CCT: 2700 K (90 CRI)



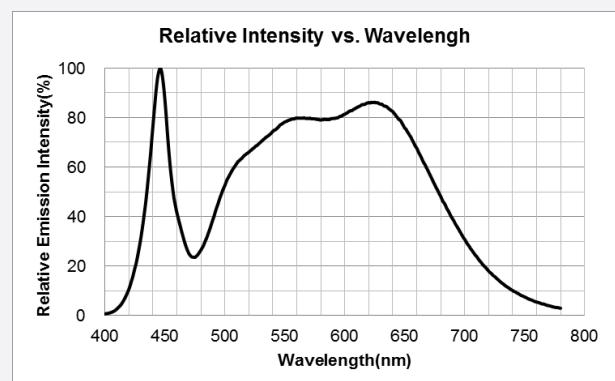
CCT: 3000 K (90 CRI)



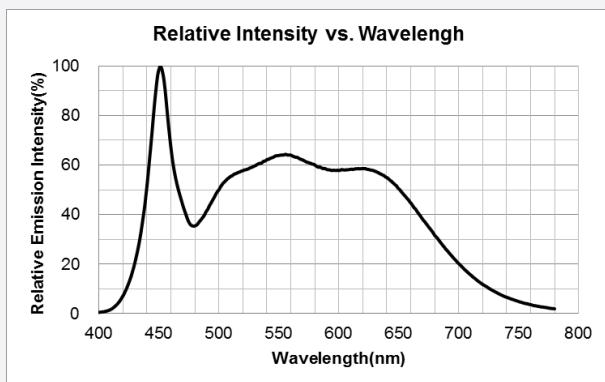
CCT: 3500 K (90 CRI)



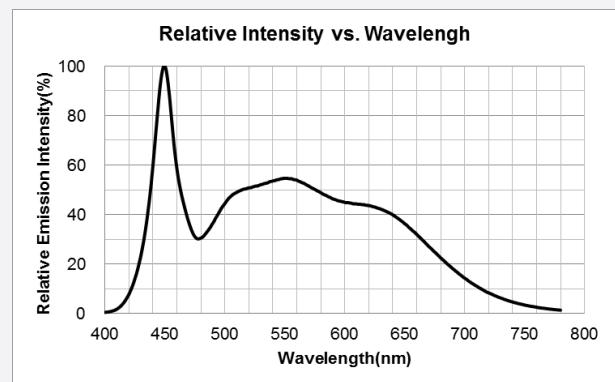
CCT: 4000 K (90 CRI)



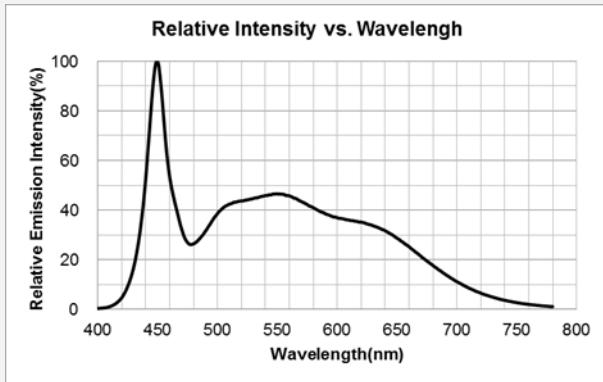
CCT: 5000 K (90 CRI)



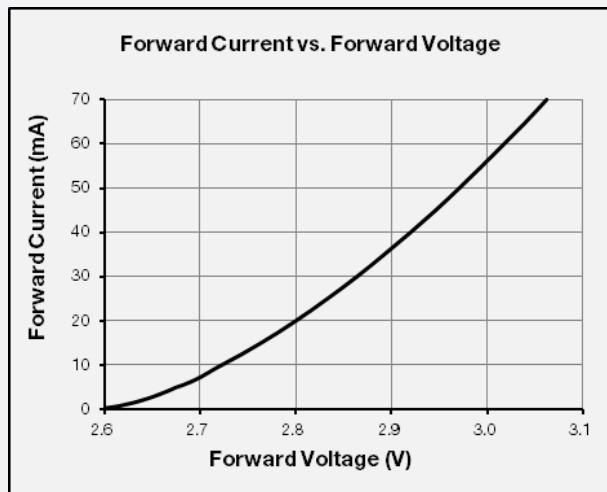
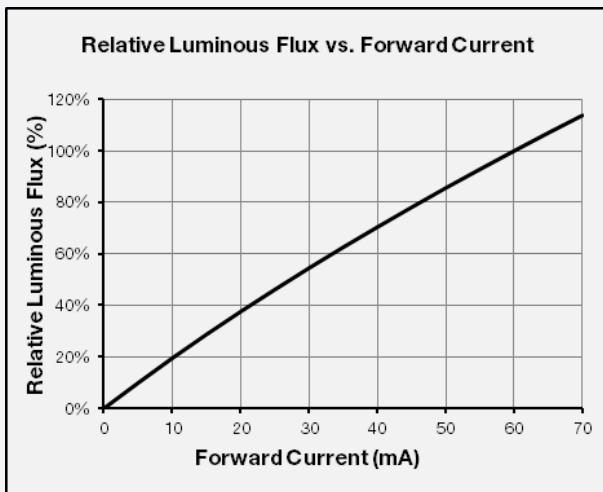
CCT: 5700 K (90 CRI)



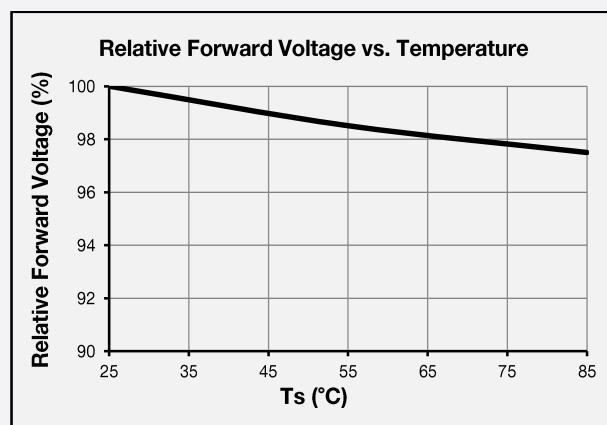
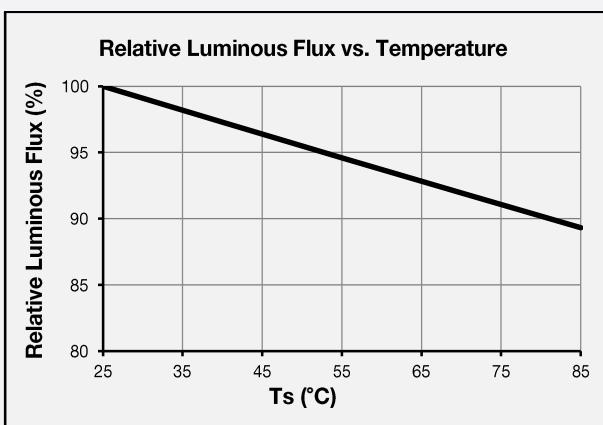
CCT: 6500 K (90 CRI)



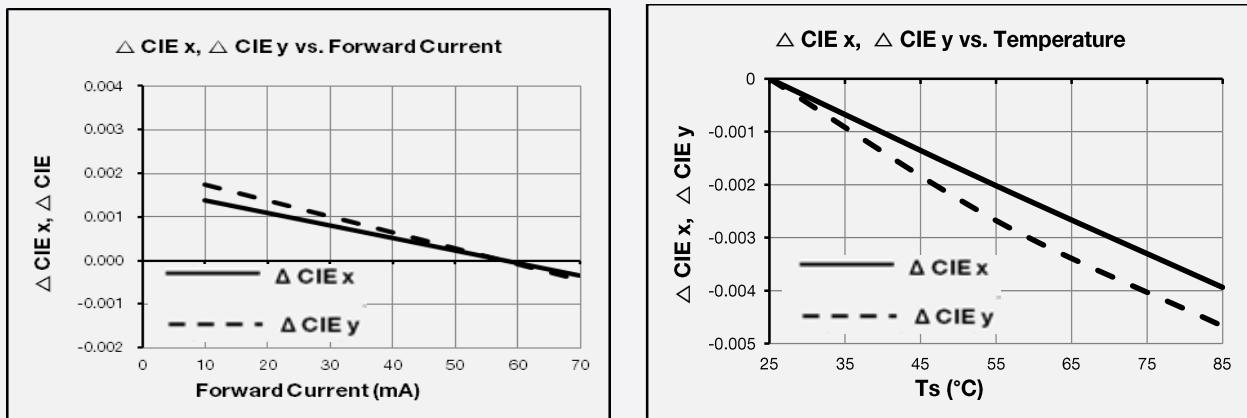
**b) Forward Current Characteristics ( $T_s = 25^\circ\text{C}$ )**



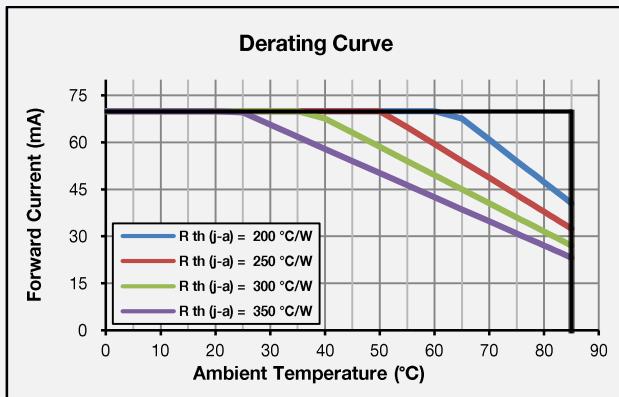
**c) Temperature Characteristics ( $I_F = 60 \text{ mA}$ )**



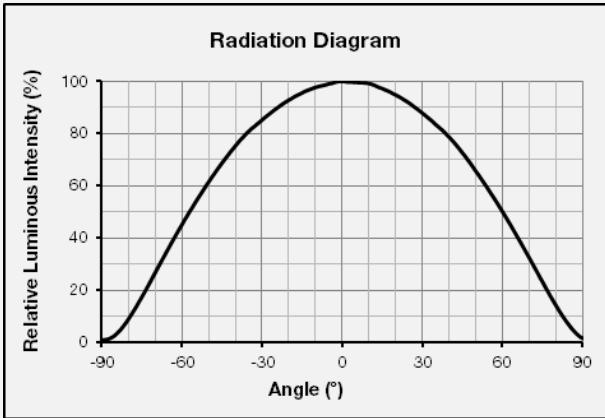
d) Color Shift Characteristics ( $I_F = 60 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )



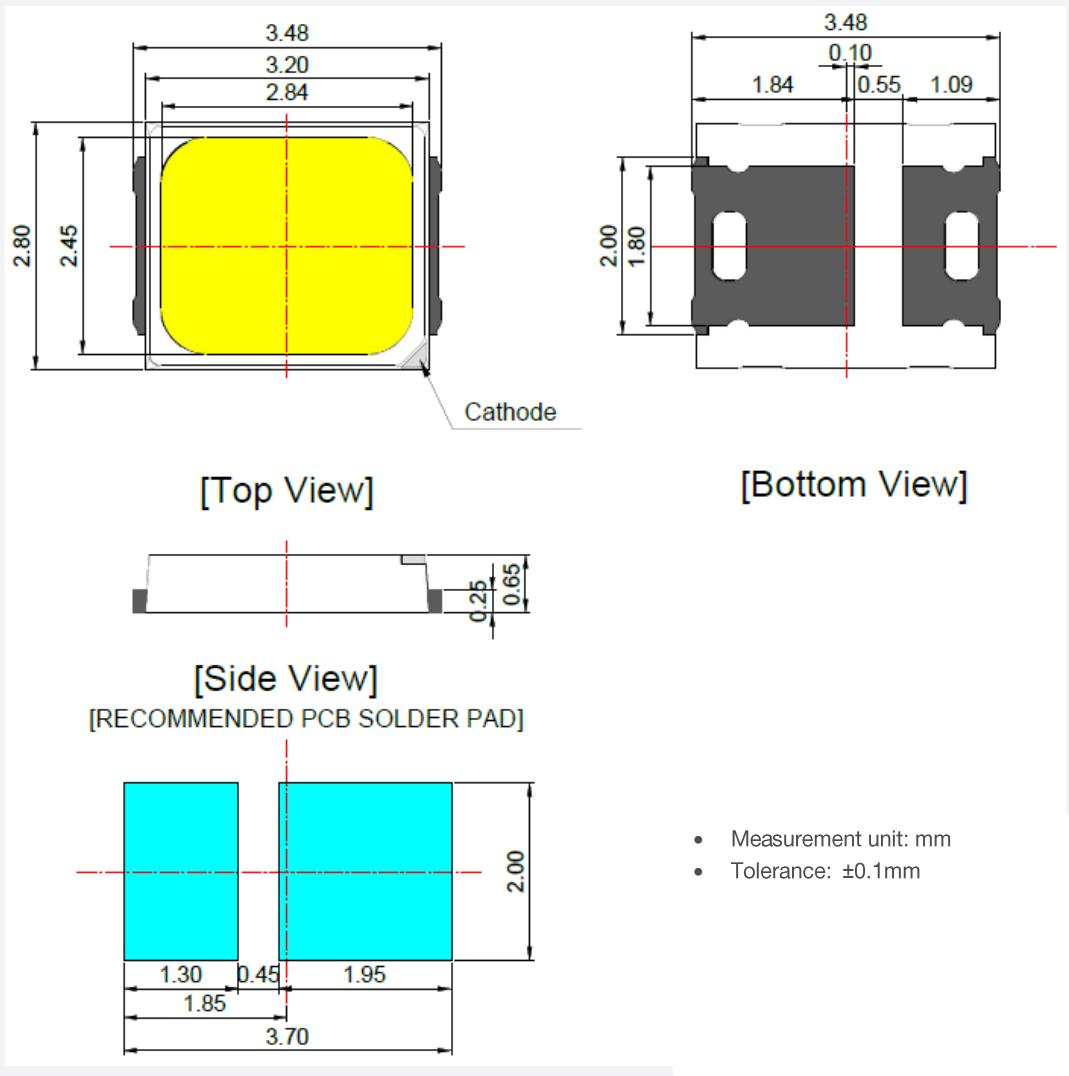
e) Derating Curve



f) Beam Angle Characteristics ( $I_F = 60 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )



#### 4. Outline Drawing & Dimension



#### Notes:

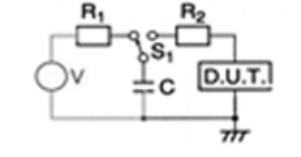
- 1)  $T_s$  point and measurement method:
  - ① Measure one point at the cathode pad, if necessary remove PSR of PCB to reach  $T_s$  point.
  - ② All pads must be soldered to the PCB to dissipate heat properly, otherwise the LED can be damaged.

#### Precautions:

- 1) Pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid strong pressure on the LEDs. Do not put stress on the LEDs during heating.
- 2) Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.
- 3) Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

## 5. Reliability Test Items & Conditions

### a) Test Items

Test Item	Test Condition	Test Hour / Cycle	Sample No.
Room Temperature Life Test	25 °C, DC 70 mA	1000 h	22
High Temperature Life Test	85 °C, DC 70 mA	1000 h	22
High Temperature Humidity Life Test	85 °C, 85 % RH, DC 70 mA	1000 h	22
Low Temperature Life Test	-40 °C, DC 70 mA	1000 h	22
Powered Temperature Cycle Test	-45 °C ~ 85 °C, each 20 min, on/off 5 min Temp. Change time 100min, DC 70 mA	100 cycles	22
Temperature Cycle	-40°C / 15 min ↔ 100 °C / 15 min	200 cycles	100
High Temperature Storage	120 °C	1000 h	11
Low Temperature Storage	-40 °C	1000 h	11
ESD (HBM)	 <p>R<sub>1</sub>: 10 MΩ  R<sub>2</sub>: 1.5 kΩ  C: 100 pF  V: ±2 kV</p>	5 times	30

### b) Criteria for Judging the Damage

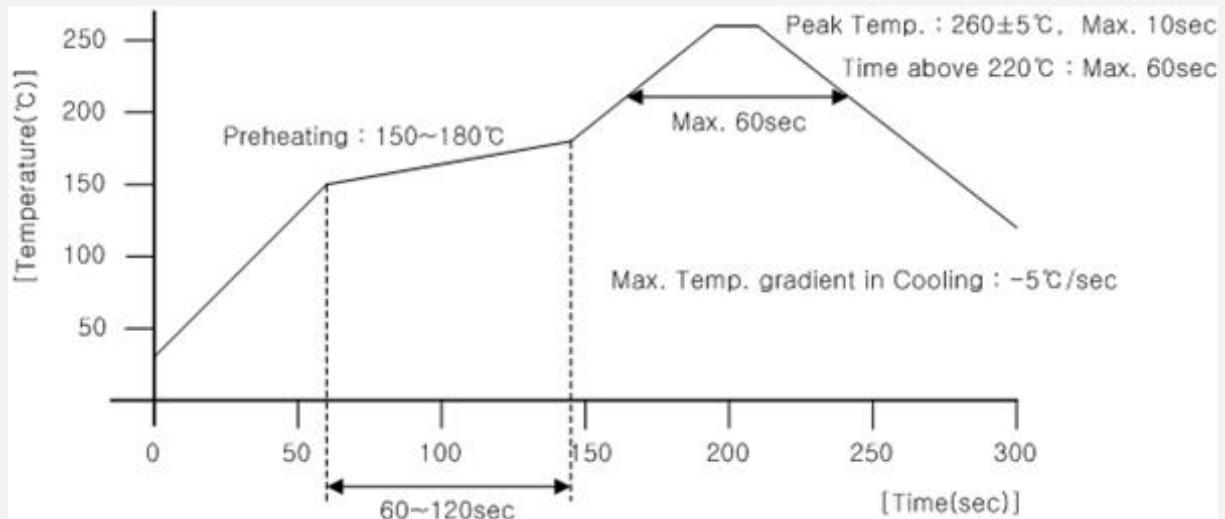
Item	Symbol	Test Condition (T <sub>s</sub> = 25 °C)	Limit	
			Min	Max
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 60 mA	Init. Value * 0.9	Init. Value * 1.1
Luminous Flux	Φ <sub>v</sub>	I <sub>F</sub> = 60 mA	Init. Value * 0.7	Init. Value * 1.1



## 6. Soldering Conditions

### a) Reflow Conditions (Pb free)

Reflow frequency: 2 times max.



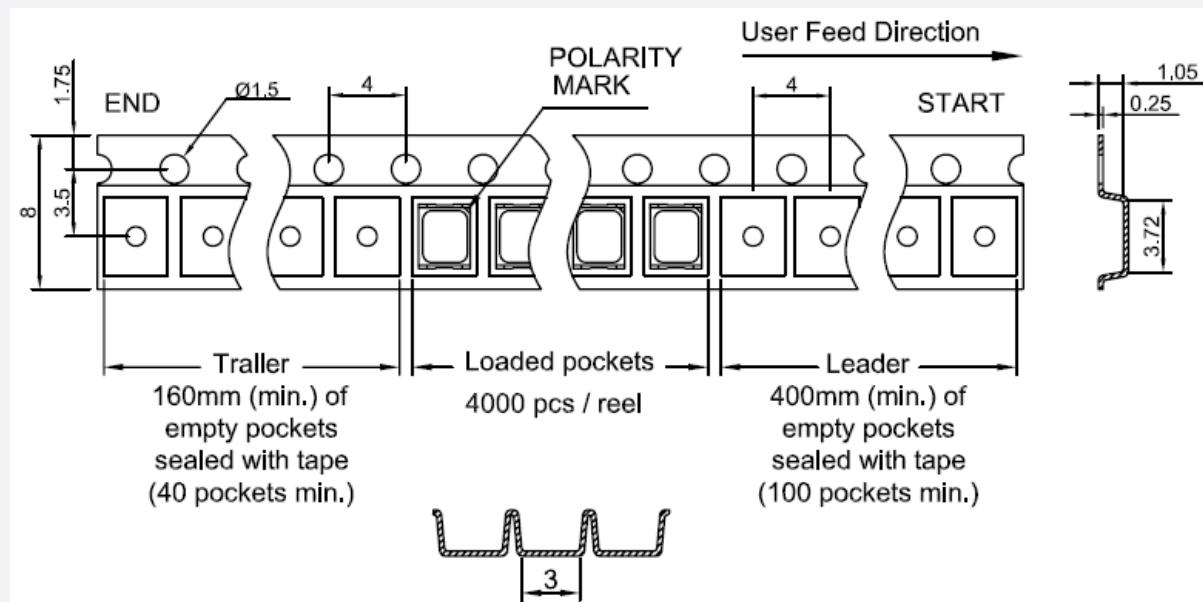
### b) Manual Soldering Conditions

Not more than 5 seconds @ max. 300 °C, under soldering iron.

## 7. Tape & Reel

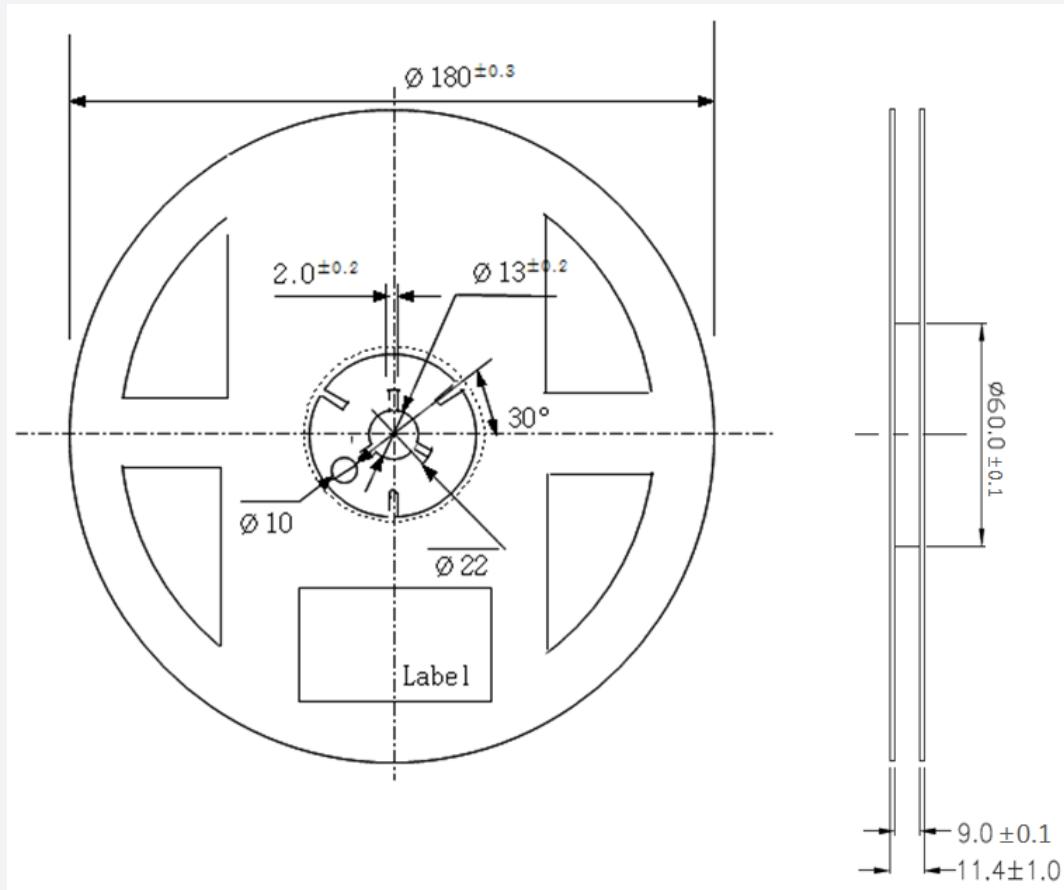
### a) Taping Dimension

(unit: mm)



**b) Reel Dimension (max 4,000 pcs)**

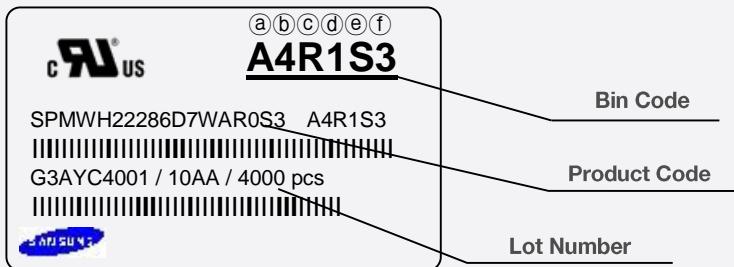
(unit: mm)

**Notes:**

- 1) Quantity: The quantity/reel is 4,000 pcs
- 2) All dimensions are millimeters (tolerance :  $\pm 0.2$ mm)
- 3) Packaging: P/N, Manufacturing data code no. and quantity are indicated on the aluminum packing bag

## 8. Label Structure

### a) Label Structure



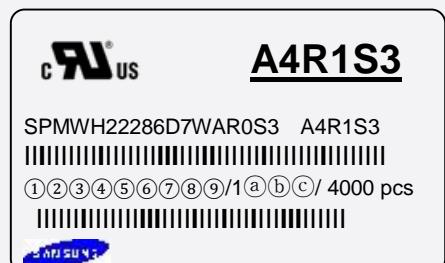
Note: Denoted bin code and product code above is only an example (see description on page 6)

Bin Code:

- (a)(b): Forward Voltage bin (refer to page 10)
- (c)(d): Chromaticity bin (refer to page 9-15)
- (e)(f): Luminous Flux bin (refer to page 7)

### b) Lot Number

The lot number is composed of the following characters:



①②③④⑤⑥⑦⑧⑨ / 1⑩⑪⑫⑬ / 4,000 pcs

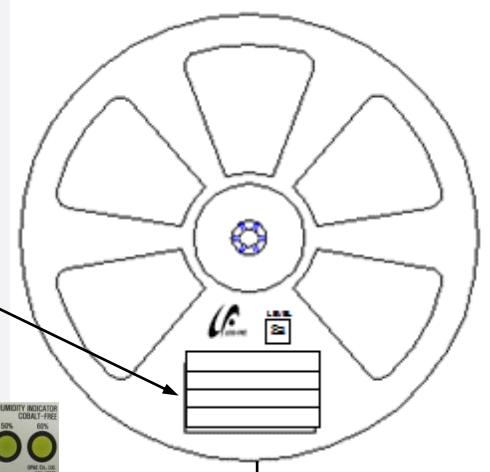
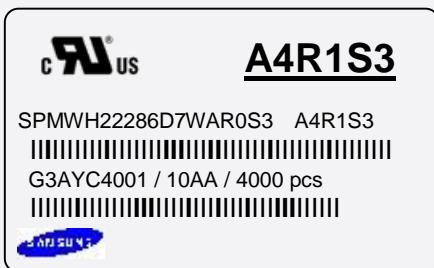
- ① : Production site (S: Giheung, Korea, G: Tianjin, China)
- ② : 3 (LED)
- ③ : Product state (A: Normal, B: Bulk, C: First Production, R: Reproduction, S: Sample)
- ④ : Year (Z: 2015, A: 2016, B: 2017...)
- ⑤ : Month (1~9, A, B, C)
- ⑥ : Day (1~9, A, B~V)
- ⑦⑧⑨ ⑩⑪⑫⑬ : Product serial number



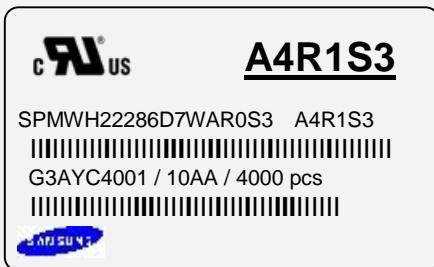
## 9. Packing Structure

### a) Packing Process (The quantity of PKG on the Reel to be Max 4,000 pcs)

Reel



Aluminum Vinyl Packing Bag

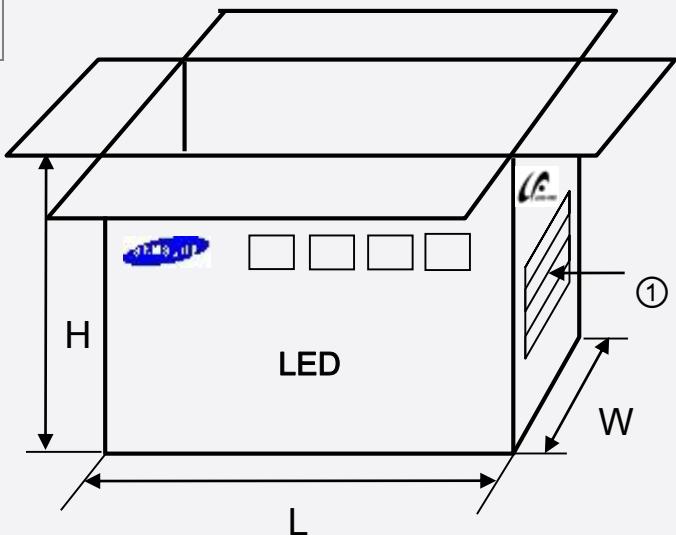
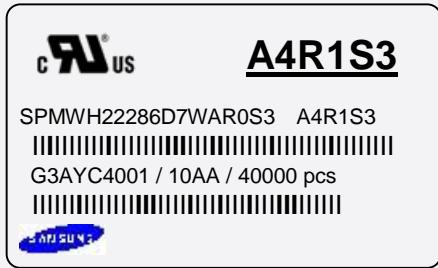


Outer Box

Material: Paper (SW3B(B))

Type	Size (mm)			Note
	L	W	H	
7 inch L	245 ± 5	220 ± 5	182 ± 5	Up to 10 reels
7 inch S	245 ± 5	220 ± 5	86 ± 5	Up to 5 reels

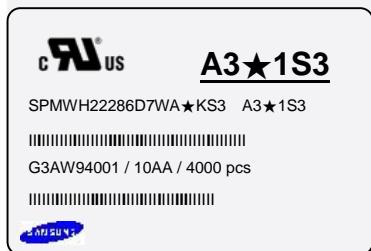
① Side Label



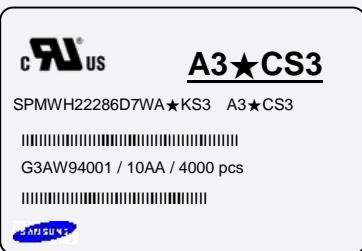
## b) Packing Process for kitting

### Reel

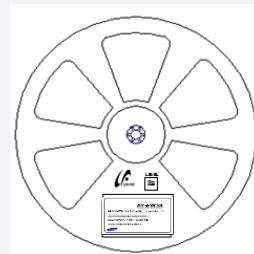
#### Kitting 'A'



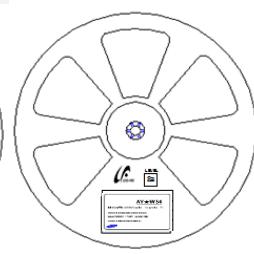
#### Kitting 'B'



#### Kitting 'A'

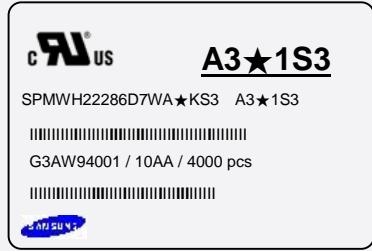


#### Kitting 'B'

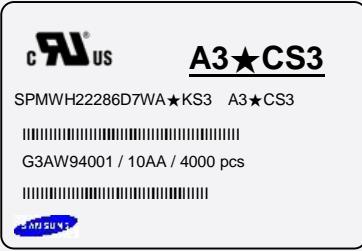


### Aluminum Vinyl Packing Bag

#### Kitting 'A'

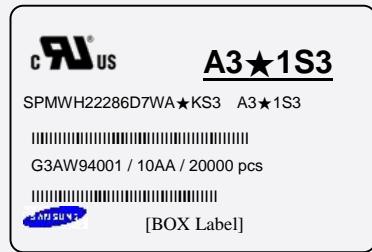


#### Kitting 'B'

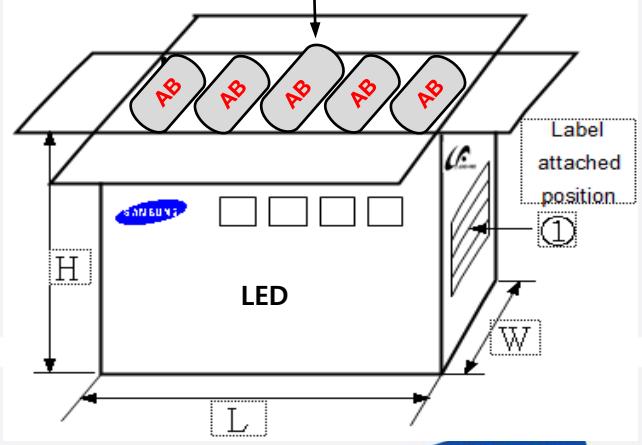
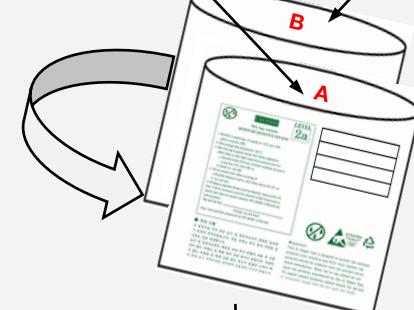
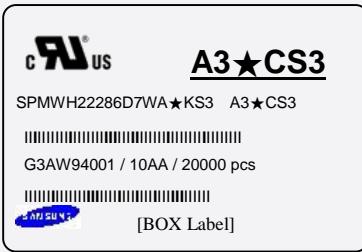


### Outer Box

#### Kitting 'A'



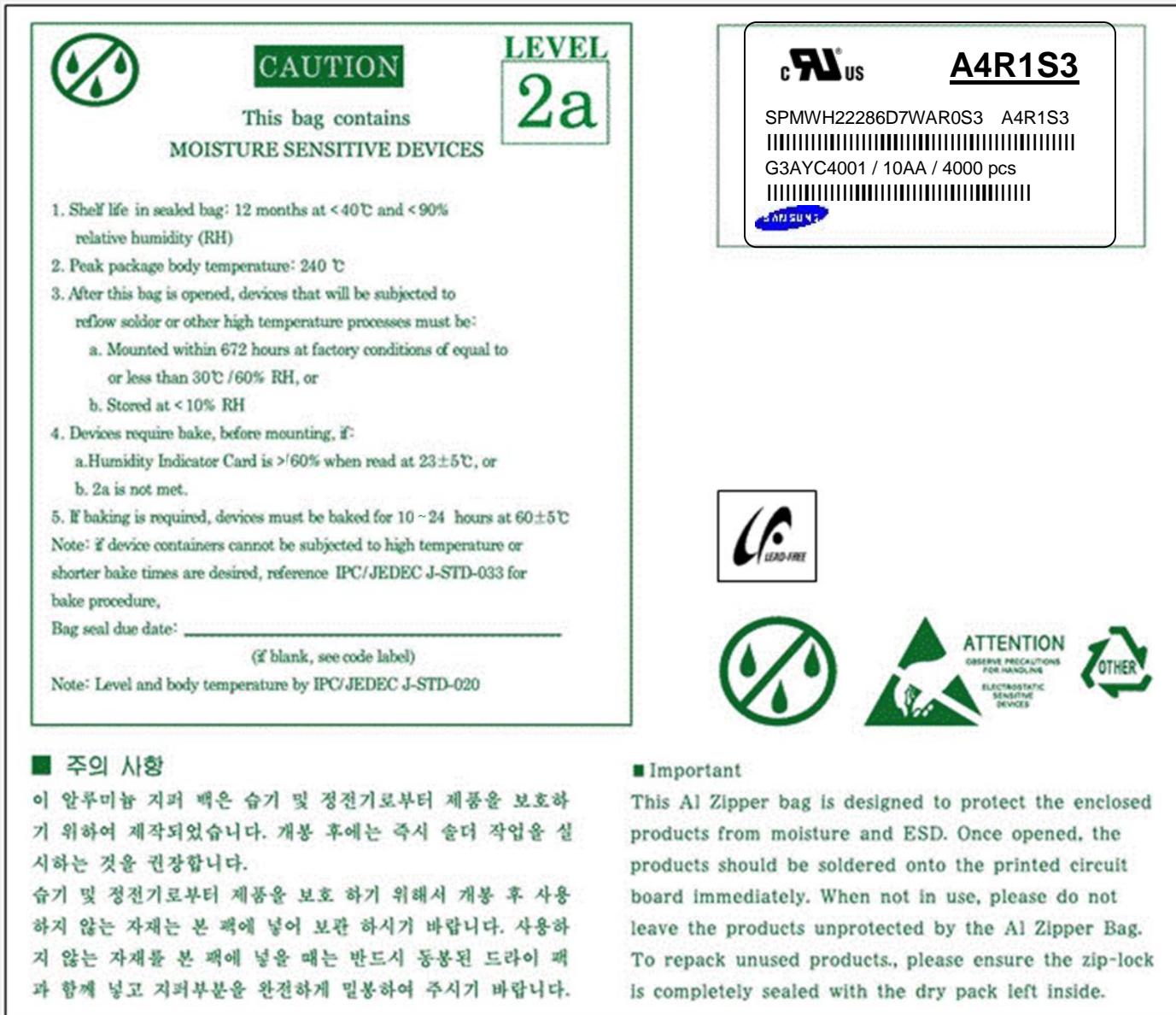
#### Kitting 'B'



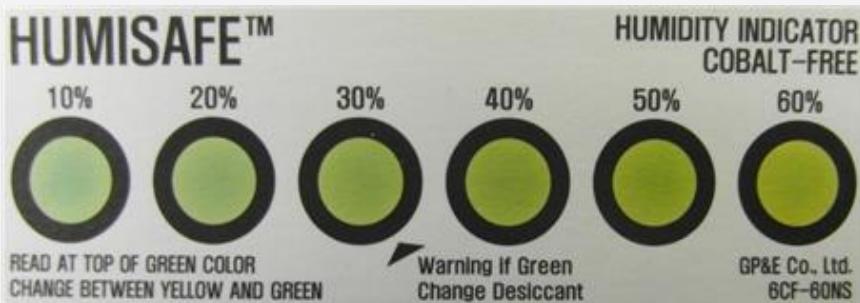
Material: Paper (SW3B(B))

Type	Size (mm)			Note
	L	W	H	
7 inch L	245 ± 5	220 ± 5	182 ± 5	Up to 10 reels

c) Aluminum Vinyl Packing Bag



d) Humidity Indicator Card inside Aluminum Vinyl Bag



## 10. Precautions in Handling & Use

- 1) For over-current protection, users are recommended to apply resistors connected in series with the LEDs to mitigate sudden change of the forward current caused by shift of forward voltage.
- 2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When cleaning is required, IPA is recommended as the cleaning agent. Some solvent-based cleaning agent may damage the silicone resins used in the device.
- 3) When the device is in operation, the forward current should be carefully determined considering the maximum ambient temperature and corresponding junction temperature.
- 4) LEDs must be stored in a clean environment. If the LEDs are to be stored for three months or more after being shipped from Samsung, they should be packed with a nitrogen-filled container (shelf life of sealed bags is 12 months at temperature 0~40 °C, 0~90 % RH).
- 5) After storage bag is opened, device subjected to soldering, solder reflow, or other high temperature processes must be:
  - a. Mounted within 672 hours (28 days) at an assembly line with a condition of no more than 30 °C / 60 % RH<sup>\*Note 1</sup>, or
  - b. Mounted within 24 hours (1 day) at an assembly line with a condition of more than 30 °C / 70 % RH<sup>\*Note 2</sup>, or
  - c. Stored at <10 % RH.

\*Note 1, 2: IPC/JEDEC J-STD-033A, Recommended Equivalent Total Floor Life Table

Package Type and Body Thickness	Moisture Sensitivity Level	Maximum Percent Relative Humidity						Temperature
		40%	50%	60%	70%	80%	90%	
Body Thickness <2.1mm	Level 2a	∞	∞	28	1	1	1	30°C
		∞	∞	∞	2	1	1	25°C
		∞	∞	∞	2	2	1	20°C

- 6) Repack unused devices with anti-moisture packing, fold to close any opening and then store in a dry place.
- 7) Devices require baking before mounting, if humidity card reading is >60 % at 23 ± 5 °C.
- 8) Devices must be baked for 1 hour at 60 ± 5 °C, if baking is required.
- 9) The LEDs are sensitive to the static electricity and surge current. It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs. If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices. Damaged LEDs may show some unusual characteristics such as increase in leakage current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.
- 10) VOCs (Volatile Organic Compounds) can be generated from adhesives, flux, hardener or organic additives used in luminaires (fixtures). Transparent LED silicone encapsulant is permeable to those chemicals and they may lead to a discoloration of encapsulant when they exposed to heat or light. This phenomenon can cause a significant loss of light emitted (output) from the luminaires. In order to prevent these problems, we recommend users to know the physical properties of materials used in luminaires and they must be carefully selected.
- 11) Risk of sulfurization (or tarnishing)  
The LED from Samsung uses a silver-plated lead frame and its surface color may change to black (or dark colored) when it is exposed to sulfur (S), chlorine (Cl) or other halogen compound. Sulfurization of lead frame may cause intensity degradation, change of chromaticity coordinates and, in extreme cases, open circuit. It requires caution. Due to possible sulfurization of lead frame, LED should not be used and stored together with oxidizing substances made of materials such as rubber, plain paper, lead solder cream, etc.



# Legal and additional information.

## About Samsung Electronics Co., Ltd.

Samsung Electronics Co. Ltd inspires the world and shapes the future with transformative ideas and technologies. The company is redefining the worlds of TVs, smartphones, wearable devices, tablets, digital appliances, network systems, and semiconductor and LED solutions. For the latest news, please visit the Samsung Newsroom at <http://news.samsung.com>.

Copyright © 2017 Samsung Electronics Co., Ltd. All rights reserved.

Samsung is a registered trademark of Samsung Electronics Co., Ltd.

Specifications and designs are subject to change without notice. Non-metric weights and measurements are approximate. All data were deemed correct at time of creation. Samsung is not liable for errors or omissions. All brand, product, service names and logos are trademarks and/or registered trademarks of their respective owners and are hereby recognized and acknowledged.

Samsung Electronics Co., Ltd.

95, Samsung 2-ro

Giheung-gu

Yongin-si, Gyeonggi-do, 446-711

KOREA

[www.samsungled.com](http://www.samsungled.com)

