

## Middle Power LED Series 3030

# LM301B

## CRI 80



### Features & Benefits

- 0.3 W class middle power LED
- Mold resin for high reliability
- Standard form factor for design flexibility (3.0 × 3.0 mm)



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## 1. Characteristics

### a) Absolute Maximum Rating

| Item                            | Symbol    | Rating     | Unit    | Condition                   |
|---------------------------------|-----------|------------|---------|-----------------------------|
| Ambient / Operating Temperature | $T_a$     | -40 ~ +85  | °C      | -                           |
| Storage Temperature             | $T_{stg}$ | -40 ~ +100 | °C      | -                           |
| LED Junction Temperature        | $T_j$     | 110        | °C      | -                           |
| Forward Current                 | $I_F$     | 180        | mA      | -                           |
| Pulse Forward Current           | $I_{FP}$  | 300        | mA      | Duty 1/10, pulse width 10ms |
| Assembly Process Temperature    | -         | 260<br><10 | °C<br>s | -                           |
| ESD (HBM)                       | -         | 5          | kV      | -                           |

### b) Electro-optical Characteristics ( $I_F = 65 \text{ mA}$ , $T_s = 25 \text{ °C}$ )

| Item                                             | Unit | Rank | Bin | Min. | Typ. | Max. |
|--------------------------------------------------|------|------|-----|------|------|------|
| Forward Voltage ( $V_F$ )                        | V    | XA   | AY  | 2.6  | -    | 2.7  |
|                                                  |      |      | AZ  | 2.7  | -    | 2.8  |
|                                                  |      |      | A1  | 2.8  | -    | 2.9  |
| Reverse Voltage<br>(@ 5 mA)                      | V    |      |     | 0.7  | -    | 1.2  |
| Color Rendering Index ( $R_a$ )                  | -    |      |     | 80   | -    | -    |
| Thermal Resistance<br>(junction to solder point) | °C/W |      |     | -    | 7.5  | -    |
| Beam Angle                                       | °    |      |     | -    | 120  | -    |

#### Note:

Samsung maintains measurement tolerance of: forward voltage =  $\pm 0.1 \text{ V}$ , luminous flux =  $\pm 5 \%$ , CRI =  $\pm 3$

**c) Electro-optical Characteristics ( $T_s = 25\text{ }^{\circ}\text{C}$ )**

| Item                       | CRI | Nominal CCT (K) | SH   |      | SJ   |      | SK   |      | SL   |      | Current |
|----------------------------|-----|-----------------|------|------|------|------|------|------|------|------|---------|
|                            |     |                 | Min. | Max. | Min. | Max. | Min. | Max. | Min. | Max. |         |
|                            |     |                 | 32   | 34   | 34   | 36   | 36   | 38   | 38   | 40   | 65mA    |
| Luminous Flux ( $\Phi_v$ ) | 80  | 2700            |      |      |      |      |      |      |      |      |         |
|                            |     | 3000            |      |      |      |      |      |      |      |      |         |
|                            |     | 3500            |      |      |      |      |      |      |      |      |         |
|                            |     | 4000            |      |      |      |      |      |      |      |      |         |
|                            |     | 5000            |      |      |      |      |      |      |      |      |         |
|                            |     | 5700            |      |      |      |      |      |      |      |      |         |
|                            |     | 6500            |      |      |      |      |      |      |      |      |         |

**Note:**

Samsung maintains measurement tolerance of: forward voltage =  $\pm 0.1\text{V}$ , luminous flux =  $\pm 5\%$ , CRI =  $\pm 3$

## 2. Product Code Information

| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 |
|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|----|----|
| S | P | M | W | H | T | 3 | 2 | A | M  | D  | 5  | X  | A  | R  | 0  | S  | 0  |

| Digit | PKG Information              | Code | Specification                                                                                         |
|-------|------------------------------|------|-------------------------------------------------------------------------------------------------------|
| 1 2 3 | Samsung Package Middle Power | SPM  |                                                                                                       |
| 4 5   | Color                        | WH   | White                                                                                                 |
| 6     | Product Version              | T    | F : Film 3000K, 2700K, D : Dispensing 6500K, 5700K, 5000K, 4000K, 3500K                               |
| 7 8 9 | Form Factor                  | 32A  | 3.0 x 3.0 x 0.7 mm; 2 pads;                                                                           |
| 10    | Sorting Current (mA)         | M    | 65 mA                                                                                                 |
| 11    | Chromaticity Coordinates     | D    | ANSI Standard, MacAdam 3 step ellipse bin, MacAdam 5 step ellipse bin                                 |
| 12    | CRI                          | 5    | Min. 80                                                                                               |
| 13 14 | Forward Voltage (V)          | XA   | <div>2.6~2.9<div>Bin Code:<div>AY 2.6~2.7</div><div>AZ 2.7~2.8</div><div>A1 2.8~2.9</div></div></div> |
| 15 16 | CCT (K)                      | W●   | 2700 <div>WA, WB, WC, WD, WE, WF, WG, WH, WJ, WK, WL, WM, W3</div>                                    |
|       |                              | V●   | 3000 <div>VA, VB, VC, VD, VE, VF, VG, VH, VJ, VK, VL, VM, V3</div>                                    |
|       |                              | U●   | 3500 <div>UA, UB, UC, UD, UE, UF, UG, UH, UJ, UK, UL, UM, U3</div>                                    |
|       |                              | T●   | 4000 <div>TA, TB, TC, TD, TE, TF, TG, TH, TJ, TK, TL, TM, T3</div>                                    |
|       |                              | R●   | 5000 <div>RA, RB, RC, RD, RE, RF, RG, RH, RJ, RK, RL, RM, R3</div>                                    |
|       |                              | Q●   | 5700 <div>QA, QB, QC, QD, QE, QF, QG, QH, QJ, QK, QL, QM, Q3</div>                                    |
|       |                              | P●   | 6500 <div>PA, PB, PC, PD, PE, PF, PG, PH, PJ, PK, PL, PM, P3</div>                                    |
|       |                              |      | ● : "0" (Whole bin) "3" (MacAdam 3-step ellipse bin) or "K" (K Kitting) or "S" (S Kitting)            |
| 17 18 | Luminous Flux                | S0   | <div>Bin Code:</div> SH, SJ, SK, SL                                                                   |

**a) Luminous Flux Bins ( $I_f = 65 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )**

| CRI (Ra)<br>Min. | Nominal CCT<br>(K) | Product Code       | Flux Bin | Flux Range<br>( $\Phi_v$ , lm) |
|------------------|--------------------|--------------------|----------|--------------------------------|
| 80               | 2700               | SPMWHF32AMD5XAW●S0 | SH       | 32.0~34.0                      |
|                  |                    |                    | SJ       | 34.0 ~36.0                     |
|                  | 3000               | SPMWHF32AMD5XAV●S0 | SJ       | 34.0 ~36.0                     |
|                  |                    |                    | SK       | 36.0 ~38.0                     |
|                  | 3500               | SPMWHD32AMD5XAU●S0 | SJ       | 34.0 ~36.0                     |
|                  |                    |                    | SK       | 36.0 ~38.0                     |
|                  | 4000               | SPMWHD32AMD5XAT●S0 | SK       | 36.0 ~38.0                     |
|                  |                    |                    | SL       | 38.0 ~ 40.0                    |
|                  |                    |                    | SK       | 36.0 ~38.0                     |
|                  |                    |                    | SL       | 38.0 ~ 40.0                    |
|                  | 5000               | SPMWHD32AMD5XAR●S0 | SK       | 36.0 ~38.0                     |
|                  |                    |                    | SL       | 38.0 ~ 40.0                    |
|                  | 5700               | SPMWHD32AMD5XAQ●S0 | SK       | 36.0 ~38.0                     |
|                  |                    |                    | SL       | 38.0 ~ 40.0                    |
|                  | 6500               | SPMWHD32AMD5XAP●S0 | SK       | 36.0 ~38.0                     |
|                  |                    |                    | SL       | 38.0 ~ 40.0                    |

**Note:**

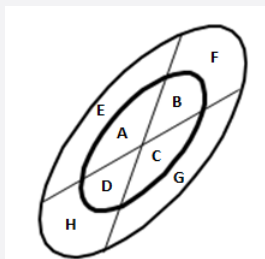
"●" can be "0" (Whole bin), "3" (MacAdam 3-step ellipse bin), "K" (K Kitting) or "S" (S Kitting) of the color binning

**b) Kitting Rule**

### 1) S Kitting Bin Concept

1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, lm).
2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (AY+AY), (AZ+AZ) or (A1+A1)
3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)
4. A luminous flux(lm) of kitting bin is combined by a pair of IV rank such as (SH+SH), (SH+SJ), (SJ+SJ), (SJ+SK), (SK+SK), (SK+SL) or (SL+SL)

#### [Kitting example]



#### [Binning Information]

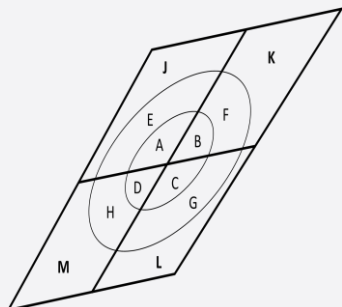
|     | Bin #1                  | Bin #2                  | Remark |
|-----|-------------------------|-------------------------|--------|
| VF  | AY                      | AY                      |        |
|     | AZ                      | AZ                      |        |
|     | A1                      | A1                      |        |
| CIE | A                       | G                       |        |
|     | C                       | E                       |        |
|     | D                       | F                       |        |
|     | B                       | H                       |        |
|     | E                       | G                       |        |
|     | F                       | H                       |        |
|     | MacA. 3step(A, B, C, D) | MacA. 3step(A, B, C, D) |        |
| IV  | SH                      | SH                      |        |
|     | SH                      | SJ                      |        |
|     | SJ                      | SJ                      |        |
|     | SJ                      | SK                      |        |
|     | SK                      | SK                      |        |
|     | SK                      | SL                      |        |
|     | SL                      | SL                      |        |
|     |                         |                         |        |

### 2) K Kitting Bin Concept

1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, lm).

2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (AY+AY), (AY+AZ), (AZ+AZ), (AZ+A1) or (A1+A1)
3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)
4. A luminous flux(lm) of kitting bin is combined by a pair of IV rank such as (SH+SH), (SH+SJ), (SJ+SJ), (SJ+SK), (SK+SK), (SK+SL) or (SL+SL)

**[Kitting example]**



**[Binning Information]**

|     | Bin #1                  | Bin #2                  | Remark |
|-----|-------------------------|-------------------------|--------|
| VF  | AY                      | AY                      |        |
|     | AY                      | AZ                      |        |
|     | AZ                      | AZ                      |        |
|     | AZ                      | A1                      |        |
|     | A1                      | A1                      |        |
| CIE | H                       | K                       |        |
|     | F                       | M                       |        |
|     | E                       | L                       |        |
|     | G                       | J                       |        |
|     | E                       | G                       |        |
|     | F                       | H                       |        |
|     | MacA. 3step(A, B, C, D) | MacA. 3step(A, B, C, D) |        |
| IV  | SH                      | SH                      |        |
|     | SH                      | SJ                      |        |
|     | SJ                      | SJ                      |        |
|     | SJ                      | SK                      |        |
|     | SK                      | SK                      |        |
|     | SK                      | SL                      |        |
|     | SL                      | SL                      |        |

c) Color Bins ( $I_f = 65 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )

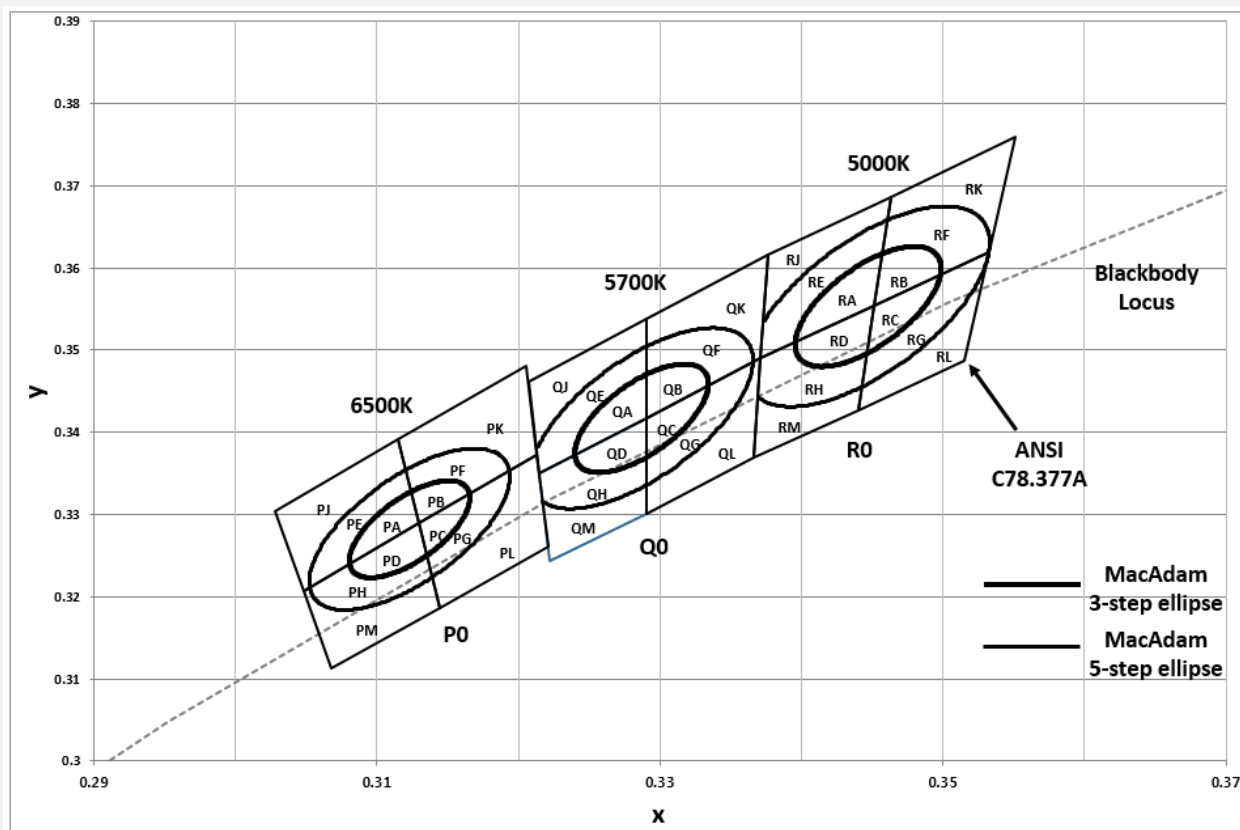
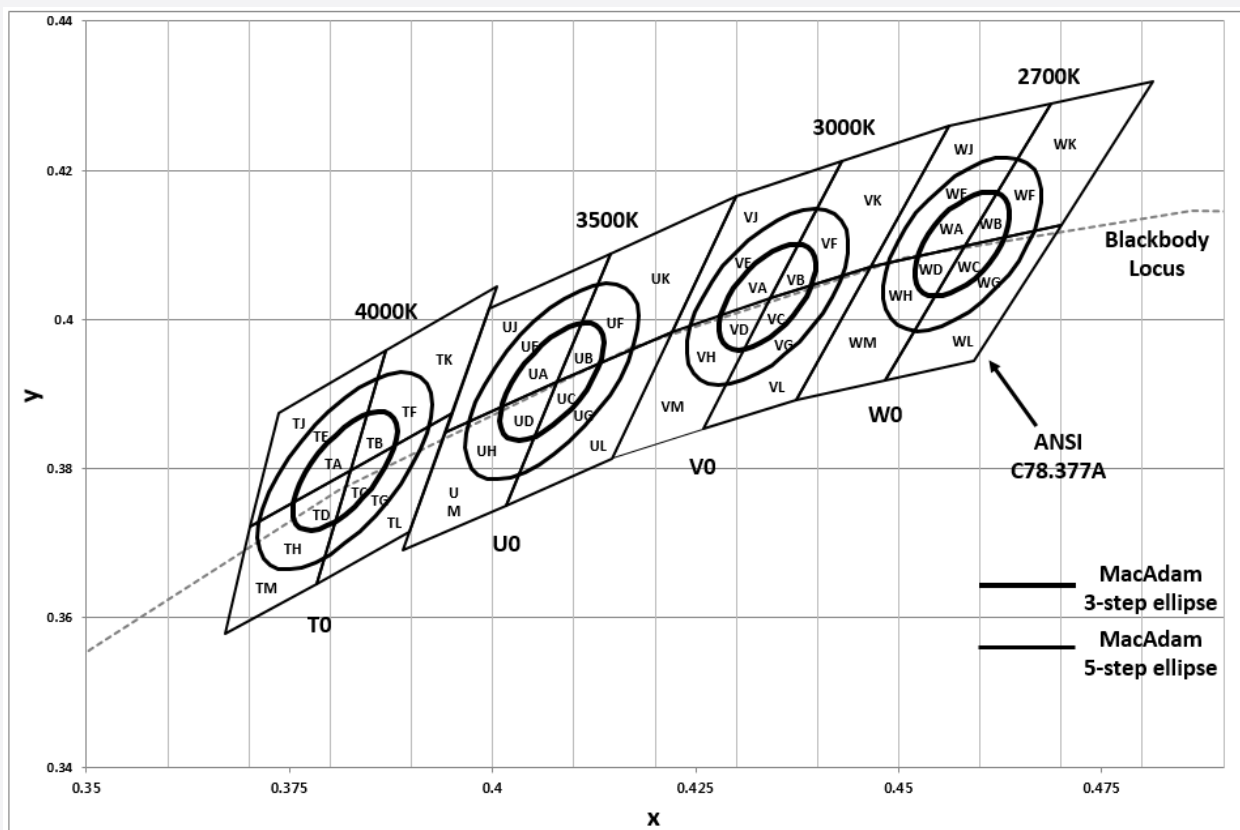
| CRI (Ra)<br>Min. | Nominal CCT<br>(K) | Product Code       | Color Rank                         | Chromaticity Bins                                 |
|------------------|--------------------|--------------------|------------------------------------|---------------------------------------------------|
| 80               | 2700               | SPMWHF32AMD5XAW0S0 | W0<br>(Whole bin)                  | WA, WB, WC, WD, WE, WF, WG, WH,<br>WJ, WK, WL, WM |
|                  |                    | SPMWHF32AMD5XAW3S0 | W3<br>(MacAdam 3-step ellipse bin) | WA, WB, WC, WD                                    |
|                  |                    | SPMWHF32AMD5XAWSS0 | WS<br>(S Kitting)                  | WA, WB, WC, WD, WE, WF, WG, WH,                   |
|                  |                    | SPMWHF32AMD5XAWKS0 | WK<br>(K Kitting)                  | WA, WB, WC, WD, WE, WF, WG, WH,<br>WJ, WK, WL, WM |
|                  | 3000               | SPMWHF32AMD5XAV0S0 | V0<br>(Whole bin)                  | VA, VB, VC, VD, VE, VF, VG, VH,<br>VJ, VK, VL, VM |
|                  |                    | SPMWHF32AMD5XAV3S0 | V3<br>(MacAdam 3-step ellipse bin) | VA, VB, VC, VD                                    |
|                  |                    | SPMWHF32AMD5XAVSS0 | VS<br>(S Kitting)                  | VA, VB, VC, VD, VE, VF, VG, VH                    |
|                  |                    | SPMWHF32AMD5XAVKS0 | VK<br>(K Kitting)                  | VA, VB, VC, VD, VE, VF, VG, VH,<br>VJ, VK, VL, VM |
|                  | 3500               | SPMWHD32AMD5XAU0S0 | U0<br>(Whole bin)                  | UA, UB, UC, UD, UE, UF, UG, UH,<br>UJ, UK, UL, UM |
|                  |                    | SPMWHD32AMD5XAU3S0 | U3<br>(MacAdam 3-step ellipse bin) | UA, UB, UC, UD                                    |
|                  |                    | SPMWHD32AMD5XAUSS0 | US<br>(S Kitting)                  | UA, UB, UC, UD, UE, UF, UG, UH                    |
|                  |                    | SPMWHD32AMD5XAUKS0 | UK<br>(K Kitting)                  | UA, UB, UC, UD, UE, UF, UG, UH,<br>UJ, UK, UL, UM |
|                  | 4000               | SPMWHD32AMD5XAT0S0 | T0<br>(Whole bin)                  | TA, TB, TC, TD, TE, TF, TG, TH,<br>TJ, TK, TL, TM |
|                  |                    | SPMWHD32AMD5XAT3S0 | T3<br>(MacAdam 3-step ellipse bin) | TA, TB, TC, TD                                    |
|                  |                    | SPMWHD32AMD5XATSS0 | TS<br>(S Kitting)                  | TA, TB, TC, TD, TE, TF, TG, TH                    |
|                  |                    | SPMWHD32AMD5XATKS0 | TK<br>(K Kitting)                  | TA, TB, TC, TD, TE, TF, TG, TH,<br>TJ, TK, TL, TM |
|                  | 5000               | SPMWHD32AMD5XAR0S0 | R0<br>(Whole bin)                  | RA, RB, RC, RD, RE, RF, RG, RH<br>RJ,RK,RL,RM     |
|                  |                    | SPMWHD32AMD5XAR3S0 | R3<br>(MacAdam 3-step ellipse bin) | RA, RB, RC, RD                                    |
|                  |                    | SPMWHD32AMD5XARSS0 | RS<br>(S Kitting)                  | RA, RB, RC, RD, RE, RF, RG, RH                    |
|                  |                    | SPMWHD32AMD5XARKS0 | RK<br>(K Kitting)                  | RA, RB, RC, RD, RE, RF, RG, RH<br>RJ,RK,RL,RM     |
|                  | 5700               | SPMWHD32AMD5XAQ0S0 | Q0<br>(Whole bin)                  | QA, QB, QC, QD, QE, QF, QG, QH<br>QJ,QK,QL,QM     |
|                  |                    | SPMWHD32AMD5XAQ3S0 | Q3<br>(MacAdam 3-step ellipse bin) | QA, QB, QC, QD                                    |
|                  |                    | SPMWHD32AMD5XAQSS0 | QS<br>(S Kitting)                  | QA, QB, QC, QD, QE, QF, QG, QH                    |

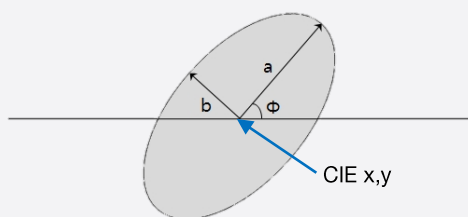
|      |                    |                                    |                                                   |
|------|--------------------|------------------------------------|---------------------------------------------------|
| 6500 | SPMWHD32AMD5XAQKS0 | QK<br>(K Kitting)                  | QA, QB, QC, QD, QE, QF, QG, QH<br>QJ, QK, QL, QM  |
|      | SPMWHD32AMD5XAP0S0 | P0<br>(Whole bin)                  | PA, PB, PC, PD, PE, PF, PG, PH,<br>PJ, PK, PL, PM |
|      | SPMWHD32AMD5XAP3S0 | P3<br>(MacAdam 3-step ellipse bin) | PA, PB, PC, PD                                    |
|      | SPMWHD32AMD5XAPSS0 | PS<br>(S Kitting)                  | PA, PB, PC, PD, PE, PF, PG, PH                    |
|      | SPMWHD32AMD5XAPKS0 | PK<br>(K Kitting)                  | PA, PB, PC, PD, PE, PF, PG, PH,<br>PJ, PK, PL, PM |

#### d) Voltage Bins ( $I_F = 65 \text{ mA}$ , $T_s = 25^\circ\text{C}$ )

| CRI ( $R_a$ )<br>Min. | Nominal CCT<br>(K) | Product Code | Voltage Rank | Voltage Bin | Voltage Range<br>(V) |
|-----------------------|--------------------|--------------|--------------|-------------|----------------------|
| -                     | -                  | -            | XA           | AY          | 2.6 ~ 2.7            |
|                       |                    |              |              | AZ          | 2.7 ~ 2.8            |
|                       |                    |              |              | A1          | 2.8 ~ 2.9            |

e) Chromaticity Region & Coordinates ( $I_F = 65 \text{ mA}$ ,  $T_s = 25^\circ\text{C}$ )



**f) Chromaticity Region & Coordinates ( $I_F = 65 \text{ mA}$ ,  $T_s = 25 \text{ }^\circ\text{C}$ )**


| MacAdam Ellipse (W3, W5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.4578 | 0.4101 | 53.70    | 0.0081  | 0.0042  |
| 5-step                   | 0.4578 | 0.4101 | 53.70    | 0.01350 | 0.00700 |

| MacAdam Ellipse (V3, V5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.4338 | 0.4030 | 53.22    | 0.0083  | 0.0041  |
| 5-step                   | 0.4338 | 0.4030 | 53.22    | 0.01390 | 0.00680 |

| MacAdam Ellipse (U3, U5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.4073 | 0.3917 | 54.00    | 0.00927 | 0.00414 |
| 5-step                   | 0.4073 | 0.3917 | 54.00    | 0.01545 | 0.00690 |

| MacAdam Ellipse (T3, T5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.3818 | 0.3797 | 53.72    | 0.00939 | 0.00402 |
| 5-step                   | 0.3818 | 0.3797 | 53.72    | 0.01565 | 0.00670 |

| MacAdam Ellipse (R3, R5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.3447 | 0.3553 | 59.62    | 0.0082  | 0.0035  |
| 5-step                   | 0.3447 | 0.3553 | 59.62    | 0.01370 | 0.00590 |

| MacAdam Ellipse (Q3, Q5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.3287 | 0.3417 | 59.09    | 0.00746 | 0.00320 |
| 5-step                   | 0.3287 | 0.3417 | 59.09    | 0.01243 | 0.00533 |

| MacAdam Ellipse (P3, P5) |        |        |          |         |         |
|--------------------------|--------|--------|----------|---------|---------|
| Step                     | CIE x  | CIE y  | $\theta$ | a       | b       |
| 3-step                   | 0.3123 | 0.3282 | 58.57    | 0.00669 | 0.00285 |
| 5-step                   | 0.3123 | 0.3282 | 58.57    | 0.01115 | 0.00475 |

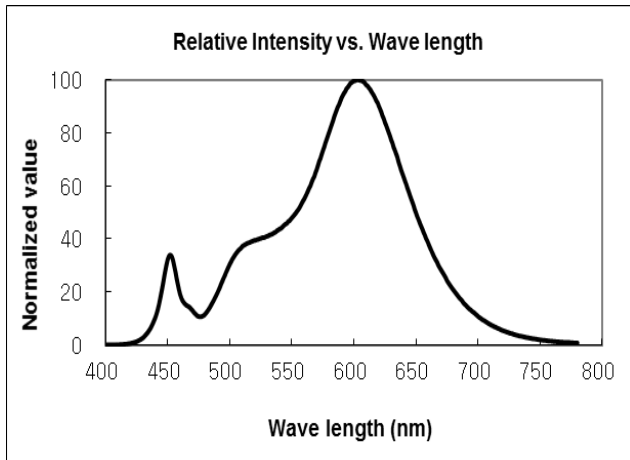
**Note:**

Samsung maintains measurement tolerance of:  $C_x, C_y = \pm 0.005$

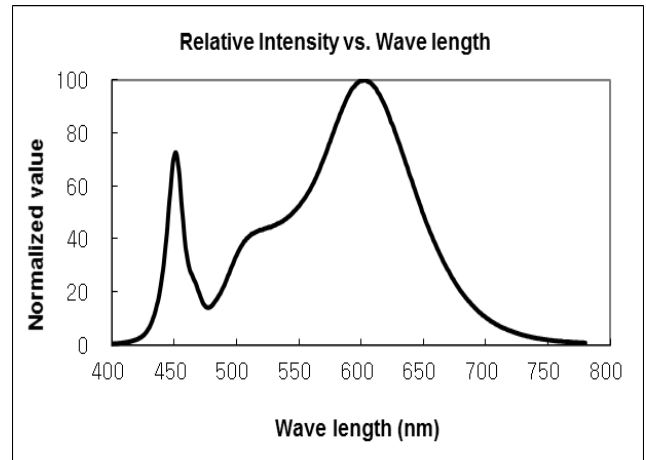
### 3. Typical Characteristics Graphs

#### a) Spectrum Distribution ( $I_f = 65 \text{ mA}$ , $T_s = 25^\circ \text{C}$ )

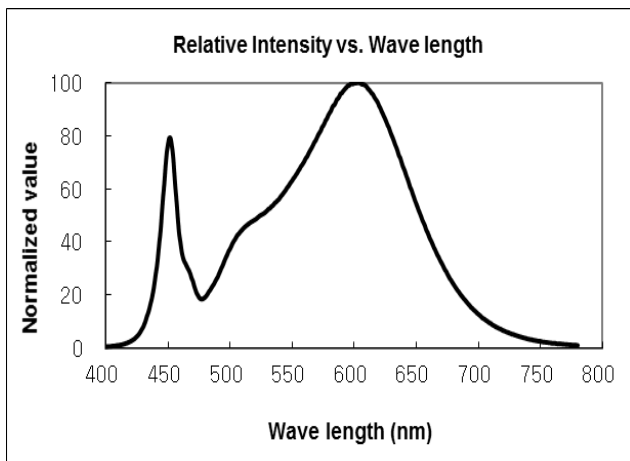
CCT : 2700K (80 CRI)



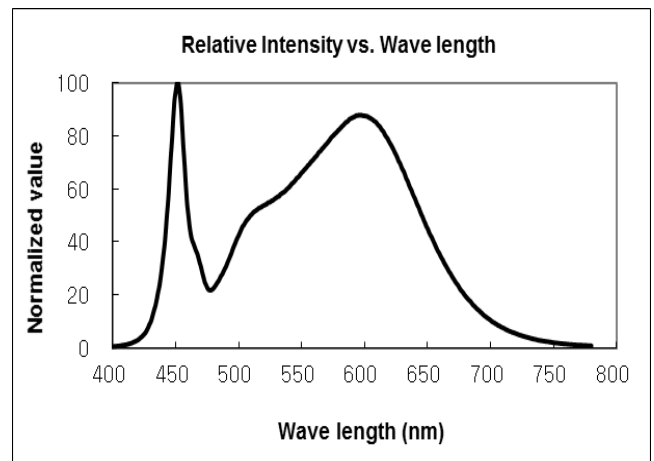
CCT : 3000K (80 CRI)



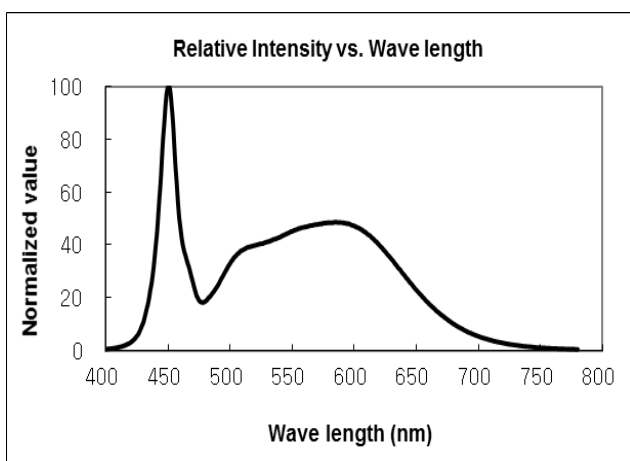
CCT : 3500K (80 CRI)



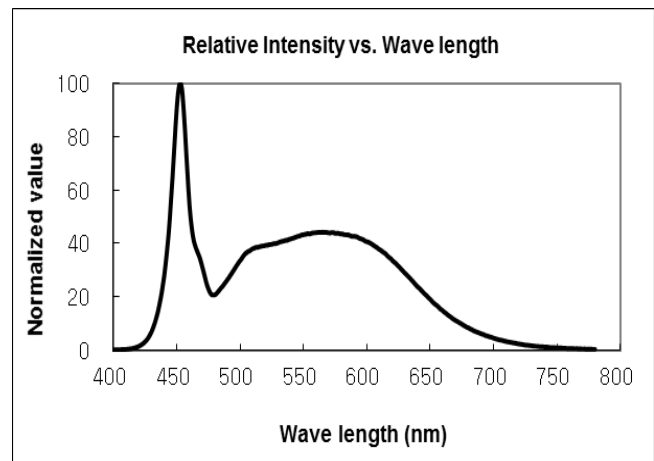
CCT : 4000K (80 CRI)



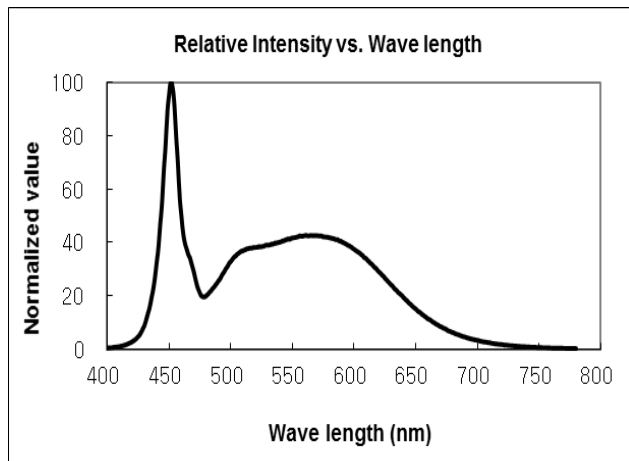
CCT : 5000K (80 CRI)



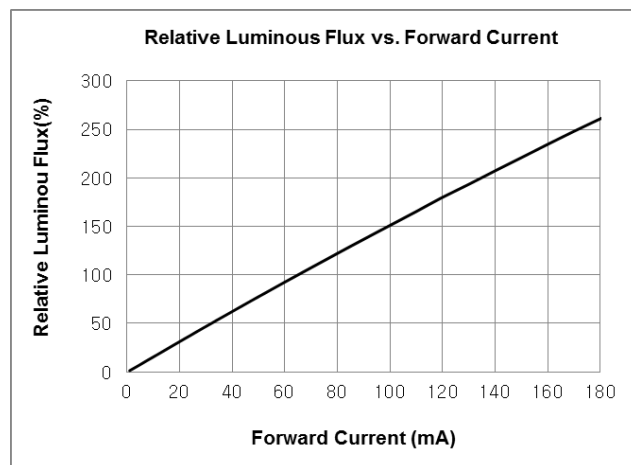
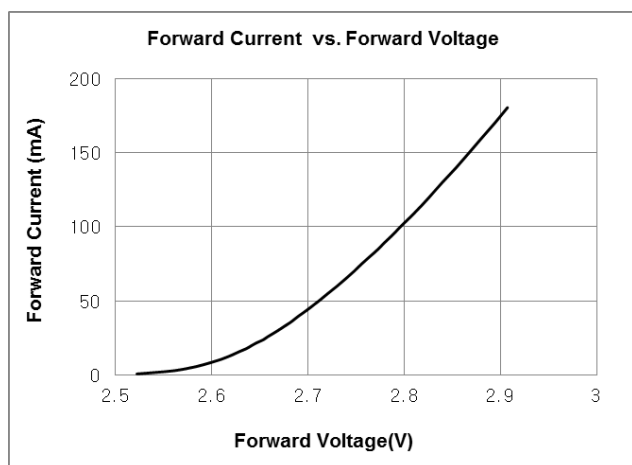
CCT : 5700K (80 CRI)



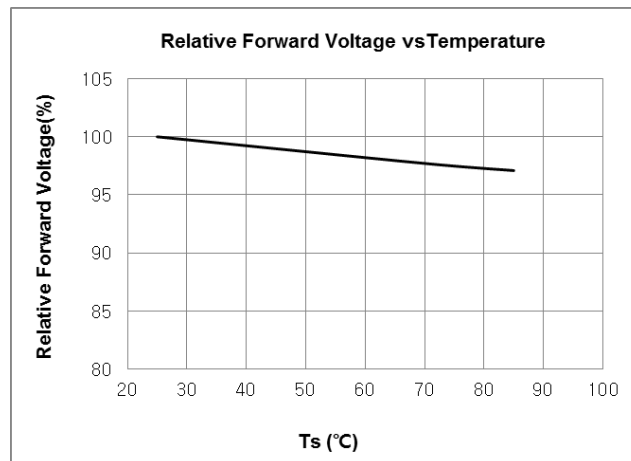
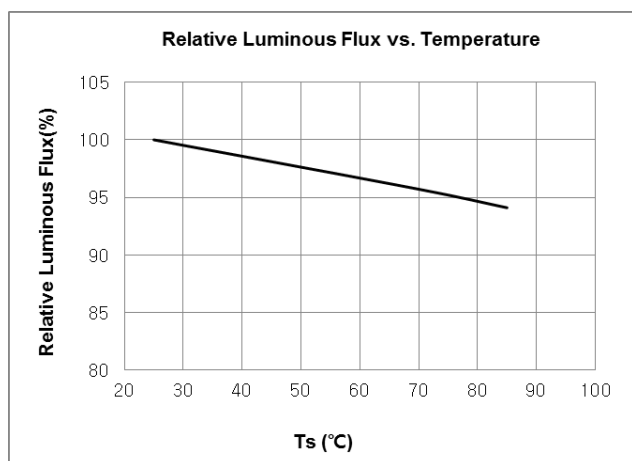
CCT : 6500K (80 CRI)



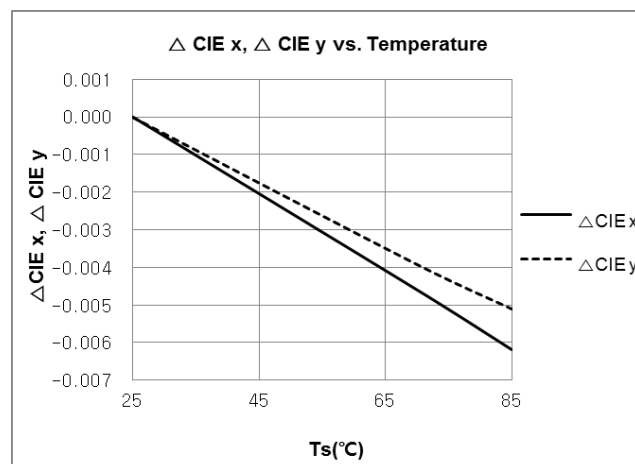
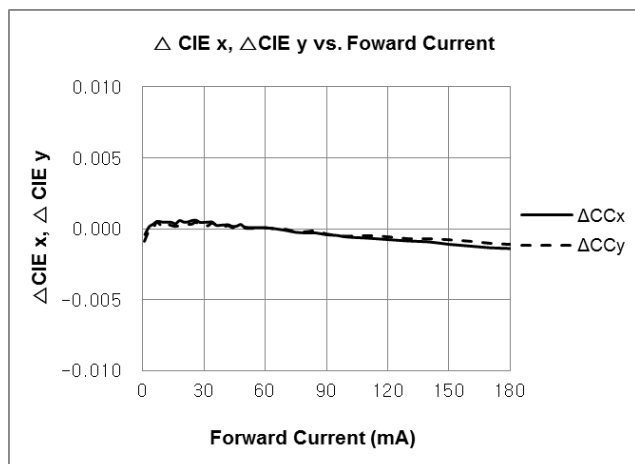
### b) Forward Current Characteristics ( $T_s = 25^\circ\text{C}$ )



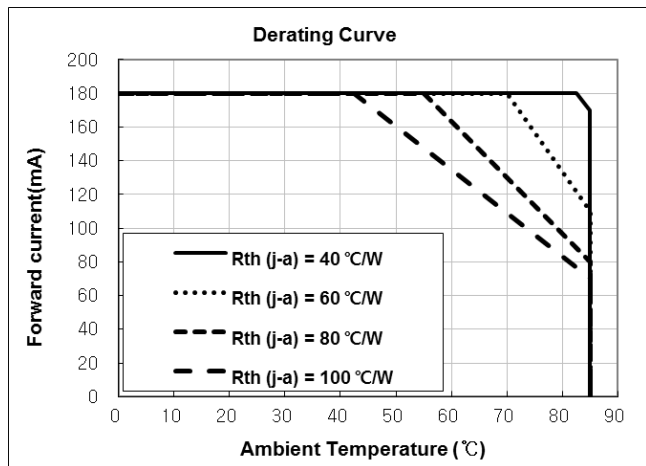
### c) Temperature Characteristics ( $I_f = 65\text{ mA}$ )



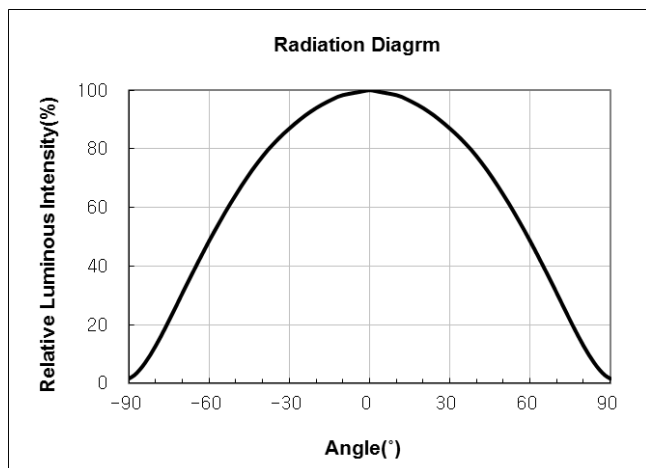
d) Color Shift Characteristics,  $T_s = 25^\circ\text{C}$ ,  $I_f = 65\text{ mA}$



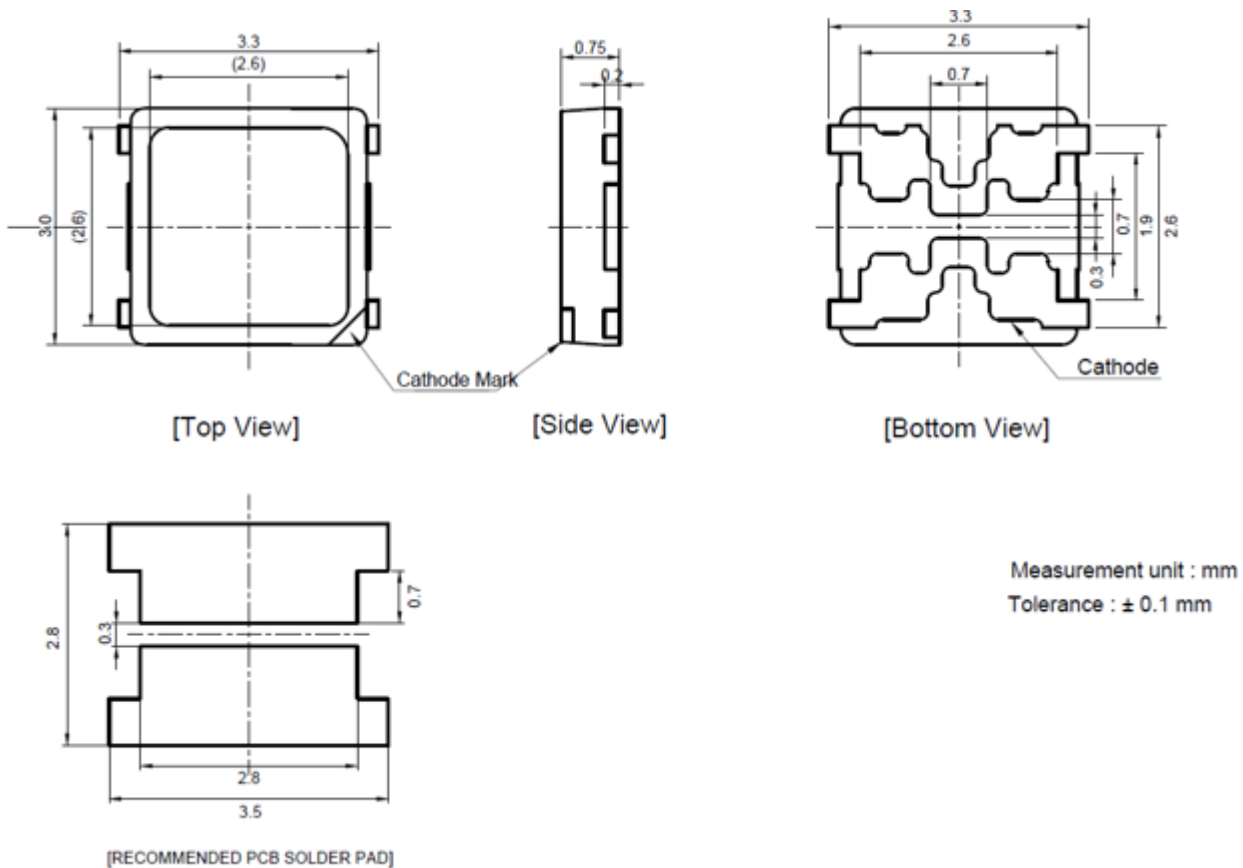
e) Derating Curve



f) Beam Angle Characteristics ( $T_s = 25^\circ\text{C}$ ,  $I_f = 65\text{ mA}$ )



#### 4. Outline Drawing & Dimension



#### Notes:

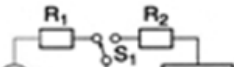
- 1) This LED has built-in ESD protection device(s) connected in parallel to LED chip(s).
- 2)  $T_s$  point and measurement method:
  - ① Measure one point at the cathode pad, if necessary remove PSR of PCB to reach  $T_s$  point.
  - ② All pads must be soldered to the PCB to dissipate heat properly, otherwise the LED can be damaged.

#### Precautions:

- 1) Pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid strong pressure on the LEDs. Do not put stress on the LEDs during heating.
- 2) Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.
- 3) Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

## 5. Reliability Test Items & Conditions

### a) Test Items

| Test Item                           | Test Condition                                                                                                                                                                | Test Hour / Cycle | Sample No. |
|-------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|------------|
| Room Temperature Life Test          | 25 °C, DC 180 mA                                                                                                                                                              | 1000 h            | 22         |
| High Temperature Life Test          | 85 °C, DC 180 mA                                                                                                                                                              | 1000 h            | 22         |
| High Temperature Humidity Life Test | 85 °C, 85 % RH, DC 180 mA                                                                                                                                                     | 1000 h            | 22         |
| Low Temperature Life Test           | -40 °C, DC 180 mA                                                                                                                                                             | 1000 h            | 22         |
| Powered Temperature Cycle Test      | -45 °C / 20 min ↔ 85 °C / 20 min, sweep 100 min<br>cycle on/off: each 5 min, DC 180 mA                                                                                        | 100 cycles        | 22         |
| Thermal Cycle                       | -45 °C / 15 min ↔ 125 °C / 15 min<br>→ Hot plate 180 °C                                                                                                                       | 500 cycles        | 100        |
| High Temperature Storage            | 120 °C                                                                                                                                                                        | 1000 h            | 11         |
| Low Temperature Storage             | -40 °C                                                                                                                                                                        | 1000 h            | 11         |
| ESD (HBM)                           |  <div> R<sub>1</sub>: 10 MΩ<br/> R<sub>2</sub>: 1.5 kΩ<br/> C: 100 pF<br/> V: ±5 kV </div> | 5 times           | 30         |
| ESD (MM)                            | <div> R<sub>1</sub>: 10 MΩ<br/> R<sub>2</sub>: 0<br/> C: 200 pF<br/> V: ±0.5 kV </div>                                                                                        | 5 times           | 30         |
| Vibration Test                      | 20~2000~20 Hz, 200 m/s <sup>2</sup> , sweep 4 min<br>X, Y, Z 3 direction, each 1 cycle                                                                                        | 4 cycles          | 11         |
| Mechanical Shock Test               | 1500 g, 0.5 ms<br>3 shocks each X-Y-Z axis                                                                                                                                    | 5 cycles          | 11         |

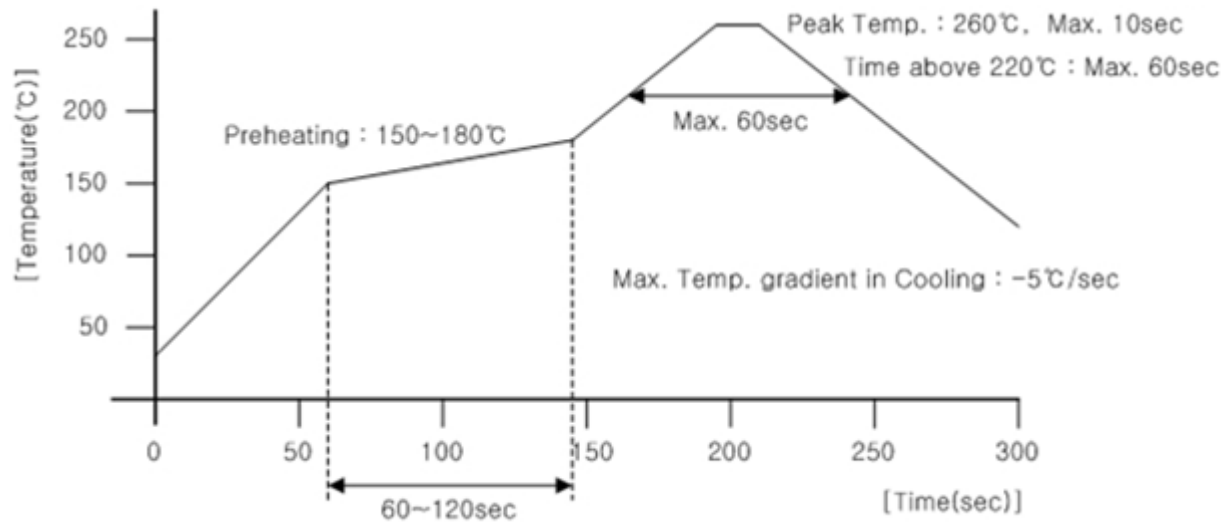
### b) Criteria for Judging the Damage

| Item            | Symbol         | Test Condition<br>(T <sub>s</sub> = 25 °C) | Limit             |                   |
|-----------------|----------------|--------------------------------------------|-------------------|-------------------|
|                 |                |                                            | Min               | Max               |
| Forward Voltage | V <sub>F</sub> | I <sub>F</sub> = 65 mA                     | Init. Value * 0.9 | Init. Value * 1.1 |
| Luminous Flux   | Φ <sub>v</sub> | I <sub>F</sub> = 65 mA                     | Init. Value * 0.7 | Init. Value * 1.1 |

## 6. Soldering Conditions

### a) Reflow Conditions (Pb free)

Reflow frequency: 2 times max.



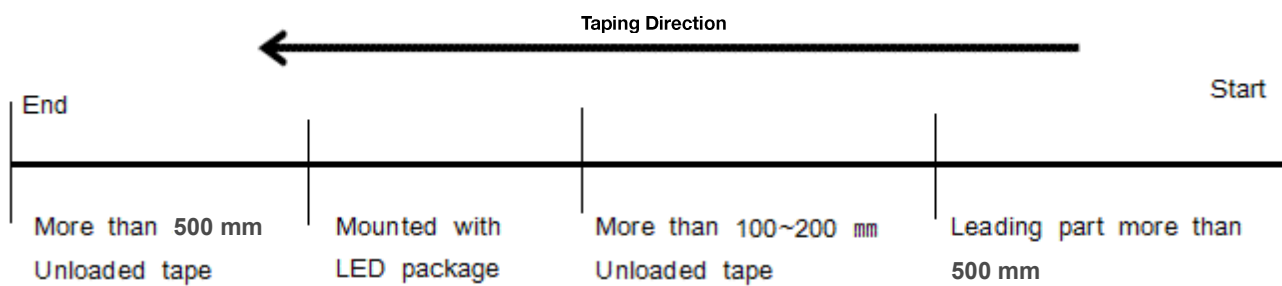
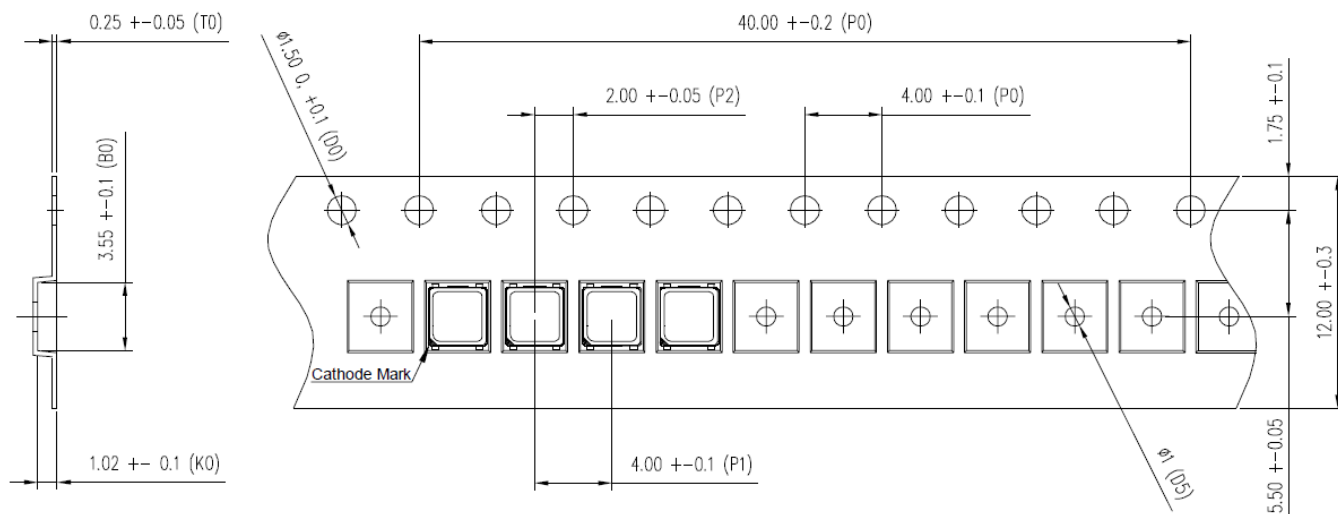
### b) Manual Soldering Conditions

Not more than 5 seconds @ max. 300 °C, under soldering iron.

## 7. Tape & Reel

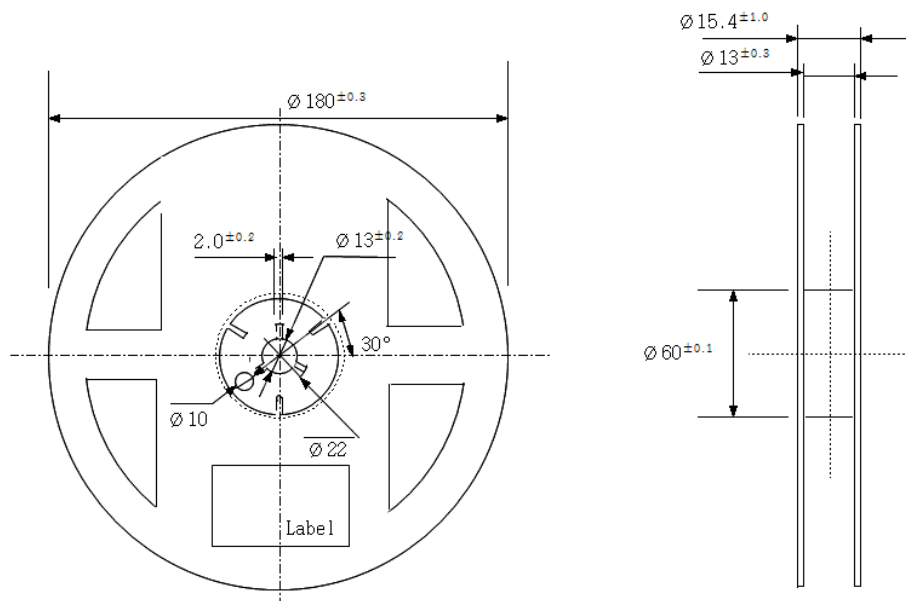
### a) Taping Dimension

(unit: mm)



## b) Reel Dimension

(unit: mm)

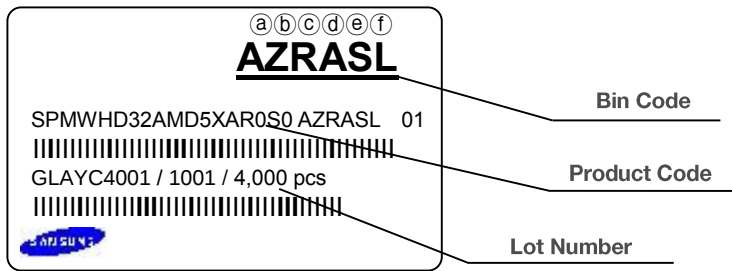


### Notes:

- 1) Quantity: The quantity/reel is 4,000 pcs
- 2) Cumulative Tolerance: Cumulative tolerance / 10 pitches is  $\pm 0.2$  mm
- 3) Adhesion Strength of Cover Tape: Adhesion strength is 0.1-0.7 N when the cover tape is turned off from the carrier tape at  $10^\circ$  angle to the carrier tape
- 4) Packaging: P/N, Manufacturing data code no. and quantity are indicated on the aluminum packing bag

## 8. Label Structure

### a) Label Structure



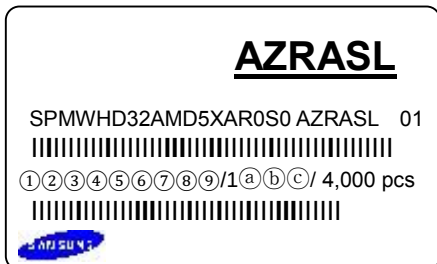
Note: Denoted bin code and product code above is only an example (see description on page 5)

Bin Code:

- ⒶⒷ: Forward Voltage bin (refer to page 8)
- ⒸⒹ: Chromaticity bin (refer to page 10-13)
- ⒺⒻ: Luminous Flux bin (refer to page 8)

### b) Lot Number

The lot number is composed of the following characters:



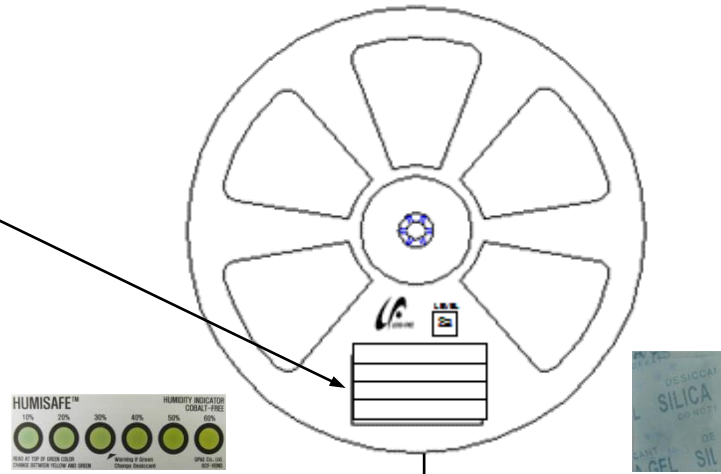
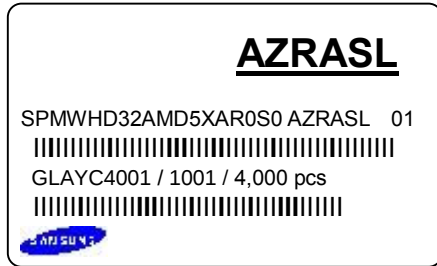
① ②③④⑤⑥⑦⑧⑨ / 1ⒶⒷⒸ / 4,000 pcs

- ① : Production site (S: Giheung, Korea, G: Tianjin, China)
- ② : L (LED)
- ③ : Product state (A: Normal, B: Bulk, C: First Production, R: Reproduction, S: Sample)
- ④ : Year (Z: 2015, A: 2016, B: 2017...)
- ⑤ : Month (1~9, A, B, C)
- ⑥⑦⑧⑨ : Day (1~9, A, B~V)
- ⒶⒷⒸ : Product serial number (001 ~ 999)

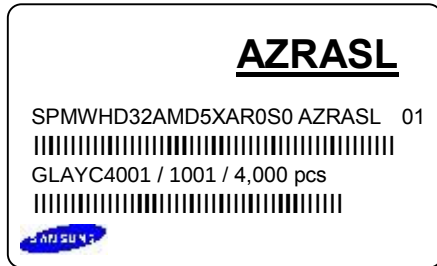
## 9. Packing Structure

### a) Packing Process (The quantity of PKG on the Reel to be Max 4,000pcs)

#### Reel



#### Aluminum Vinyl Packing Bag

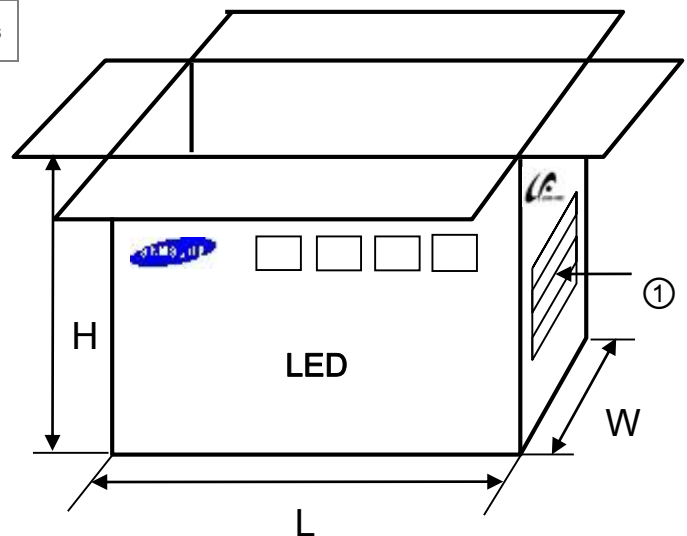
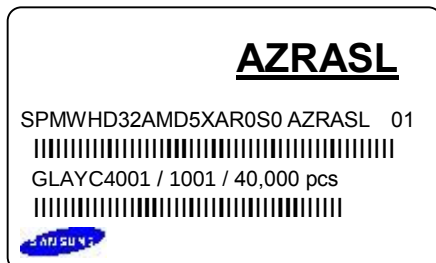


#### Outer Box

Material: Paper (SW3B(B))

| Type     | Size (mm) |         |         | Note           |
|----------|-----------|---------|---------|----------------|
|          | L         | W       | H       |                |
| 7 inch L | 245 ± 5   | 220 ± 5 | 182 ± 5 | Up to 10 reels |
| 7 inch S | 245 ± 5   | 220 ± 5 | 86 ± 5  | Up to 5 reels  |

#### ① Side Label



b) Packing Process for kitting (The quantity of PKG on the Reel to be Max 4,000pcs)

Reel

**Kitting 'A'**

**AY★DSK**

SPMWH32AMD5XA★SS0 AY★DSK 01

GLAW94001 / 1001 / 4,000 pcs

**Kitting 'B'**

**AY★FSK**

SPMWH32AMD5XA★SS0 AY★FSK 01

GLAW94001 / 1001 / 4,000 pcs

Aluminum Vinyl Packing Bag

**Kitting 'A'**

**AY★DSK**

SPMWH32AMD5XA★SS0 AY★DSK 01

GLAW94001 / 1001 / 4,000 pcs

**Kitting 'B'**

**AY★FSK**

SPMWH32AMD5XA★SS0 AY★FSK 01

GLAW94001 / 1001 / 4,000 pcs

Outer Box

**Kitting 'A'**

**AY★DSK**

SPMWH32AMD5XA★SS0 AY★DSK 01

GLAW94001 / 1001 / 40,000 pcs

[BOX Label]

**Kitting 'B'**

**AY★FSK**

SPMWH32AMD5XA★SS0 AY★FSK 01

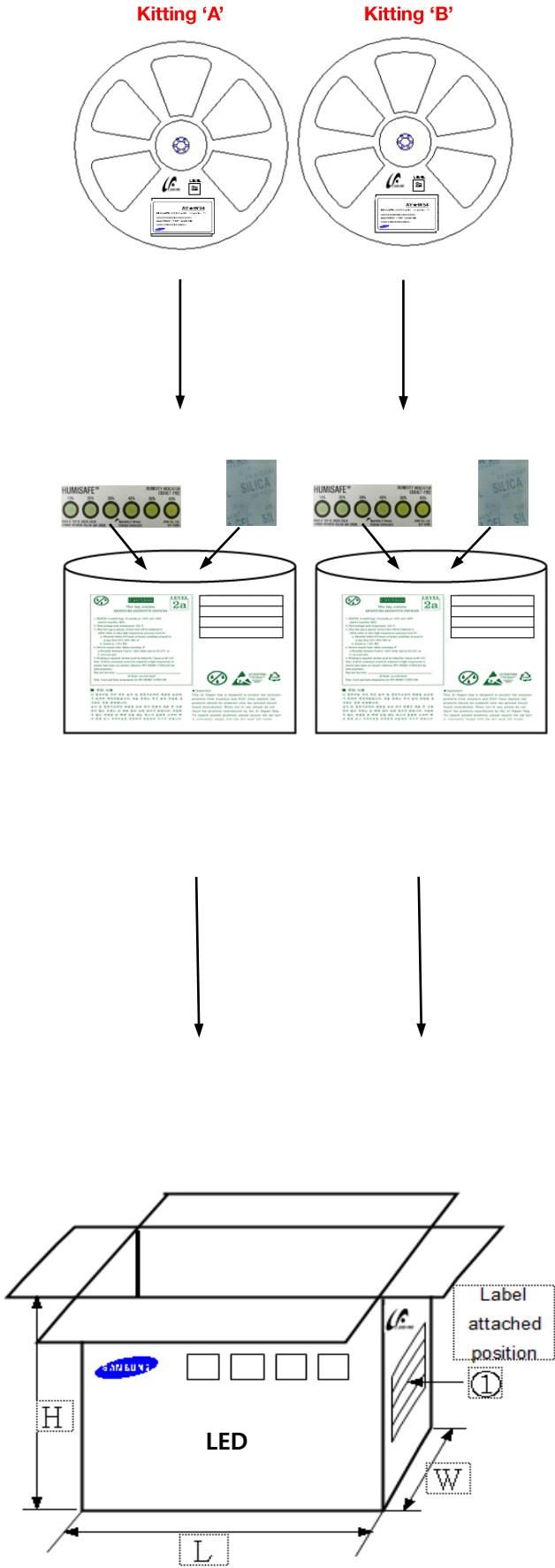
GLAW94001 / 1001 / 40,000 pcs

[BOX Label]

Note: "★" can be Nominal CCT code.

Material: Paper (SW3B(B))

| Type     | Size (mm) |         |         | Note           |
|----------|-----------|---------|---------|----------------|
|          | L         | W       | H       |                |
| 7 inch L | 245 ± 5   | 220 ± 5 | 182 ± 5 | Up to 10 reels |



## c) Aluminum Vinyl Packing Bag



**CAUTION**  
 This bag contains  
**MOISTURE SENSITIVE DEVICES**

**LEVEL**  
2a

1. Shelf life in sealed bag: 12 months at <40℃ and <90% relative humidity (RH)
2. Peak package body temperature: 240 ℃
3. After this bag is opened, devices that will be subjected to reflow solder or other high temperature processes must be:
  - a. Mounted within 672 hours at factory conditions of equal to or less than 30℃ /60% RH, or
  - b. Stored at < 10% RH
4. Devices require bake, before mounting, if:
  - a. Humidity Indicator Card is >60% when read at 23±5℃, or
  - b. 2a is not met.
5. If baking is required, devices must be baked for 10 ~ 24 hours at 60±5℃

Note: If device containers cannot be subjected to high temperature or shorter bake times are desired, reference IPC/JEDEC J-STD-033 for bake procedure,

Bag seal due date: \_\_\_\_\_  
 (If blank, see code label)

Note: Level and body temperature by IPC/JEDEC J-STD-020

**AZRASL**  
 SPMWHD32AMD5XAR0S0 AZRASL 01  
 |||||  
 GLAYC4001 / 1001 / 4,000 pcs  
 |||||  
 SAFETY







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■ Important

This Al Zipper bag is designed to protect the enclosed products from moisture and ESD. Once opened, the products should be soldered onto the printed circuit board immediately. When not in use, please do not leave the products unprotected by the Al Zipper Bag. To repack unused products., please ensure the zip-lock is completely sealed with the dry pack left inside.

## c) Silica Gel &amp; Humidity Indicator Card inside Aluminum Vinyl Bag



## 10. Precautions in Handling & Use

- 1) For over-current protection, users are recommended to apply resistors connected in series with the LEDs to mitigate sudden change of the forward current caused by shift of forward voltage.
- 2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When cleaning is required, IPA is recommended as the cleaning agent. Some solvent-based cleaning agent may damage the silicone resins used in the device.
- 3) When the device is in operation, the forward current should be carefully determined considering the maximum ambient temperature and corresponding junction temperature.
- 4) LEDs must be stored in a clean environment. If the LEDs are to be stored for three months or more after being shipped from Samsung, they should be packed with a nitrogen-filled container (shelf life of sealed bags is 12 months at temperature 0~40 °C, 0~90 % RH).
- 5) After storage bag is opened, device subjected to soldering, solder reflow, or other high temperature processes must be:
  - a. Mounted within 672 hours (28 days) at an assembly line with a condition of no more than 30 °C / 60 % RH<sup>\*Note 1</sup>, or
  - b. Mounted within 24 hours (1 day) at an assembly line with a condition of more than 30 °C / 70 % RH<sup>\*Note 2</sup>, or
  - c. Stored at <10 % RH.

\*Note 1, 2: IPC/JEDEC J-STD-033A, Recommended Equivalent Total Floor Life Table

| Package Type and Body Thickness | Moisture Sensitivity Level | Maximum Percent Relative Humidity |     |     |     |     |     | Temperature |
|---------------------------------|----------------------------|-----------------------------------|-----|-----|-----|-----|-----|-------------|
|                                 |                            | 40%                               | 50% | 60% | 70% | 80% | 90% |             |
| Body Thickness <2.1mm           | Level 2a                   | ∞                                 | ∞   | 28  | 1   | 1   | 1   | 30°C        |
|                                 |                            | ∞                                 | ∞   | ∞   | 2   | 1   | 1   | 25°C        |
|                                 |                            | ∞                                 | ∞   | ∞   | 2   | 2   | 1   | 20°C        |

- 6) Repack unused devices with anti-moisture packing, fold to close any opening and then store in a dry place.
- 7) Devices require baking before mounting, if humidity card reading is >60 % at 23 ± 5 °C.
- 8) Devices must be baked for 10~24 hours at 60 ± 5 °C, if baking is required.
- 9) The LEDs are sensitive to the static electricity and surge current. It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs. If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices. Damaged LEDs may show some unusual characteristics such as increase in leakage current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.
- 10) VOCs (Volatile Organic Compounds) can be generated from adhesives, flux, hardener or organic additives used in luminaires (fixtures). Transparent LED silicone encapsulant is permeable to those chemicals and they may lead to a discoloration of encapsulant when they exposed to heat or light. This phenomenon can cause a significant loss of light emitted (output) from the luminaires. In order to prevent these problems, we recommend users to know the physical properties of materials used in luminaires and they must be carefully selected.
- 11) Risk of sulfurization (or tarnishing)  
 The LED from Samsung uses a silver-plated lead frame and its surface color may change to black (or dark colored) when it is exposed to sulfur (S), chlorine (Cl) or other halogen compound. Sulfurization of lead frame may cause intensity degradation, change of chromaticity coordinates and, in extreme cases, open circuit. It requires caution. Due to possible sulfurization of lead frame, LED should not be used and stored together with oxidizing substances made of materials such as rubber, plain paper, lead solder cream, etc.

# Legal and additional information.

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