

Integral metal plane

for power or ground

(0,50 mm) .0197"

QSH SERIES

HIGH SPEED GROUND PLANE SOCKET

Blade &

Beam

Design

9 GHz / 18 Gbps

8 GHz / 16 Gbps

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?QSH

Insulator Material: Liquid Crystal Polymer
Contact Material: Phosphor Bronze Plating: Au or Sn over 50µ" (1,27 µm) Ni Current Rating:

Contact: 2 A per pin (1 pin powered per row) Ground Plane: 25 A per ground plane (1 ground plane powered)

Operating Temp Range: -55°C to +125°C Voltage Rating: 125 VAC (5 mm Stack Height) Max Cycles: 100 RoHS Compliant: Yes

Processing:

Lead-Free Solderable: Yes SMT Lead Coplanarity: (0,10 mm) .004" max (030-060) (0,15 mm) .006" max (090) Board Stacking: For applications requiring

more than two connectors per board contact ipg@samtec.com

Board Mates:

QTH/QSH

5 mm Stack Height

Single-Ended Signaling

Differential Pair Signaling

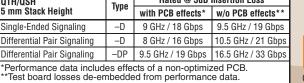
Differential Pair Signaling

Cable Mates: HQCD, HQDP (See Also Available note)





Protocols Supported 8 100 GbE 8 Hypertransport™ XAUI PCI Express® 8 SATA InfiniBand™ Download app notes at www.samtec.com/appnote Contact SIG @ samtec.com for questions on protocols



PINS PER ROW QSH NO. OF PAIRS

-D

-DP

Performance data for other stack heights and complete test data available at www.samtec.com?QSH or contact sig@samtec.com

01

PLATING OPTION

= Single

Ended

D-DP

= Differential

(–01 only)

MATED

HEIGHT

WITH QSH'

(5,00) .197

(8,00) .315

(11,00) .433

OTHER OPTION

RECOGNITIONS

For complete scope of recognitions see www.samtec.com/quality





ALSO AVAILABLE (MOQ Required)

- 14 mm, 15 mm, 22 mm and 30 mm stack height (Caution: Some automatic placement/inspection machines may have component height restrictions. Please consult machinery specifications.)
- 30μ" (0,76 μm) Gold (Specify -H plating for Data Rate cable mating applications.)
- Edge Mount & Guide Posts
- 80 (-DP), 120, 150 positions per row
- Retention Option Contact Samtec.

-030, -060, -090 (60 total pins per bank = -D)

-020, -040, -060 (20 pairs per bank = -D-DP)

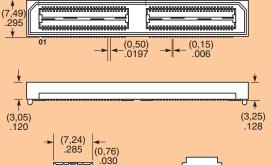
-D = (No. of Pins per Row/30) x

(20,00) .7875 + (1,27) .050

-DP = (No. of Pairs per Row/20) x

(20,00) .7875 + (1,27) .050

(20,00) .7875



_F = Gold Flash on Signal Pins and Ground Plane, Matte Tin on tails

= 10µ" (0,25 µm) Gold on Signal Pins and Ground Plane, Matte Tin on tails

 $-C^*$ = Electro-Polished Selective

50μ" (1,27 μm) min Au over 150μ" (3,81 μm) Ni on Signal Pins in contact area, 10μ" (0,25 μm) min Au over 50μ" (1,27 μm) Ni on Ground Plane in contact area, Matte Tin over 50µ" (1,27 µm) min Ni on all solder tails

-04 (16,00) .630 -05 (19,00) .748 -07 (25,00) .984 *Processing conditions will affect mated height.

QTH

LEAD

STYLE

-01

-02

-03

*Note: -C Plating passes 10 year MFG testing

Note: Some lengths, styles and options are non-standard.

-K = (8,25 mm) .325" DIA Polyimide Film Pick & Place Pad

-TR

= Tape & Reel (-090 positions maximum)

= Latching

Option (N/A on -060 (-D-DP) & -09 positions)

OTHER SOLUTIONS

 Board Spacing Standoffs. See SO Series

Due to technical progress, all designs, specifications and components are subject to change without notice.

WWW.SAMTEC.COM