

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

General Purpose Transistors

PNP Silicon

This transistor is designed for general purpose amplifier applications. It is housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

Features

• Pb-Free Package is Available

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	-40	Vdc
Collector-Base Voltage	V _{CBO}	-40	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	Ic	-200	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) @T _A = 25°C Derated above 25°C	P _D	200 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) @T _A = 25°C Derated above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	400	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

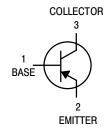
- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 × 1.0 Inch Pad



ON Semiconductor®

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GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT





CASE 463 SOT-416/SC-75 STYLE 1

MARKING DIAGRAM



2A = Device Code M = Date Code*

= Pb–Free Package

(Note: Microdot may be in either location)
*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

	Device	Package	Shipping _†
ſ	MMBT3906TT1	SOT-416	3000 / Tape & Reel
ľ	MMBT3906TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure. BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

	Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERIS	TICS	•		_	
Collector – Emitter Bre (I _C = –1.0 mAdc, I	eakdown Voltage (Note 3) _B = 0)	V _(BR) CEO	-40	_	Vdc
Collector – Base Brea (I _C = –10 μAdc, I _E		V _(BR) CBO	-40	_	Vdc
Emitter – Base Breako (I _E = –10 μAdc, I _C		V _{(BR)EBO}	-5.0	_	Vdc
Base Cutoff Current (V _{CE} = -30 Vdc, V	/ _{EB} = -3.0 Vdc)	I _{BL}	_	-50	nAdc
Collector Cutoff Curre (V _{CE} = -30 Vdc, V		I _{CEX}	_	-50	nAdc
ON CHARACTERIST	ICS (Note 3)				
DC Current Gain	$V_{CE} = -1.0 \text{ Vdc}$ $V_{CE} = -1.0 \text{ Vdc}$ $V_{CE} = -1.0 \text{ Vdc}$	h _{FE}	60 80 100 60 30	- 300 - -	_
Collector – Emitter Sa (I _C = -10 mAdc, I _E (I _C = -50 mAdc, I _E	$_{3} = -1.0 \text{ mAdc}$	V _{CE(sat)}	_ _	-0.25 -0.4	Vdc
Base – Emitter Satura $(I_C = -10 \text{ mAdc}, I_E (I_C = -50 \text{ mAdc}, I_E)$	$_{3} = -1.0 \text{ mAdc}$	V _{BE(sat)}	-0.65 -	-0.85 -0.95	Vdc
SMALL-SIGNAL CH	ARACTERISTICS				
Current – Gain – Band (I _C = –10 mAdc, V	dwidth Product $f'_{CE} = -20 \text{ Vdc, } f = 100 \text{ MHz}$	f _⊤	250	-	MHz
Output Capacitance (V _{CB} = -5.0 Vdc, I	_E = 0, f = 1.0 MHz)	C _{obo}	_	4.5	pF
Input Capacitance1 (V _{EB} = -0.5 Vdc, I	_C = 0, f = 1.0 MHz)	C _{ibo}	_	10.0	pF
Input Impedance (V _{CE} = -10 Vdc, I ₀	_C = −1.0 mAdc, f = 1.0 kHz)	h _{ie}	2.0	12	kΩ
Voltage Feedback Ra (V _{CE} = −10 Vdc, I ₀	tio $_{\text{C}} = -1.0 \text{ mAdc, f} = 1.0 \text{ kHz}$	h _{re}	0.1	10	X 10 ⁻⁴
Small – Signal Curren (V _{CE} = –10 Vdc, I ₀	t Gain _C = −1.0 mAdc, f = 1.0 kHz)	h _{fe}	100	400	_
Output Admittance (V _{CE} = -10 Vdc, I ₀	$_{\rm C} = -1.0 \text{ mAdc, f} = 1.0 \text{ kHz}$	h _{oe}	3.0	60	μmhos
Noise Figure (V _{CE} = -5.0 Vdc, I	I_C = -100 μAdc, R_S = 1.0 k Ω, f = 1.0 kHz)	NF	_	4.0	dB
SWITCHING CHARA	CTERISTICS				
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t _d	t _d – t _r –		ne
Rise Time	$(I_C = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t _r			ns
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_C = -10 \text{ mAdc})$	t _S	-	225	ns
Fall Time	$(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$	t _f	_	75	113

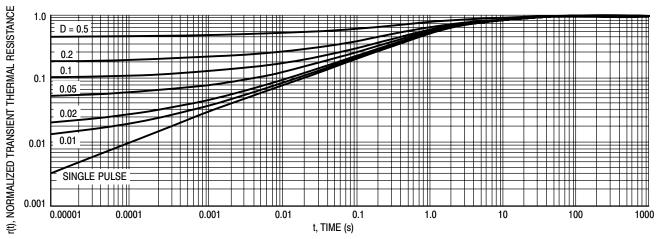
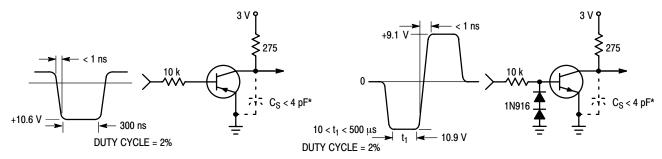


Figure 1. Normalized Thermal Response



* Total shunt capacitance of test jig and connectors

Figure 2. Delay and Rise Time Equivalent Test Circuit

Figure 3. Storage and Fall Time Equivalent Test Circuit

TYPICAL TRANSIENT CHARACTERISTICS

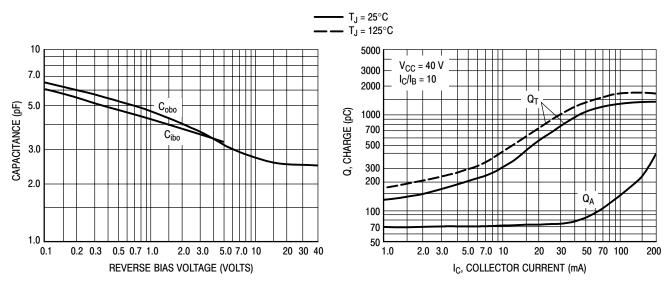
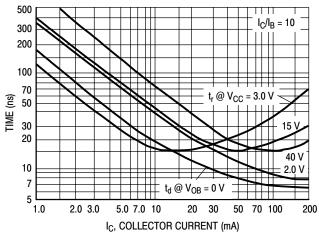


Figure 4. Capacitance

Figure 5. Charge Data



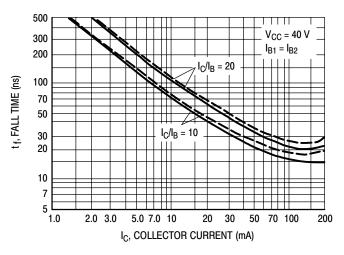
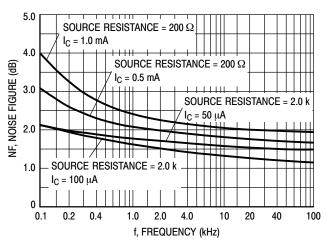


Figure 6. Turn-On Time

Figure 7. Fall Time

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

 $(V_{CE} = -5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth} = 1.0 \text{ Hz})$



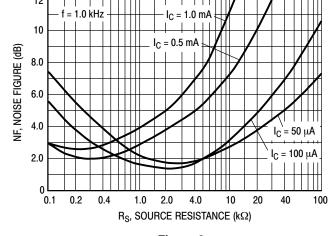
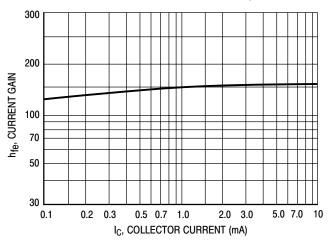


Figure 8.

Figure 9.

h PARAMETERS

 $(V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^{\circ}\text{C})$



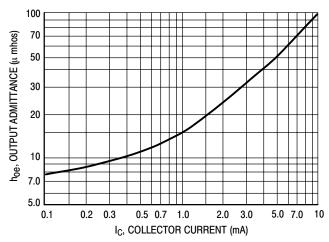
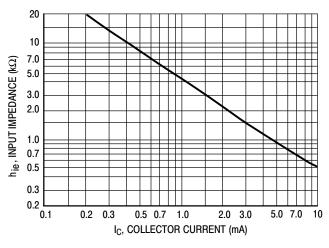


Figure 10. Current Gain

Figure 11. Output Admittance



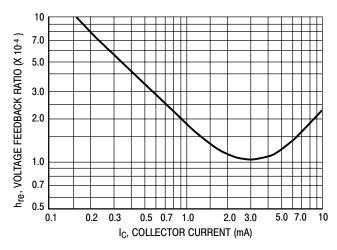


Figure 12. Input Impedance

Figure 13. Voltage Feedback Ratio

STATIC CHARACTERISTICS

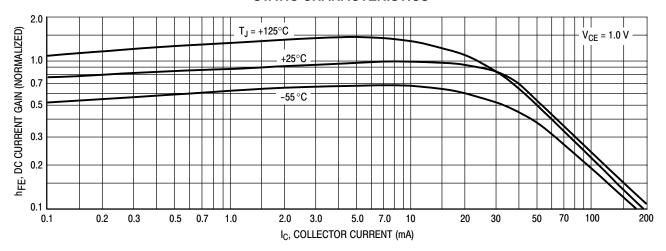


Figure 14. DC Current Gain

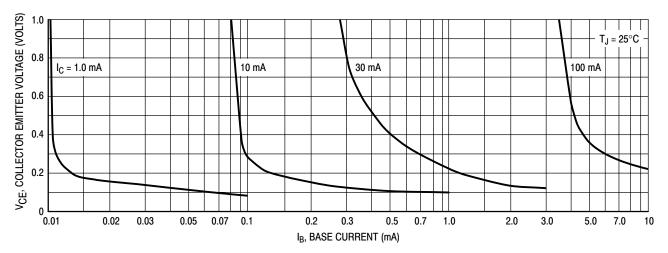
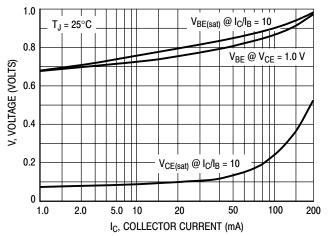


Figure 15. Collector Saturation Region

1.0



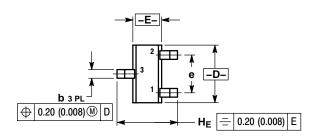
 $\theta_{V_{r}}$ TEMPERATURE COEFFICIENTS (mV/C) 0.5 θ_{VC} FOR V_{CE(sat)} +25°C TO +125°C -55 °C TO +25°C 0 -0.5 +25°C TO +125°C -1.0 θ_{VS} FOR $V_{BE(sat)}$ -55 °C TO +25°C -1.5 0 20 40 100 120 140 180 200 80 160 IC, COLLECTOR CURRENT (mA)

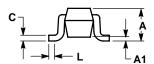
Figure 16. "ON" Voltages

Figure 17. Temperature Coefficients

PACKAGE DIMENSIONS

SC-75/SOT-416 CASE 463-01 ISSUE F



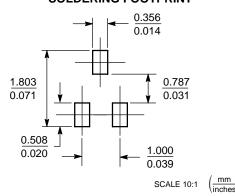


- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.059	0.063	0.067
Е	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC			C	0.04 BSC	
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.061	0.063	0.065

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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