

## **Rochester Electronics Manufactured Components**

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

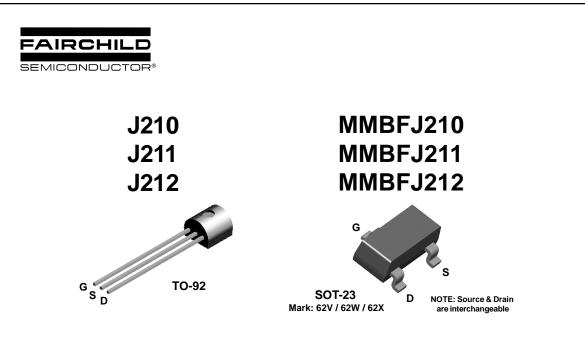
# **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
  - Class Q Military
  - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)

• Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



## **N-Channel RF Amplifier**

This device is designed for HF/VHF mixer/amplifier and applications where Process 50 is not adequate. Sufficient gain and low noise for sensitive receivers. Sourced from Process 90.

### Absolute Maximum Ratings\* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>DG</sub>	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	- 25	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> ,T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## **Thermal Characteristics**

Symbol	Characteristic		Max	Units	
		J210-212	*MMBFJ210-212		
PD	Total Device Dissipation	350	225	mW	
	Derate above 25°C	2.8	1.8	mW/°C	
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case	125		°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W	

TA = 25°C unless otherwise noted

\*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

# N-Channel RF Amplifier

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHAF	RACTERISTICS				
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_G = 1.0 \ \mu A, \ V_{DS} = 0$	- 25		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = 15 V, V_{DS} = 0$		- 100	pА
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.0 nA 210 211 212	-1.0 - 2.5 - 4.0	-3.0 - 4.5 - 6.0	V V V
ON CHAR	ACTERISTICS				
IDSS	Zero-Gate Voltage Drain Current*	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 210 211 212	2.0 7.0 15	15 20 40	m A m A m A
gfs	GNAL CHARACTERISTICS Common Source Forward Transconductance	$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 kHz$ 210 211 212	4000 6000 7000	12,000 12,000 12,000	μmho μmho μmho
goss *Pulse Test: Puls	Common Source Output Conductance se Width ≤ 300 µS	$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 \text{ kHz}$		200	
*Pulse Test: Puls	Conductance se Width ≤ 300 μS al Characteristics Parameter Interactions	Com	non Drai	200	μmhos
*PulseTest: Puls <b>Typica</b> 100	Conductance se Width ≤ 300 µS Al Characteristics Parameter Interactions	50 Com		200	μmho: : <b>e</b>
*Pulse Test: Pulse <b>Typica</b> 100	Conductance width ≤ 300 $\mu$ S al Characteristics Parameter Interactions $g_{fs}$ , $I_{DSS} @ V_{DS} = 10V$ , $V_{GS} = 0$ PULSED $r_{DS} @ V_{DS} = 100$ mV, $V_{GS} = 0$	50 Com		200	μmhos :e = 0V
*Pulse Test: Pulse <b>Typica</b> 100	$\label{eq:sewidth} \underbrace{ \text{Conductance}}_{\text{Sewidth} \leq 300\mu\text{S}} \\ \hline \textbf{al Characteristics} \\ \hline \textbf{Parameter Interactions} \\ \hline \textbf{g}_{\text{fs}}, \textbf{I}_{\text{DSS}} @ \textbf{V}_{\text{DS}} = 100, \textbf{V}_{\text{GS}} = 0 \\ \hline \textbf{V}_{\text{GS}} (\textbf{OFF}) @ \textbf{V}_{\text{DS}} = 100, \textbf{V}_{\text{GS}} = 0 \\ \hline \textbf{V}_{\text{GS}} (\textbf{OFF}) @ \textbf{V}_{\text{DS}} = 100, \textbf{V}_{\text{D}} = 1.0 \\ \hline \textbf{nA} \\ \hline$	50 Com		200	μmhos :e = 0V
*Pulse Test: Pulse <b>Typica</b> 100	$\label{eq:sewidth} \underbrace{ \text{Conductance} } \\ \text{Sewidth} \le 300  \mu \text{S} \\ \hline \textbf{Al Characteristics} \\ \hline \textbf{Parameter Interactions} \\ \hline \textbf{g}_{\text{fs}}, \textbf{I}_{\text{DSS}} @ \textbf{V}_{\text{DS}} = 10 \text{V}, \textbf{V}_{\text{GS}} = 0 \text{PULSED} \\ \hline \textbf{r}_{\text{DS}} @ \textbf{V}_{\text{DS}} = 100 \text{ mV}, \textbf{V}_{\text{GS}} = 0 \\ \hline \textbf{V}_{\text{GS}(\text{OFF})} @ \textbf{V}_{\text{DS}} = 10 \text{V}, \textbf{I}_{\text{D}} = 1.0 \text{ nA} \\ \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} \\ \hline \textbf{I}_{\text{DSS}} & \hline \textbf{I}_{\text{DSS}} & \hline \textbf$	50 Com	OFF) = -4.51	200	μmhos :e = 0V
*Pulse Test: Pulse <b>Typica</b> 100	$\begin{tabular}{l l l l l l l l l l l l l l l l l l l $	50 Com	OFF) = -4.51	in-Sourc	μmho:
*Pulse Test: Pulse <b>Typica</b> 100	$\frac{Conductance}{Se Width ≤ 300 \mu S}$ al Characteristics Parameter Interactions $\frac{9_{fs}, I_{DSS} @ V_{DS} = 10V, V_{GS} = 0 PULSED}{V_{GS} (0 FF) @ V_{DS} = 100 mV, V_{GS} = 0}$ $\frac{V_{GS} (0 FF) @ V_{DS} = 10V, I_{D} = 1.0 nA}{I_{DSS}}$	50 Com	OFF) = -4.5V -1 -2.0V -2.5V	in-Sourc	μmho :e = 0V
*Pulse Test: Pulse <b>Typica</b> 100	$\frac{Conductance}{Se Width ≤ 300 \mu S}$ al Characteristics Parameter Interactions $\frac{9_{fs}, I_{DSS} @ V_{DS} = 10V, V_{GS} = 0 PULSED}{V_{GS} (0 FF) @ V_{DS} = 100 mV, V_{GS} = 0}$ $\frac{V_{GS} (0 FF) @ V_{DS} = 10V, I_{D} = 1.0 nA}{I_{DSS}}$	50 Com	OFF) = -4.5V -1 -2.0V -2.5V	in-Sourc	μmho:
CURRENT (mA) *Pulse Test: Puls Typica 100 200 200 200 200 200 200 200 200 200	$\frac{Conductance}{Se Width ≤ 300 \mu S}$ al Characteristics Parameter Interactions $\frac{9_{fs}, I_{DSS} @ V_{DS} = 10V, V_{GS} = 0 PULSED}{V_{GS} (0 FF) @ V_{DS} = 100 mV, V_{GS} = 0}$ $\frac{V_{GS} (0 FF) @ V_{DS} = 10V, I_{D} = 1.0 nA}{I_{DSS}}$	1000 1000 50 T <sub>A</sub> = 25°0 TYP V <sub>GS</sub> 500 TYP V <sub>GS</sub> 1000 100 100 100 100 100 100 1	OFF) = -4.5V -1 -2.0V -2.5V	in-Sourc	μmho:

-5.0 -10

-1.0

 $V_{GS(OFF)}$  – GATE CUTOFF VOLTAGE (V)

-0.1

0

1.0

2.0

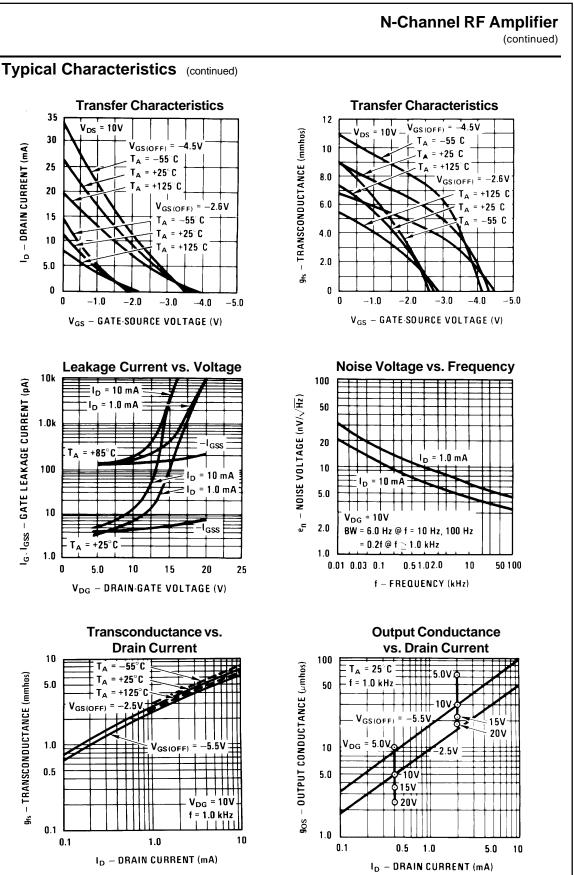
 $V_{DS}$  – DRAIN-SOURCE VOLTAGE (V)

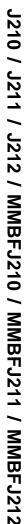
3.0

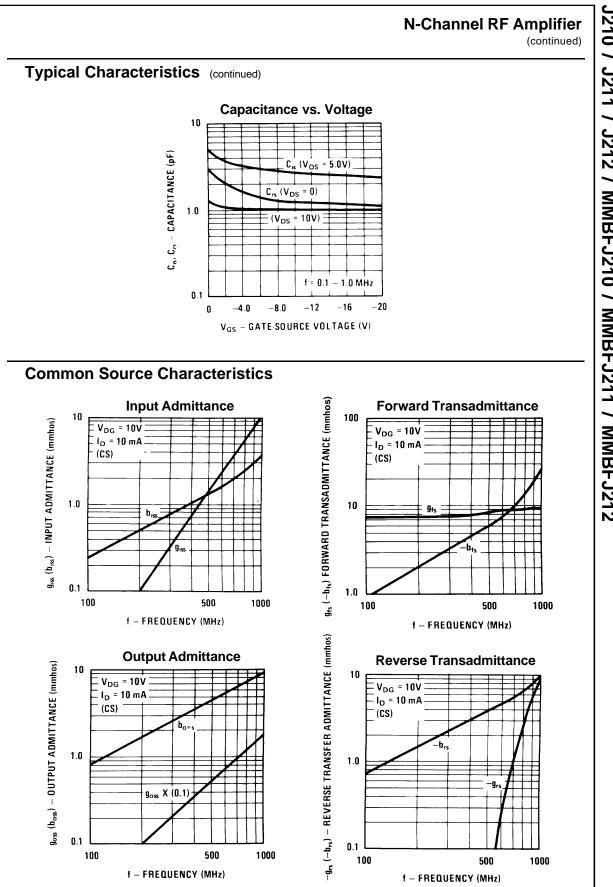
4.0

5.0

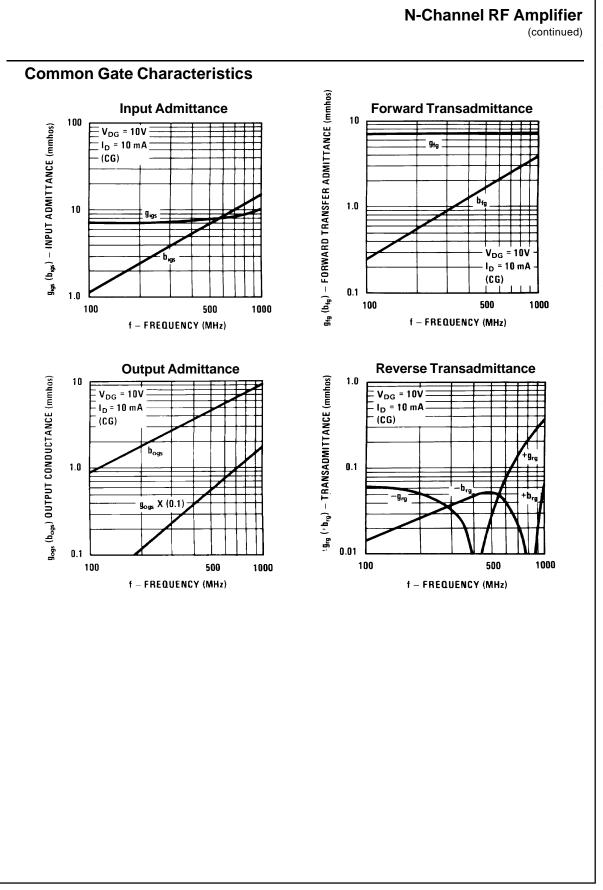
J210 / J211 / J212 / MMBFJ210 / MMBFJ211 / MMBFJ212







J210 / J211 / J212 / MMBFJ210 / MMBFJ211 / MMBFJ212



J210 / J211 / J212 / MMBFJ210 / MMBFJ211 / MMBFJ212

#### TRADEMARKS The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks. FAST<sup>®</sup> ACEx™ **OPTOPLANAR™** SuperSOT<sup>™</sup>-3 FASTr™ PACMAN™ SuperSOT<sup>™</sup>-6 Bottomless™ POP™ CoolFET™ FRFET™ SuperSOT<sup>™</sup>-8 CROSSVOLT™ SyncFET™ GlobalOptoisolator<sup>™</sup> PowerTrench<sup>®</sup> GTO™ TinyLogic™ DenseTrench™ QFET™ UHC™ HiSeC™ QS™ DOME™ **EcoSPARK**<sup>™</sup> **ISOPLANAR™** QT Optoelectronics<sup>™</sup> UltraFET<sup>®</sup> VCX™ E<sup>2</sup>CMOS<sup>™</sup> LittleFET™ Quiet Series<sup>™</sup> SILENT SWITCHER® EnSigna™ MicroFET™ FACT™ MICROWIRE™ SMART START™ Stealth™ OPTOLOGIC™ FACT Quiet Series™

### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user. 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### **PRODUCT STATUS DEFINITIONS**

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.
	•	Rev. H2