

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



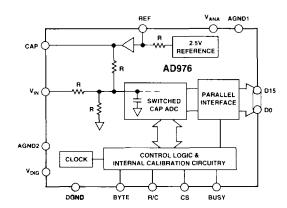
16-Bit, 200 kSPS BiCMOS A/D Converter

AD976

FEATURES

Fast 16-Bit ADC with 200 kSPS Throughput Single 5 V Supply Operation Input Range: ±10 V 100 mW Max Power Dissipation Choice of External or Internal 2.5 V Reference High Speed Parallel Interface On-Chip Clock 28-Pin Skinny DIP, SOIC or SSOP Packages

FUNCTIONAL BLOCK DIAGRAM



GENERAL DESCRIPTION

The AD976 is a high speed, low power 16-bit A/D converter that operates from a single 5 V supply. The part contains a successive approximation, switched capacitor ADC, an internal 2.5 V reference, and a high speed parallel interface. The ADC is factory calibrated to minimize all linearity errors. The analog full scale input is the standard industrial range of ±10 V.

The AD976 is comprehensively tested for ac parameters such as SNR and THD, as well as the more traditional parameters of offset, gain, and linearity.

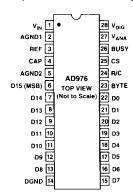
The AD976 is fabricated on Analog Devices' BiCMOS process which has high performance bipolar devices along with CMOS transitors.

The AD976 is available in skinny 28-pin DIP, SOIC, and SSOP packages.

PRODUCT HIGHLIGHTS

- Fast 200 kSPS Throughput.
 The AD976 is a high speed, 16-bit ADC based on aswitched capacitor architecture which is factory calibrated.
- Single-Supply Operation.
 The AD976 operates from a single 5 V supply and dissipates only 100 mW max.
- Comprehensive DC and AC Specifications.As well as the traditional specifications of offset, gain, and linearity, the AD976 is fully tested for SNR and THD.
- Complete A/D Solution.
 The AD976 offers a highly integrated solution containing an accurate A/D, reference and on-chip clock.

PIN CONFIGURATION DIP, SOIC and SSOP Packages



This information applies to a product under development. Its characteristics and specifications are subject to change without notice. Analog Devices assumes no obligation regarding future manufacture unless otherwise agreed to in writing.

To obtain the most recent version or complete data sheet, call our fax retrieval system at 1-800-446-6212 or visit our World Wide Web site at http://www.analog.com.

AD976—SPECIFICATIONS $(T_{MIN}$ to T_{MAX} , $F_{S}=200$ kHz, $V_{DIG}=+5$ V \pm 10%, $V_{AMA}=+5$ V \pm 5% unless otherwise noted)

Parameter	Conditions		AD976A		AD976			
		Min	Typ	Max	Min	Typ	Max	Units
RESOLUTION		16			16			Bits
ANALOG INPUT Voltage Range Impedance Capacitance	:		±10 23 35			+10		V kΩ pF
HROUGHPUT SPEED Conversion Time Complete Cycle Throughput Rate		200	3.2	3.5 5	200	3.2	3.5 5	μs μs kHz
DC ACCURACY Integral Linearity Error Differential Linearity Error No Missing Codes Transition Noise ² Full-Scale Error ^{3, 4} Full-Scale Error Drift Full-Scale Error Full-Scale Error Drift Bipolar Zero Error ³ Bipolar Zero Error Drift Power Supply Sensitivity V _{ANA} = V _{DIG} = V _D V _{DIG} = 5 V ± 10%	Ext REF = 2.5 V Ext REF = 2.5 V $V_D = 5 \text{ V} \pm 5\%$ $V_{ANA} = 5 \text{ V}$	15	1.3 ±7 +2 ±2	±3 +3, 2 ±0.50 +0.50 +10 ±8	16	1.3 +5 ±2 :2	±1.5 +1.5, 1 ±0.25 +0.25 ±10	LSB ¹ LSB Bits LSB % ppm//0 ppm//0 mV ppm//0 LSB LSB
AC ACCURACY Spurious Free Dynamic Range Total Harmonic Distortion Signal-to-(Noise+Distortion) Signal-to-Noise Full Power Bandwidth	$f_{1N} = 45 \text{ kHz}$ $f_{1N} = 45 \text{ kHz}$ $f_{1N} = 45 \text{ kHz}$ 60 dB Input $f_{1N} = 45 \text{ kHz}$	90 83 83	30 250	90	96 86 86	32 250	96	dB ⁵ dB dB
SAMPLING DYNAMICS Aperture Delay Aperture Jitter Transient Response Over Voltage Recovery ⁷	Full-Scale Step		40 S	ufficient to M	teet AC	40 Specs 150	2	ns µs ns
REFERENCE Internal Reference Voltage Internal Reference Source Current External Reference Voltage Range for Specified Linearity		2.48	2.5 1 2.5	2.52	2.48	2.5	2.52	V μA V
External Reference Current Drain	Ext REF = 2.5 V	٠. ٢	2.9	100	۵.)	2.5	100	ν μA
DIGITAL INPUTS Logic Levels V _{II} V _{III} I _{II}		0.3 +2.0		+0.8 V _{DIG} + 0.3 ±10 ±10	0.3		+0.8 V _{DIG} + 0.3 ±10 ±10	V V μA μA

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