

## **Rochester Electronics Manufactured Components**

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

## **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
  - Class Q Military
  - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
  - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



# AD976—SPECIFICATIONS ( $T_{MIN}$ to $T_{MAX}$ , $F_S = 200$ kHz, $V_{DIG} = +5$ V $\pm$ 10%, $V_{ANA} = +5$ V $\pm$ 5% unless otherwise noted)

Parameter	Conditions	AD976A			AD976B			Units
		Min	Typ	Max	Min	Typ	Max	
RESOLUTION		16			16			Bits
ANALOG INPUT								
Voltage Range			+10			+10		V
Impedance			23			23		k $\Omega$
Capacitance			35			35		pF
THROUGHPUT SPEED								
Conversion Time			3.2	3.5		3.2	3.5	$\mu$ s
Complete Cycle				5			5	$\mu$ s
Throughput Rate		200			200			kHz
DC ACCURACY								
Integral Linearity Error				$\pm 3$			$\pm 1.5$	LSB <sup>1</sup>
Differential Linearity Error				$+3, -2$			$\pm 1.5, -1$	LSB
No Missing Codes		15			16			Bits
Transition Noise <sup>2</sup>			1.3			1.3		LSB
Full-Scale Error <sup>3,4</sup>				$\pm 0.50$			$\pm 0.25$	%
Full-Scale Error Drift			$\pm 7$			$\pm 5$		ppm/ $^{\circ}$ C
Full-Scale Error	Ext REF = 2.5 V			$+0.50$			$+0.25$	%
Full-Scale Error Drift	Ext REF = 2.5 V		$\pm 2$			$\pm 2$		ppm/ $^{\circ}$ C
Bipolar Zero Error <sup>5</sup>				$\pm 10$			$\pm 10$	mV
Bipolar Zero Error Drift			$\pm 2$			$\pm 2$		ppm/ $^{\circ}$ C
Power Supply Sensitivity								
$V_{ANA} = V_{DIG} = V_{DD}$	$V_{DD} = 5$ V $\pm$ 5%			$\pm 8$			$\pm 8$	LSB
$V_{DIG} = 5$ V $\pm$ 10%	$V_{ANA} = 5$ V		TBD			TBD		LSB
AC ACCURACY								
Spurious Free Dynamic Range	$f_{IN} = 45$ kHz	90			96			dB <sup>6</sup>
Total Harmonic Distortion	$f_{IN} = 45$ kHz			90			96	dB
Signal-to-(Noise+Distortion)	$f_{IN} = 45$ kHz	83			86			dB
	60 dB Input		30			32		
Signal-to-Noise	$f_{IN} = 45$ kHz	83			86			dB
Full Power Bandwidth <sup>8</sup>			250			250		kHz
SAMPLING DYNAMICS								
Aperture Delay			40			40		ns
Aperture Jitter			Sufficient to Meet AC Specs					
Transient Response	Full-Scale Step			2			2	$\mu$ s
Over Voltage Recovery <sup>7</sup>			150			150		ns
REFERENCE								
Internal Reference Voltage		2.48	2.5	2.52	2.48	2.5	2.52	V
Internal Reference Source Current			1			1		$\mu$ A
External Reference Voltage Range for Specified Linearity		2.3	2.5	2.7	2.3	2.5	2.7	V
External Reference Current Drain	Ext REF = 2.5 V			100			100	$\mu$ A
DIGITAL INPUTS								
Logic Levels								
$V_{II}$		0.3		$+0.8$	0.3		$+0.8$	V
$V_{IH}$		$+2.0$		$V_{DIG} + 0.3$	$+2.0$		$V_{DIG} + 0.3$	V
$I_{II}$				$\pm 10$			$\pm 10$	$\mu$ A
$I_{IH}$				$\pm 10$			$\pm 10$	$\mu$ A

Specifications subject to change without notice.

This information applies to a product under development. Its characteristics and specifications are subject to change without notice. Analog Devices assumes no obligation regarding future manufacture unless otherwise agreed to in writing.