

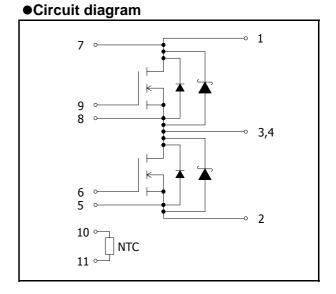
BSM400D12P2G003

Application

- · Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

Features

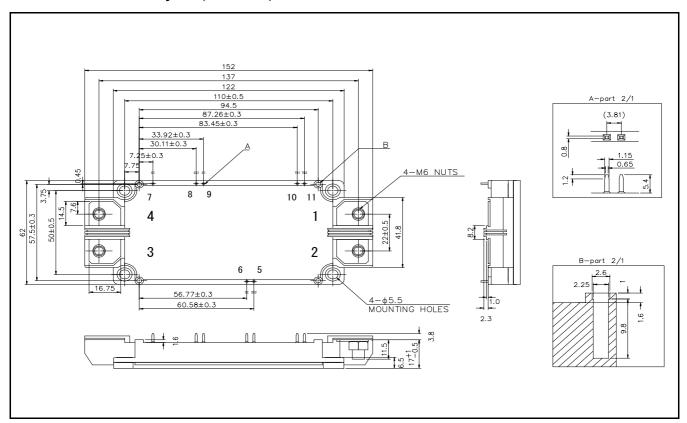
- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.



Construction

This product is a half bridge module consisting of SiC-DMOSFET and SiC-SBD from ROHM.

● Dimensions & Pin layout (Unit : mm)

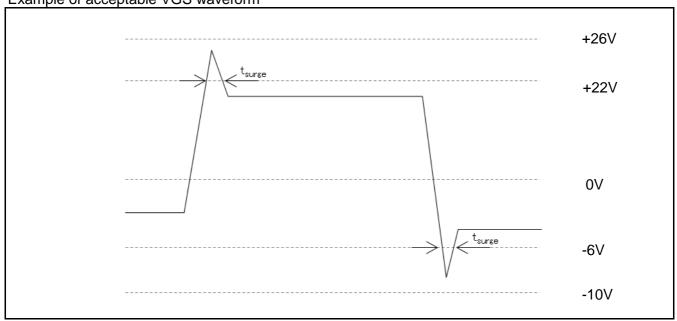


● Absolute maximum ratings (T_j = 25°C)

Parameter	Symbol	Conditions	Ratings	Unit	
Drain - Source Voltage	V_{DSS}	G-S short	1200		
Gate - Source Voltage (+)	V_{GSS}	D-S short	22	V	
Gate - Source Voltage (-)	V_{GSS}	D-S short	-6	\ \ \	
G - S Voltage (t _{surge} <300nsec)	V _{GSSsurge}	V _{GSSsurge} D-S short		7	
Drain Current Note 1)	I _D	DC(Tc=60°C) VGS=18V	397		
	I _D	DC(Tc=59°C) VGS=18V	400		
	I _{DRM}	Pulse (Tc = 60°C) 1ms VGS=18V Note 2)	800	A	
Source Current Note 1)	I _S	DC(Tc=60°C) VGS=18V	418		
	I _S	I_S DC(Tc=60°C) VGS=0V I_{SRM} Pulse (Tc = 60°C) 1ms VGS=18V $_{Note\ 2)}$			
	I _{SRM}				
	I _{SRM}	Pulse (Tc = 60°C) 1ms VGS=0V Note 2)	800		
Total Power Dissipation Note 3)	Ptot	Tc = 25°C	2450	W	
Max Junction Temperature	Tjmax		175		
Junction Temperature	unction Temperature Tjop		-40 to 150	°C	
Storage Temperature Tstg			-40 to 125		
Isolation Voltage	Visol	Terminals to baseplate f = 60Hz AC 1 min.	2500	Vrms	
Mounting Torque		Main Terminals : M6 screw	4.5	N.m	
Mounting Torque	-	Mounting to heat sink M5 screw	3.5	N·m	

- Note 1) Case temperature (Tc) is defined on the surface of base plate just under the chips.
- Note 2) Repetition rate should be kept within the range where temperature rise if die should not exceed Timax.
- Note 3) Tj is less than 175°C.

Example of acceptable VGS waveform



●Electrical characteristics (T_i=25°C)

Parameter	Symbol	Conditions			Ratings Min. Typ. Max.		
1 arameter Symbo		Conditions			Тур.	Max.	Unit
On-state static			Tj=25°C	_	2.3	3.2	V
Drain-Source	V _D s(on)	ID=400A,VGS=18V	Tj=125°C	_	3.3	_	
Voltage		Tj=150°C			3.8	4.6	
Drain Cutoff Current	IDSS	VDS=1200V,VGS=0V		_	_	4	mA
Souce-Drain Voltage	Vsd		Tj=25°C	_	1.8	2.1	V
		· · · · · · · · · · · · · · · · · · ·	Tj=125°C	_	2.3	_	
			Tj=150°C	_	2.4	3.4	
		VGS=18V,IS=400A	Tj=25°C	_	1.4	_	
			Tj=125°C	_	1.7	_	
			Tj=150°C	_	1.8	_	
Gate-Source Threshold Voltage	Vgs(th)	VDS=10V,ID=85mA	1.6	-	4	V	
Gate-Source	lgss	Vgs=22V,Vps=0V		_	_	0.5	
Leak Current		VGS=-6V,VDS=0V			_	_	μA
Switching Characteristics	td(on)	VGS(on)=18V、VGS(off)=0V VDS=600V ID=400A			60	_	ns
	tr				50	_	
	trr				70	_	
	td (off)	RG(on)=0.2 ohm, RG(off)=0.2 ohm Inductive load		_	240	_	
	tf			_	75	_	
Input Capacitance	Ciss	VDS=10V,VGS=0V,200kHz			38	_	nF
Gate Registance	RGint	Tj=25°C			1.4	_	Ω
NTC Rated Resistance	R ₂₅			_	5.0	_	kΩ
NTC B Value	B50/25			_	3370	_	K
Stray Inductance	Ls			_	10.0	_	nH
Creepage Distance	-	Terminal to heat sink		_	16.7	_	mm
		Terminal to terminal		_	16.7	_	mm
Clearance Distance	-	Terminal to heat sink		_	12.0	_	mm
		Terminal to terminal		_	11.0	_	mm
Junction-to -Case	D4b/: a\	DMOSFET(1/2 module) Note 4)			_	61	°C/kW
Thermal Resistance	permal Resistance Rth(j-c) SBE		D(1/2 module) Note 4)		_	80	
Case-to -heat sink Thermal Resistance	Rth(c-f)	Case to heat sink, per 1 module. The applied. Note 5)	_	15	_	C/KVV	

Note 4) Measurement of Tc is to be done at the point just under the chip.

Note 5) Typical value is measured by using thermally conductive grease of $\lambda=0.9W/(m\cdot K)$.

Note 6) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

<Wavelength for Switching Test>

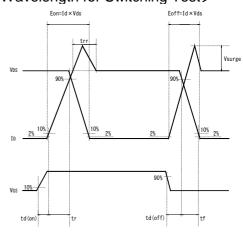


Fig.1 Output characteristic 25°C (TYP) 800 Vgs=16V 700 Vgs=18V 600 Drain current ID (A) Vgs=20V 500 Vgs=14\ 400 300 Vgs=12\ 200 Vgs=10V 100 0 2 0 6 8

(TYP)

6

Tj=150°C

VGS=18V

Tj=25°C

Tj=25°C

Tj=25°C

Tj=25°C

Tj=25°C

Tj=25°C

Fig.2 Drain source voltage characteristic

Fig.3 Drain source voltage characteristic 25°C (TYP)

Drain source voltage VDS (V)

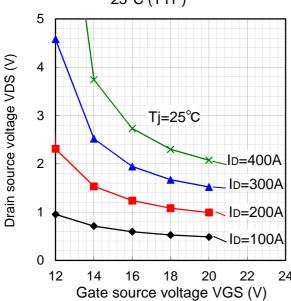


Fig.4 Ron vs Tj characteristic (TYP)

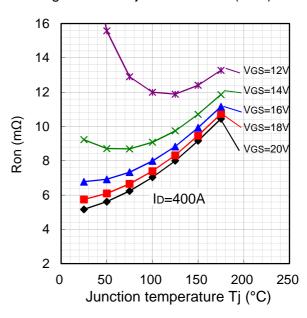


Fig.5 Forward characteristic of Diode (TYP)

1000

VGS=18V

Tj=150°C

Tj=125°C

VGS=0V

Tj=25°C

Tj=25°C

Source drain voltage VSD (V)

Fig.6 Forward characteristic of Diode (TYP) 800 Tj=25°C 700 600 Source current IS (A) VGS=18 500 Tj=150°C 400 300 Tj=125°C 200 100 0 2 5 0 Source drain voltage VSD (V)

Fig.7 Drain Current vs Gate Voltage (TYP)

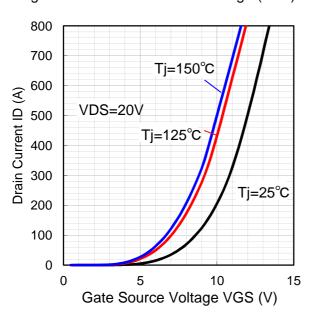
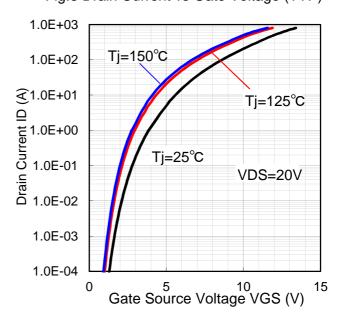


Fig.8 Drain Current vs Gate Voltage (TYP)



0

200

●Electrical characteristic curves (Typical)

Fig.9 Switching time vs drain current at

25°C (TYP)

1000

td(off)

tr

td(on)

tf

VDS=600V RG(on)=0.2Ω

VGS(on)=18V RG(off)=0.2Ω

VGS(off)=0V INDUCTIVE LOAD

125°C (TYP) 1000 td(off) Switching time (ns) 100 - tr td(on) 10 $Rg(on)=0.2\Omega$ VDS=600V $Rg(off)=0.2\Omega$ Vgs(on)=18V INDUCTIVE LOAD Vgs(off)=0V 1 200 400 600 800 1000 0 Drain current ID (A)

Fig.10 Switching time vs drain current at

Fig.11 Switching time vs drain current at 150°C (TYP)

400

600

Drain current ID (A)

800

1000

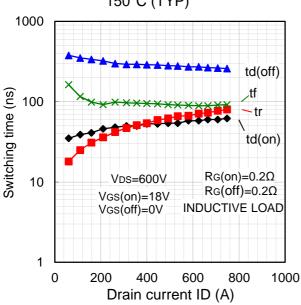


Fig.12 Switching loss vs drain current at 25°C (TYP)

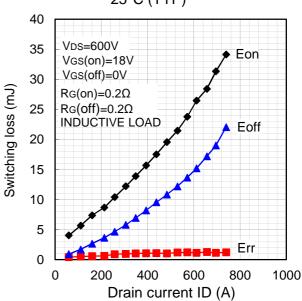


Fig.13 Switching loss vs drain current at 125°C (TYP)

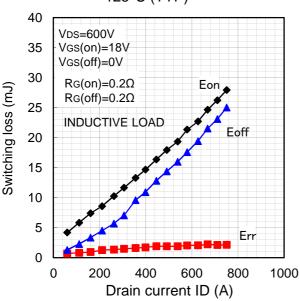


Fig.14 Switching loss vs drain current at 150°C (TYP)

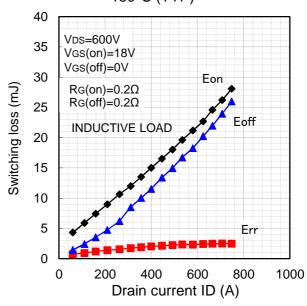


Fig.15 Recovery characteristic vs drain current at 25°C (TYP)

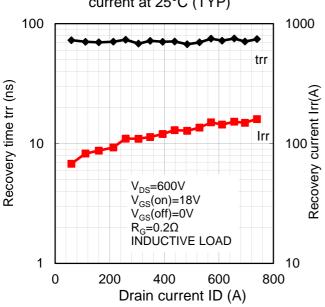


Fig.16 Recovery characteristic vs drain current at 125°C (TYP)

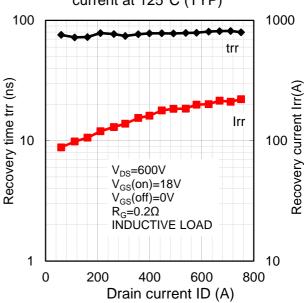


Fig.17 Recovery characteristic vs drain current at 150°C (TYP) 100 1000 Recovery time trr (ns) Recovery current Irr(A) 100 V_{DS}=600V $V_{GS}(on)=18V$ $V_{GS}(off)=0V$ $R_G=0.2\Omega$ INDUCTIVE LOAD 1 10 0 200 400 600 800 Drain current ID (A)

at 25°C (TYP)

10000

V_{DS}=600V
I_D=400A
V_{GS}(on)=18V
V_{GS}(off)=0V
INDUCTIVE LOAD

tt

10
0.1
1
1
10
Gate resistance RG (Ω)

Fig.18 Switching time vs gate resistance

Fig.19 Switching time vs gate resistance at 125°C (TYP)

10000

V_{DS}=600V
I_D=400A
V_{GS}(on)=18V
V_{GS}(off)=0V
INDUCTIVE LOAD

tr

10

Gate resistance RG (Ω)

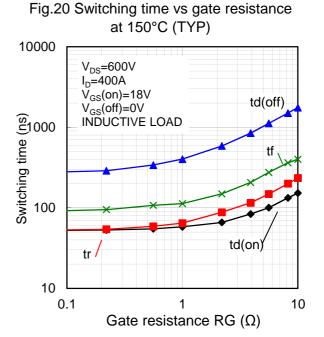




Fig.21 Switching loss vs gate resistance at 25°C (TYP)

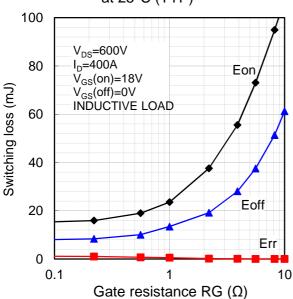


Fig.22 Switching loss vs gate resistance at 125°C (TYP)

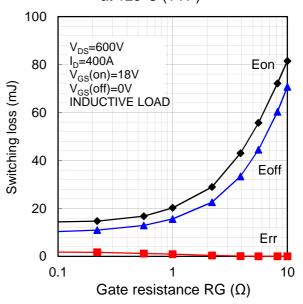
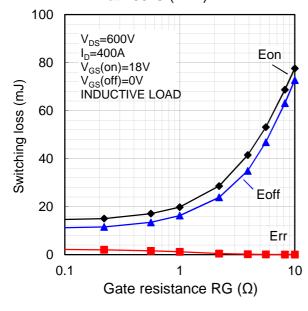
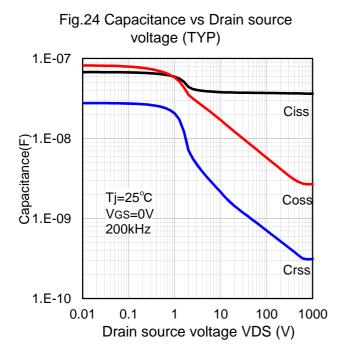


Fig.23 Switching loss vs gate resistance at 150°C (TYP)

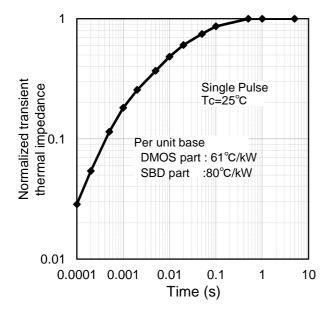




25 Gate source voltage VGS(V) 20 15 10 ID=400A V_{DS} =600VTj=25°C 5 0 500 1000 1500 2000 0 2500 Gate charge QG (nC)

Fig.25 Gate charge characteristic (TYP)

Fig.26 Transient thermal impedance (TYP)



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