

HVD368B

Variable Capacitance Diode for VCO

REJ03G0048-0200 Rev.2.00 Jan 31, 2006

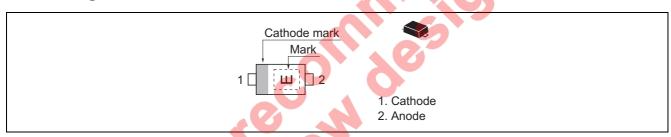
Features

- Narrow Capacitance deviation.
- Low series resistance. ($r_S = 1.1 \Omega \text{ max}$)
- Good C-V linearity.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code	
HVD368B	E	SFP	PUSF0002ZB-A	

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Reverse voltage	V_R	10	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics

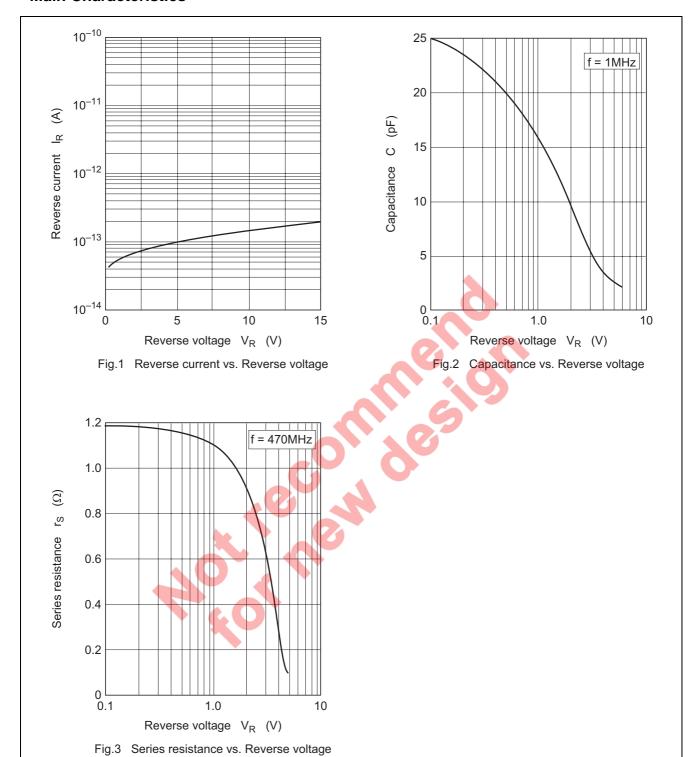
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	_	10	nA	V _R = 10 V
	I _{R2}	_	_	100		V _R = 10 V, Ta = 60°C
Capacitance	C ₁	15.0	_	16.5	pF	V _R = 1 V, f = 1 MHz
	C ₂	9.0	_	10.2		V _R = 2 V, f = 1 MHz
	C ₃	5.0	_	6.0		$V_R = 3 \text{ V, f} = 1 \text{ MHz}$
Capacitance ratio	n	2.2	_	_	_	C ₁ / C ₃
Series resistance	r _S		_	1.1	Ω	V _R = 2 V, f = 470 MHz

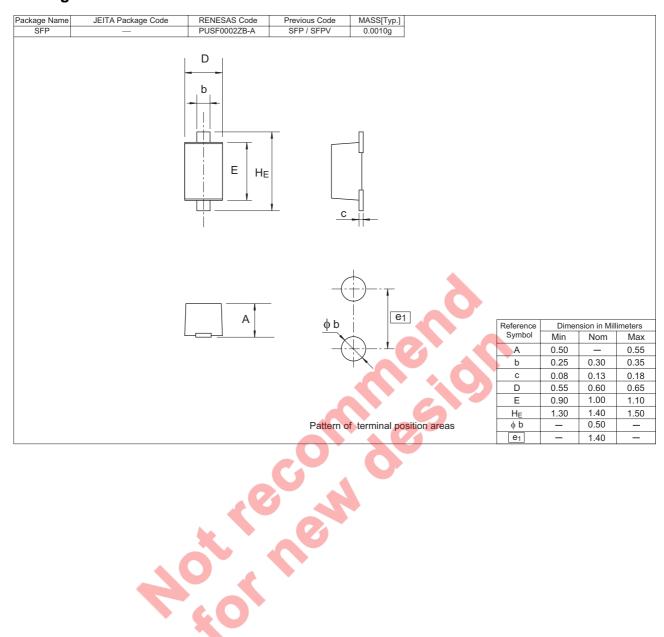
Note: For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



Main Characteristics



Package Dimensions



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April 1st, 2010 Renesas Electronics Corporation

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