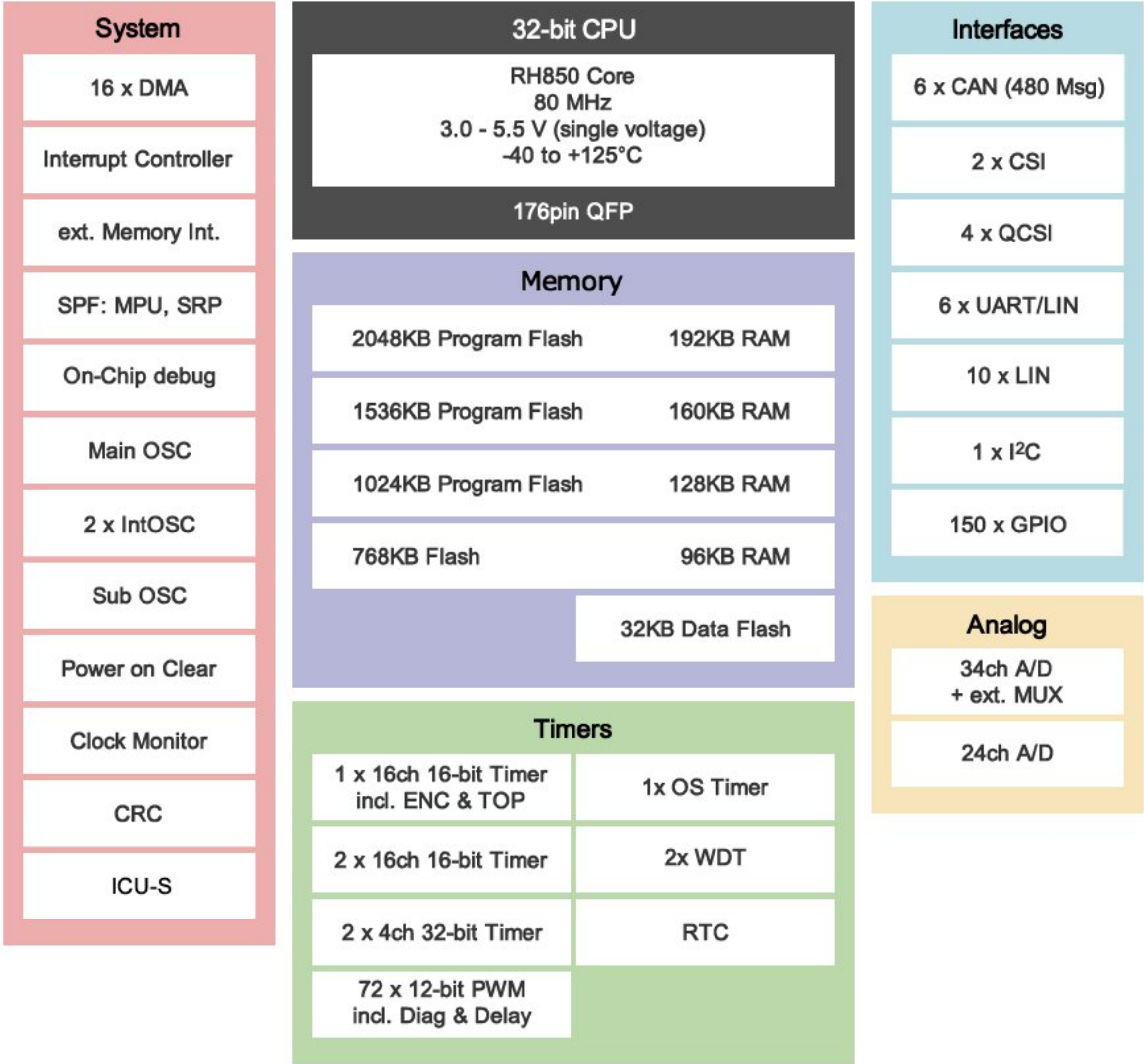


R7F7010134AFP

BLOCK DIAGRAM



PARAMETRICS

Parameters	R7F7010134AFP
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Package	
Pin Count	64
Package Type	LFQFP

Memory	
Program memory	512 KB Flash memory
RAM	64 KB
Data Flash	32 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RH850G3K (32-bit)
Max. Frequency	80 MHz
On-chip Oscillator Freq.	8 MHz, Low Speed: 240 kHz
PLL	YES
Power-On Reset	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 16ch
External Interrupt Pins	8
I/O Ports	49

Timers	
16-bit Timers	16 ch
32-bit Timers	4 ch
Watchdog Timers	2 ch
Other Timers	Timer for OS x 1ch
PWM Output	24

Analog	
A/D Converters	10-bit x 11 ch, 12-bit x 10 ch

Interfaces	
CAN	3 ch
CSIs	1 ch
SPIs	1 ch